

# PESD42VS2UT

# Double ESD protection diode array

January 2019 Product data sheet

### 1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package designed to protect up to two signal lines from the damage caused by ESD and other transients.

### 2. Features and benefits

- Unidirectional ESD protection of two lines
- High reverse standoff voltage: V<sub>RWM</sub> = 42 V
- Low leakage current: I<sub>RM</sub> ≤ 50 nA
- · ESD protection up to 23 kV
- AEC-Q101 qualified

### 3. Applications

· ESD protection for standard capacitance signals or supply lines

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode	'	,				'	
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	42	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1]	-	-	1.8	Α
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 1.8 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[2] [1]	-	80	95	V

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.

### 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	]3	3
2	K2	cathode (diode 2)		
3	A	common anode	SOT23	1 2 006aaa154



### 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PESD42VS2UT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PESD42VS2UT	GK

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode						
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	1.8	А
Per device						
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximum	ratings					
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[2]	-	23	kV

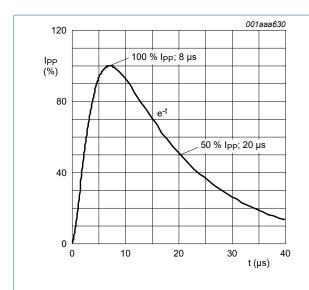
<sup>[1]</sup> Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.

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<sup>[2]</sup> Device stressed with ten non-repetitive ESD pulses.

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8/20 µs pulse waveform according to IEC 61000-4-5

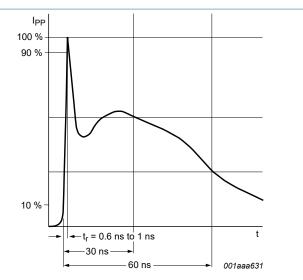


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode	,						<u>'</u>
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	42	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 2 mA; T <sub>amb</sub> = 25 °C		46.1	47	47.9	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 42 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	17	20	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[1] [2]	-	60	-	V
		$I_{PPM}$ = 1.8 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1] [2]	-	80	95	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C	[1] [3]	-	167	-	V

Measured from pin 1 or 2 to pin 3.

Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.

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### **Double ESD protection diode array**

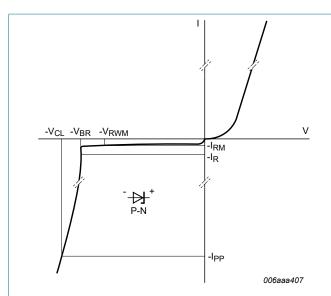
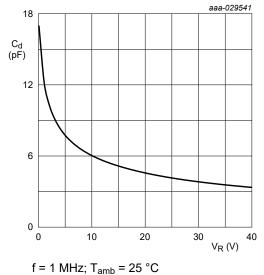


Fig. 3. V-I characteristics for a unidirectional ESD protection diode



1 - 1 Wil 12, 1 amb - 23 C

Fig. 4. Diode capacitance as a function of reverse voltage; typical values

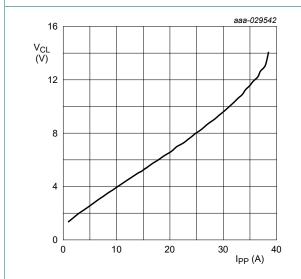


Fig. 5. Positive clamping voltage (8/20 μs pulse); typical values

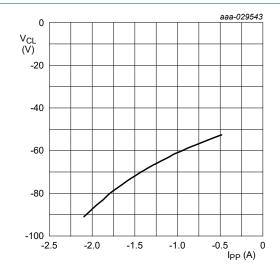
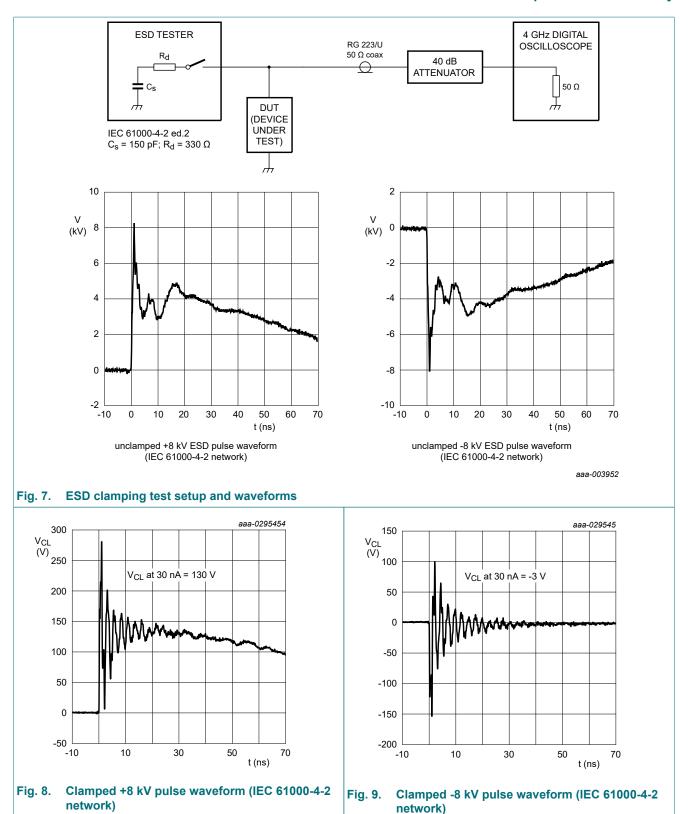
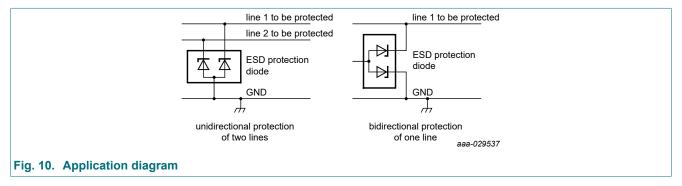


Fig. 6. Negative clamping voltage (8/20 µs pulse); typical values



### 10. Application information

The device is designed for the protection of up to two unidirectional data or signal lines from the damage caused by ESD and surge pulses. The devices may be used on lines where the signal polarities are either positive or negative with respect to ground.

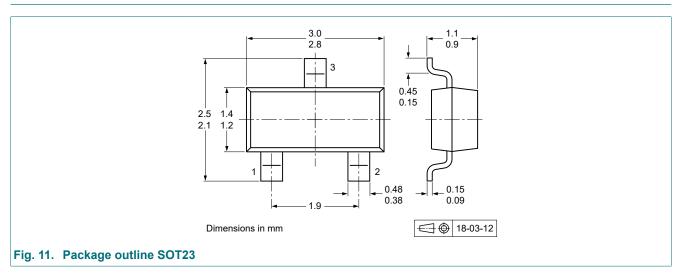


#### Circuit board layout and protection device placement

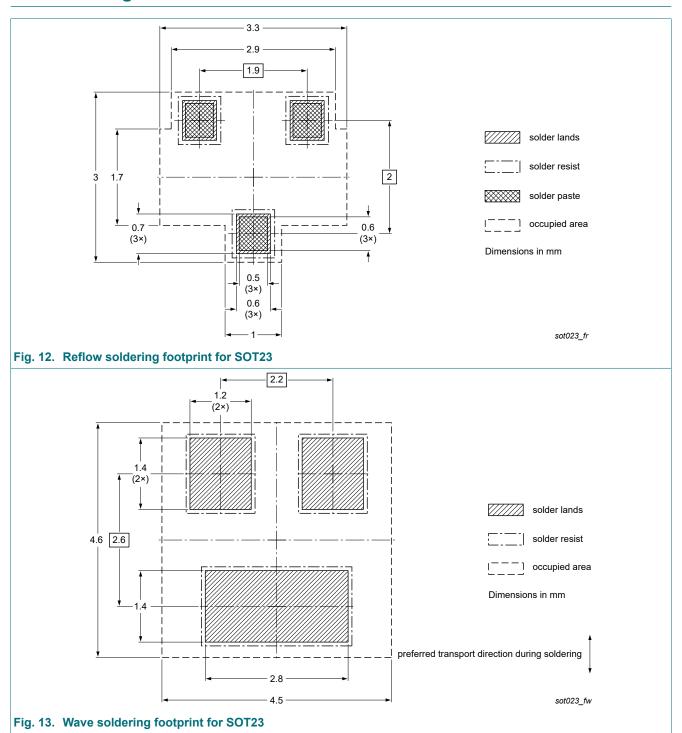
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### 11. Package outline



### 12. Soldering



## 13. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD42VS2UT_v.2	20190124	Product data sheet	-	PESD42VS2UT_v.1			
Modifications:	Product status chang	ged					
PESD42VS2UT_v.1	20190117	Objective data sheet	-	-			

### 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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