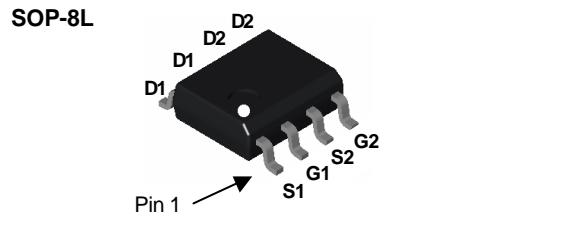
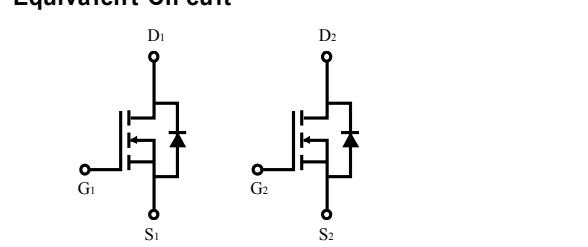
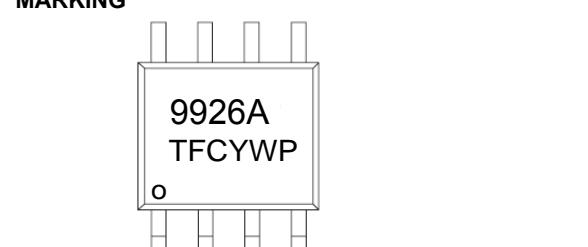


SOP-8 Plastic-Encapsulate MOSFETS

9926A

N-Channel Enhancement Mode Power MOSFET

Description												
<p>The 9926A uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge . The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.</p>	Equivalent Circuit											
General Features												
<table border="1" data-bbox="219 765 716 1012"> <thead> <tr> <th colspan="3">PRODUCT SUMMARY</th> </tr> <tr> <th>V_{DSS}</th> <th>I_D</th> <th>R_{DS(on)} (mΩ) Max</th> </tr> </thead> <tbody> <tr> <td rowspan="2">20V</td> <td>6.0A</td> <td>25 @ V_{GS} = 4.5V</td> </tr> <tr> <td>5.2A</td> <td>32 @ V_{GS} = 2.5V</td> </tr> </tbody> </table>	PRODUCT SUMMARY			V _{DSS}	I _D	R _{DS(on)} (mΩ) Max	20V	6.0A	25 @ V _{GS} = 4.5V	5.2A	32 @ V _{GS} = 2.5V	MARKING
PRODUCT SUMMARY												
V _{DSS}	I _D	R _{DS(on)} (mΩ) Max										
20V	6.0A	25 @ V _{GS} = 4.5V										
	5.2A	32 @ V _{GS} = 2.5V										
<ul style="list-style-type: none"> ● High power and current handing capability ● Lead free product is acquired ● Surface mount package 	 Y :year code W :week code											

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted				
Parameter	Symbol	Maximum		Units
Drain-Source Voltage	V _{DS}	20		V
Gate-Source Voltage	V _{GS}	±12		V
Continuous Drain Current ^A	I _D	6.0		A
Pulsed Drain Current ^B	I _{DM}	30		
Power Dissipation ^A	P _D	2		W
		1.2		
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150		°C

Thermal Characteristics				
Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	R _{θJA}	t ≤ 10s	48	62.5 °C/W
Maximum Junction-to-Ambient ^A		Steady-State	74	110 °C/W
Maximum Junction-to-Lead ^C	R _{θJL}	35	40	°C/W



SHENZHEN TUOFENG SEMICONDUCTOR TECHNOLOGY CO.,LTD

SOP-8 Plastic-Encapsulate MOSFETs

9926A

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS}=0\text{V}$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$			500	nA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D = 250\mu\text{A}$	0.5	0.65	1.0	V
$I_{\text{D(ON)}}$	On state drain current	$V_{GS}=4.5\text{V}, V_{DS}=5\text{V}$	20			A
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}, I_D = 6.0\text{A}$		20	25	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D = 5.2\text{A}$		26	32	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=15\text{V}, I_D = 6.0\text{A}$		22		S
V_{SD}	Diode Forward Voltage	$I_S = 3\text{A}, V_{GS}=0\text{V}$		0.7	1.2	V
I_S	Maximum Body-Diode Continuous Current				3	A

DYNAMIC PARAMETERS

C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$		931		pF
C_{oss}	Output Capacitance			60		pF
C_{rss}	Reverse Transfer Capacitance			50		pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$			9.5	Ω

SWITCHING PARAMETERS

$Q_g(4.5\text{V})$	Total Gate Charge (4.50V)	$V_{DD}=15\text{V}, V_{\text{GEN}}=4.5\text{V}, I_D=6\text{A}$		13		nC
$Q_g(2.5\text{V})$	Total Gate Charge (2.5V)			11		nC
Q_{gs}	Gate Source Charge			3.2		nC
Q_{gd}	Gate Drain Charge			3.5		nC
$t_{D(\text{on})}$	Turn-On Delay Time	$V_{DD}=15\text{V}, V_{\text{GEN}}=4.5\text{V}, R_L=15\Omega$ $R_{\text{GEN}}=6\Omega$		24		ns
t_r	Turn-On Rise Time			40		ns
$t_{D(\text{off})}$	Turn-Off Delay Time			50		ns
t_f	Turn-Off Fall Time			20		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		23.5		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		13.4		nC

A: The value of R_{JJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The R_{JJA} is the sum of the thermal impedance from junction to lead R_{JL} and lead to ambient.

D. The static characteristics in Figures 1 to 6, 12, 14 are obtained using 80μs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

SOP-8 Plastic-Encapsulate MOSFETs

9926A

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

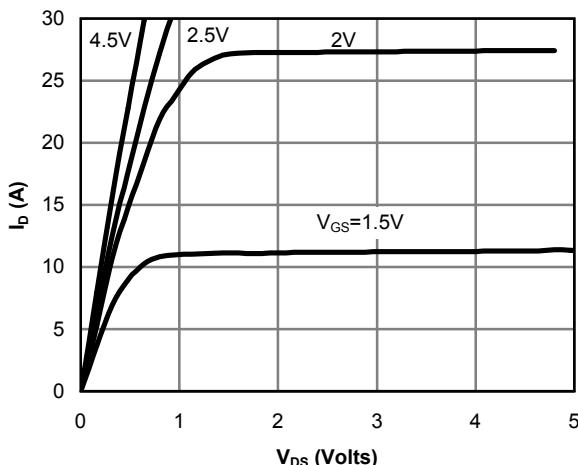


Fig 1: On-Region Characteristics

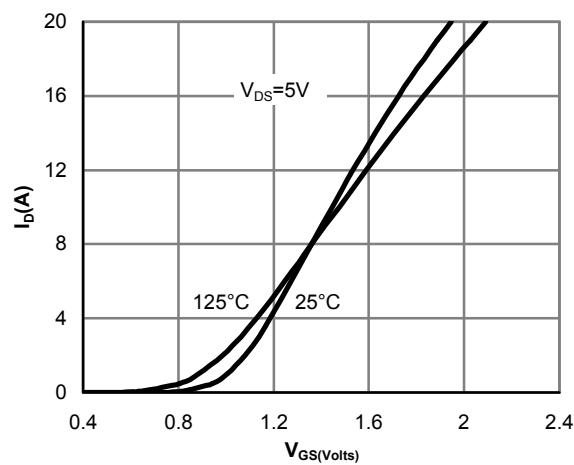


Figure 2: Transfer Characteristics

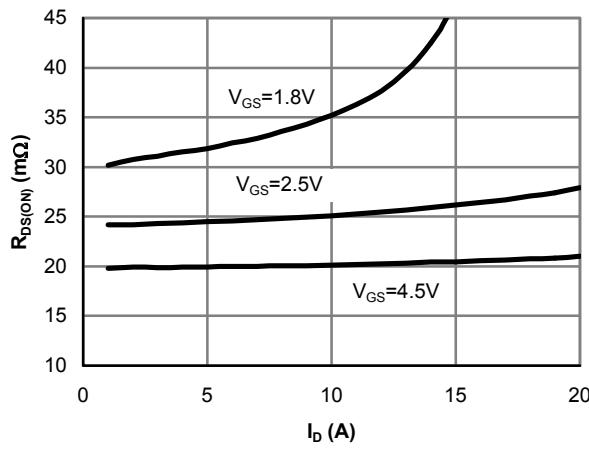


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

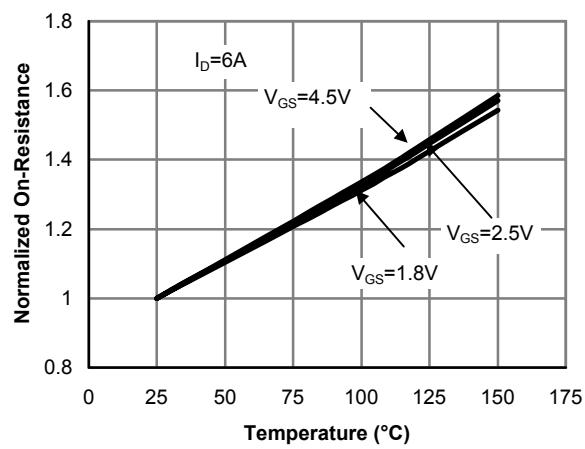


Figure 4: On-Resistance vs. Junction Temperature

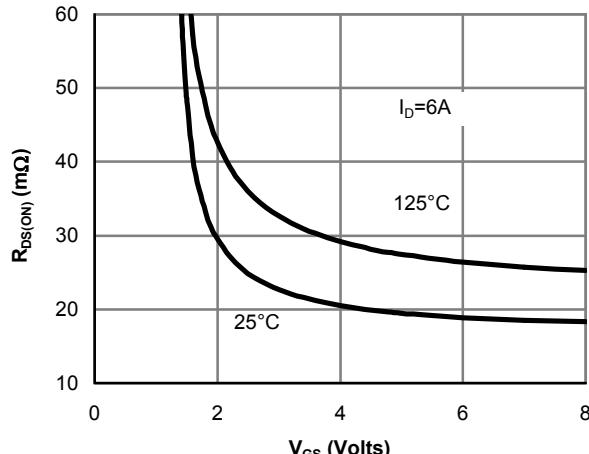


Figure 5: On-Resistance vs. Gate-Source Voltage

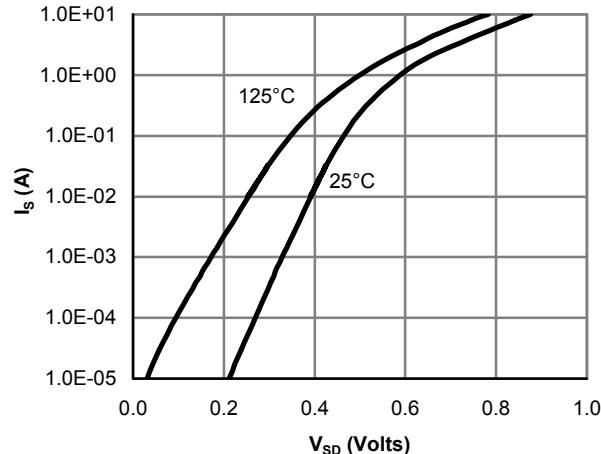
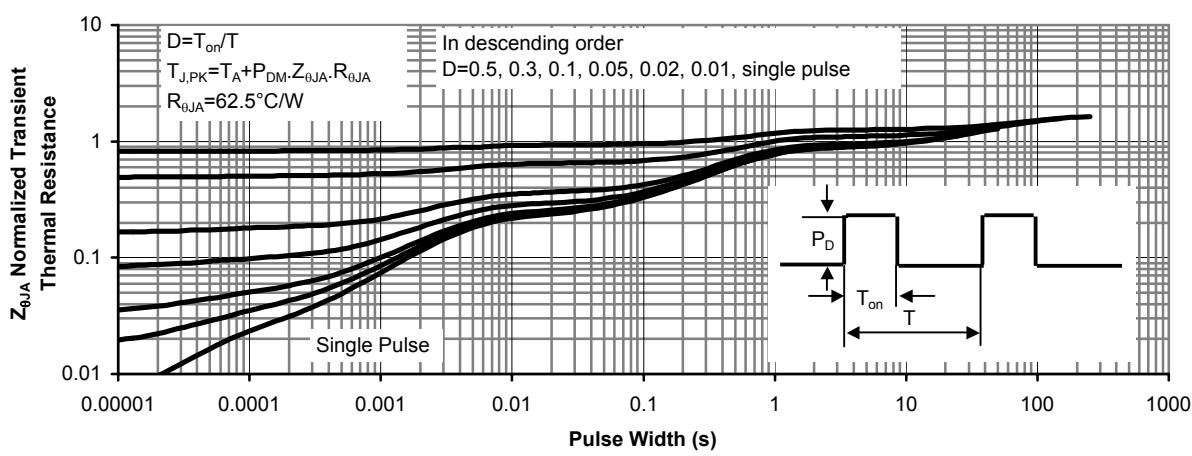
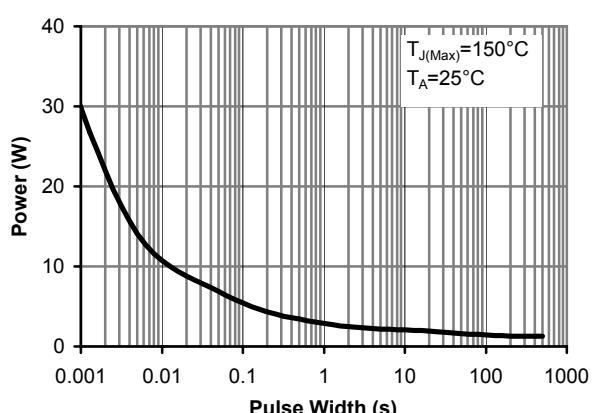
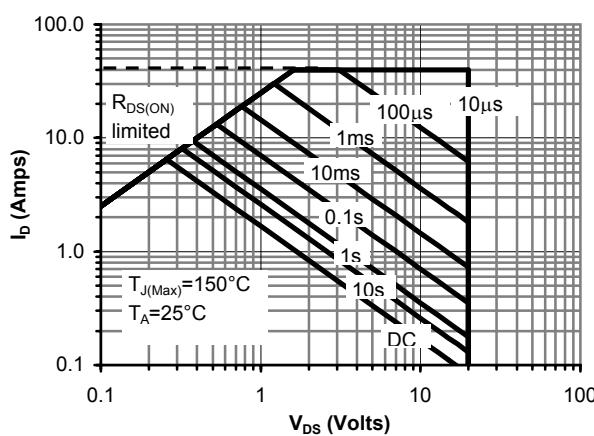
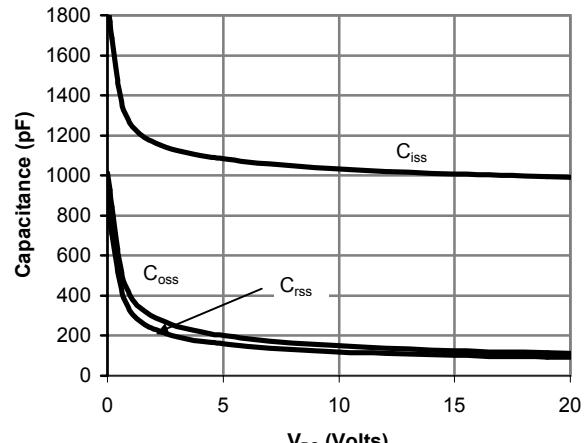
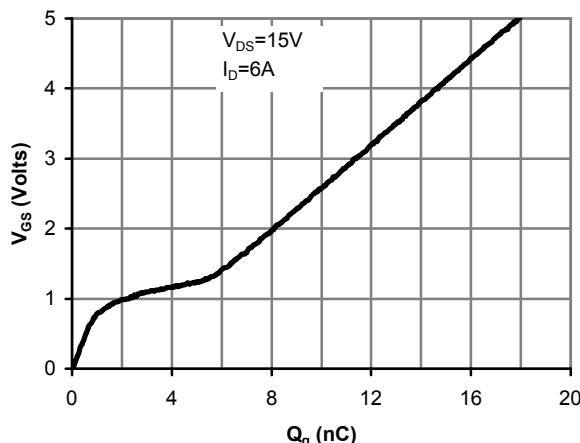


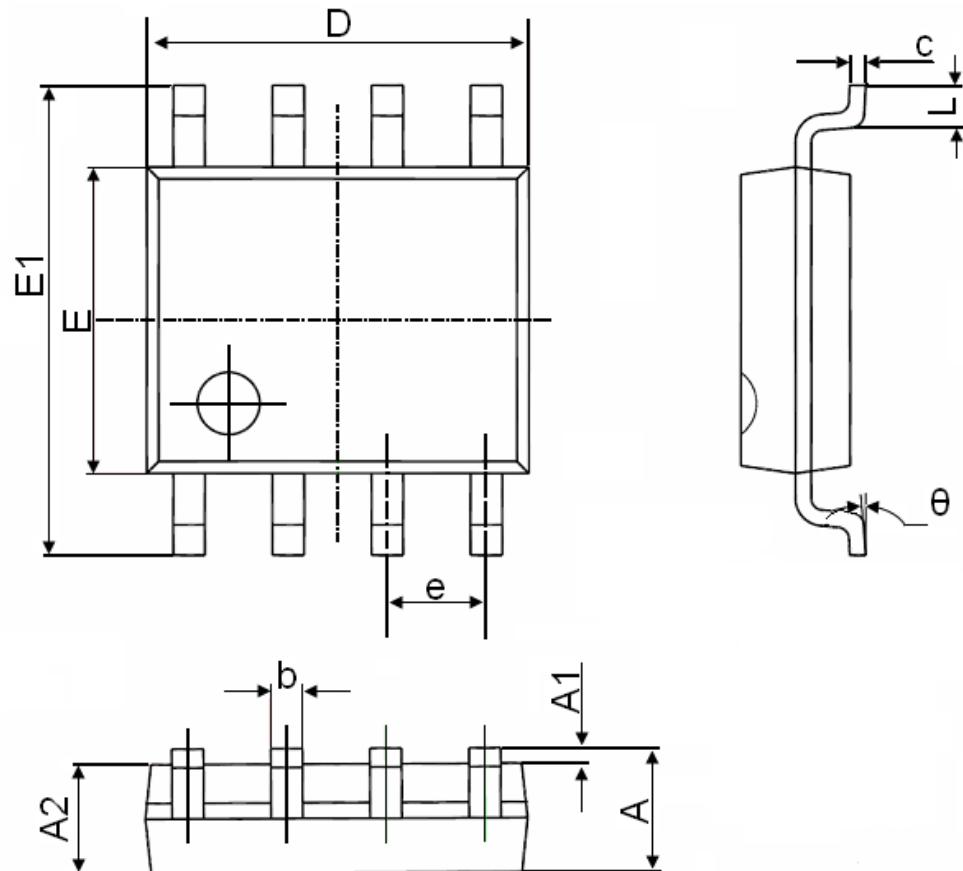
Figure 6: Body-Diode Characteristics

SOP-8 Plastic-Encapsulate MOSFETs

9926A

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS:



SOP-8 Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°
C				