1. General description

NPN/NPN general-purpose double transistors in a leadless ultra small DFN1412-6 (SOT1268) Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: BC807RA NPN/PNP complement: BC817RAPN

2. Features and benefits

- · Reduces component count
- Reduces pick and place costs
- Low package height of 0.5 mm
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- · Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor			·				
V _{CEO}	collector-emitter voltage	open base		-	-	45	V
I _C	collector current			-	-	500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	1	Α
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 100 mA; T _{amb} = 25 °C		160	-	400	
		V _{CE} = 1 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-	

[1] Pulse test: $t_p \le 300 \mu s$; $\delta \le 0.02$



45 V, 500 mA NPN/NPN general-purpose double transistors

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	7 6	
3	C2	collector TR2	2 5	(TR1) TR2)
4	E2	emitter TR2		
5	B2	base TR2	3 8 4	E1 B1 C2
6	C1	collector TR1		sym020
7	C1	collector TR1	Transparent top view	
8	C2	collector TR2	DFN1412-6 (SOT1268)	

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC817RA		plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals; body: 1.4 mm x 1.2 mm x 0.47 mm	SOT1268

7. Marking

Table 4. Marking codes

Type number	Marking code
BC817RA	A7

45 V, 500 mA NPN/NPN general-purpose double transistors

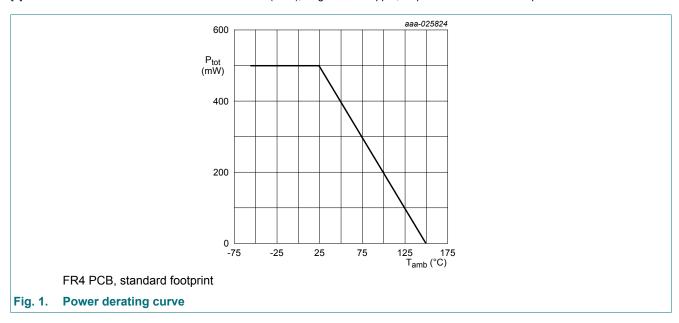
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or		,	'		
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	1	Α
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	350	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	500	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin plated and standard footprint.



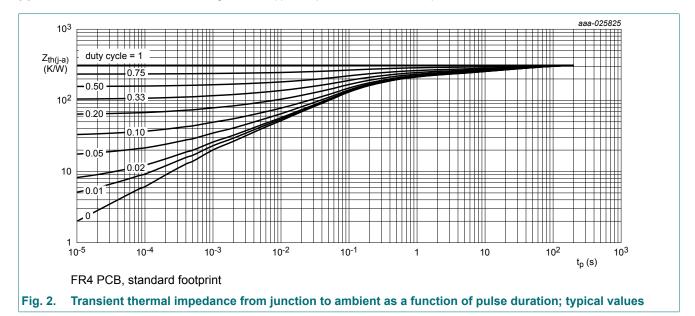
45 V, 500 mA NPN/NPN general-purpose double transistors

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	358	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



45 V, 500 mA NPN/NPN general-purpose double transistors

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor						
I _{CBO}	collector-base cut-off	V _{CB} = 20 V; I _E = 0 A; T _{amb} = 25 °C			-	100	nA
	current	V _{CB} = 20 V; I _E = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 100 mA; T _{amb} = 25 °C		160	-	400	
		V_{CE} = 1 V; I_{C} = 500 mA; T_{amb} = 25 °C	[1]	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	-	-	700	mV
V_{BE}	base-emitter voltage	V _{CE} = 1 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	-	-	1.2	V
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$		-	3	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C		100	-	-	MHz

[1] Pulse test: $t_p \le 300 \mu s$; $\delta \le 0.02$

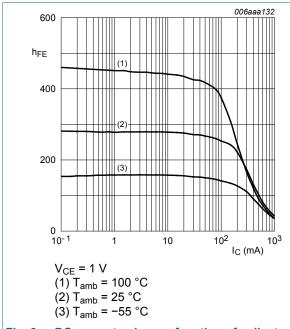


Fig. 3. DC current gain as a function of collector current; typical values

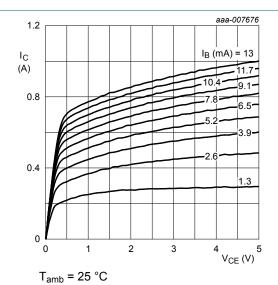


Fig. 4. Collector current as a function of collectoremitter voltage; typical values

45 V, 500 mA NPN/NPN general-purpose double transistors

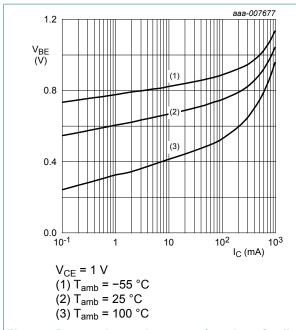


Fig. 5. Base-emitter voltage as a function of collector current; typical values

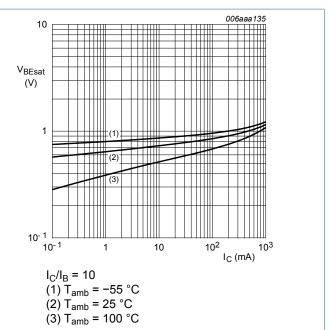
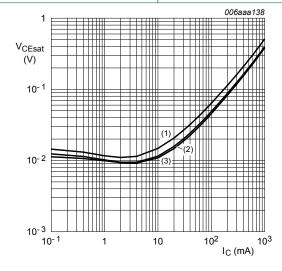


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values



 $I_{C}/I_{B} = 10$ (1) $T_{amb} = -55 \,^{\circ}C$

(2) $T_{amb} = 25 \,^{\circ}C$ (3) $T_{amb} = 100 \,^{\circ}C$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information

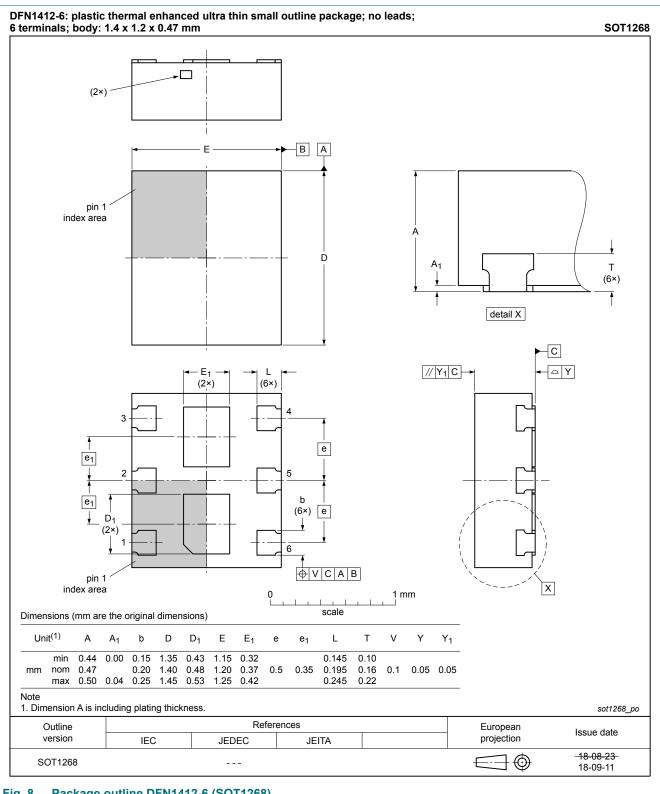
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

BC817RA

45 V, 500 mA NPN/NPN general-purpose double transistors

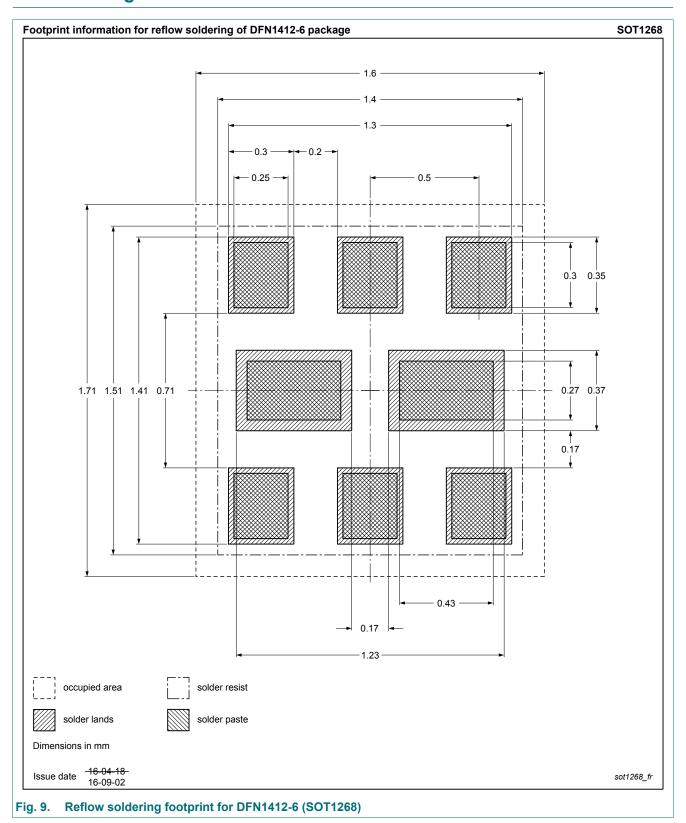
12. Package outline



Package outline DFN1412-6 (SOT1268) Fig. 8.

45 V, 500 mA NPN/NPN general-purpose double transistors

13. Soldering



45 V, 500 mA NPN/NPN general-purpose double transistors

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC817RA v.2	20180914	Product data sheet	-	BC817RA v.1			
Modifications:	Package outli	Package outline drawing updated: Unit T added					
BC817RA v.1	20170620	Product data sheet	-	-			

45 V, 500 mA NPN/NPN general-purpose double transistors

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BC817RA

45 V, 500 mA NPN/NPN general-purpose double transistors

Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	. 3
9.	Thermal characteristics	. 4
10	. Characteristics	5
11.	Test information	6
12	Package outline	7
13	Soldering	8
	Revision history	
	Legal information	

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