





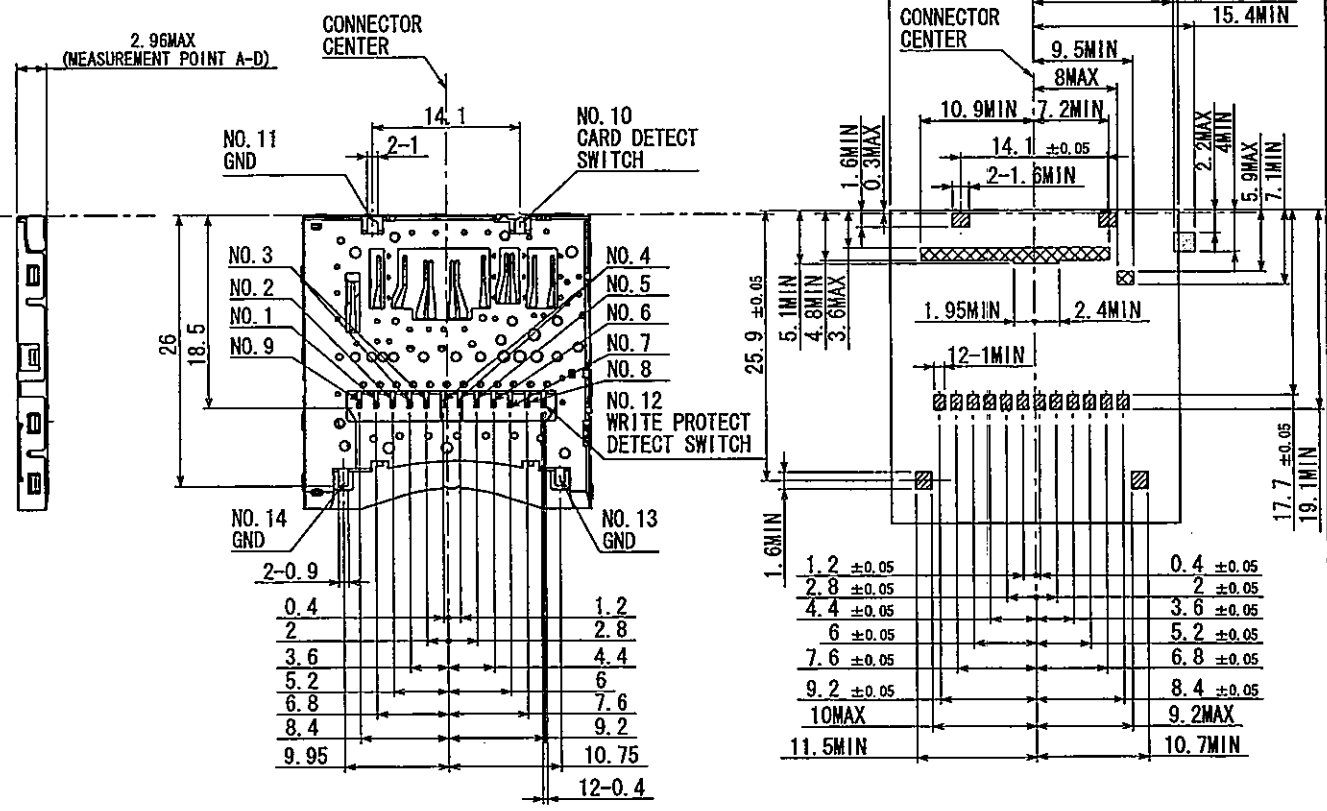
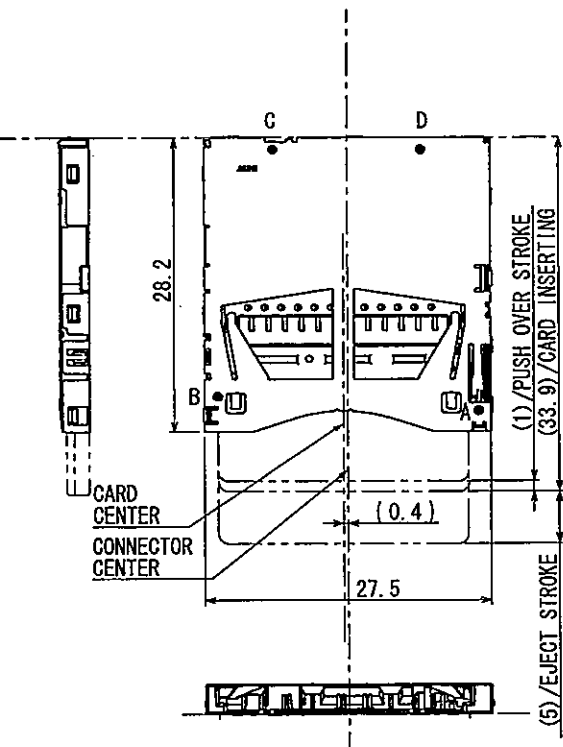
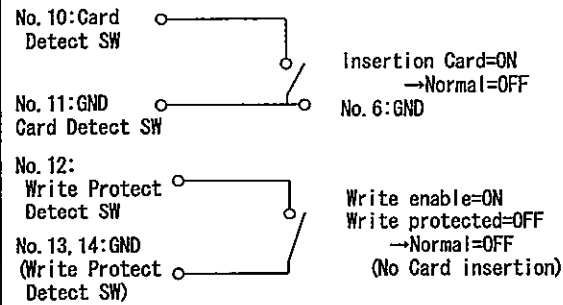
BACK GROUND
SCDA9A0200

PRELIMINARY

Recommended P.C.B layout
(Mounting face side)

-  LAND
-  NO PART AREA
-  NO PATTERN AREA
-  NO PATTERN EXPOSE AREA

Circuit Diagram for Detect SW



NOTES 1) ALL TERMINALS COPLANARITY:0.1MAX. (INITIAL)
 2) THE HEIGHT OF THE PRODUCT IS MEASURED WITH DIGITAL HEIGHT GAUGE.

TOLERANCES UNLESS OTHERWISE SPEC		SYMB	DATE	APPD	CHKD	DSGD
BASIC DIMENSIONS	TOLERANCES					
UP TO 10	±0.3					
ABOVE 10 TO 100	±0.5					
ABOVE 100	±0.8					
ANGULAR DIMENSION	±5°					

ALPS ELECTRIC CO., LTD.

DSGD	Sep. 26, 2009 N. ISHIKAWA	SCALE	NO.
CHKD	Sep. 26, 2009 H. Sasaki	TITLE	PRODUCT DRAWING (1/2)
APPD	Sep. 26, 2009 Y. Kuroda	UNIT	DOCUMENT NO.
		mm	SCDA9A0200, AE12, 003

