RS2A - RS2M

Taiwan Semiconductor

2A, 50V - 1000V Surface Mount Fast Recovery Rectifiers

FEATURES

- Low power loss, high efficiency
- Ideal for automated placement
- Glass passivated junction chip
- Fast switching for high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- Converter

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Moisture sensitivity level: level 1, per J-STD-020
- Packing code with suffix "G" means green compound (halogen-free)
- Part no. with suffix "H" means AEC-Q101 qualified
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.09 g (approximately)

KEY PARAMETERS					
PARAMETER	RAMETER VALUE UNIT				
I _{F(AV)}	2	А			
V _{RRM}	50 - 1000	V			
I _{FSM}	50 A				
T _{J MAX}	150 °C				
Package	DO-214AA (SMB)				
Configuration	Single Die	;			



DO-214AA (SMB)

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)									
PARAMETER	SYMBOL	RS2A	RS2B	RS2D	RS2G	RS2J	RS2K	RS2M	UNIT
Marking code on the device		RS2A	RS2B	RS2D	RS2G	RS2J	RS2K	RS2M	
Repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Forward current	I _{F(AV)}				2				А
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load per diode	I _{FSM}	50		A					
Junction temperature	TJ			- {	55 to +1	50			°C
Storage temperature	T _{STG}	- 55 to +150		°C					





THERMAL PERFORMANCE					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient Thermal Resistance	R _{eja}	55	°C/W		
Junction-to-lead thermal resistance	R _{eJL}	18	°C/W		

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage per diode (1)		$I_{F} = 2A, T_{J} = 25^{\circ}C$	V _F	-	1.3	V
Reverse current @ rated V _R	a and a (2)	$T_J = 25^{\circ}C$		-	5	μA
Reverse current @ fated v _R		T _J = 125°C	I _R	-	50	μA
Junction capacitance		1 MHz, V _R =4.0V	CJ	50	-	pF
	RS2A	-		-	150	ns
	RS2B					ns
	RS2D					ns
Reverse recovery time	RS2G	I _F =0.5A ,I _R =1.0A I _{RR} =0.25A	t _{rr}			ns
	RS2J			-	250	ns
	RS2K			_	500	ns
	RS2M				500	ns

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms

ORDERING INFORMATION							
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX(*)	PACKAGE	PACKING		
	н	R5		SMB	850 / 7" Plastic reel		
RS2x (Note 1)		R4	G	SMB	3,000 / 13" Paper reel		
		M4		SMB	3,000 / 13" Plastic reel		

Note:

1. "x" defines voltage from 50V (RS2A) to 1000V (RS2M)

*: Optional available

EXAMPLE P/N						
EXAMPLE P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
RS2JHR5G	RS2J	Н	R5	G	AEC-Q101 qualified Green compound	



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

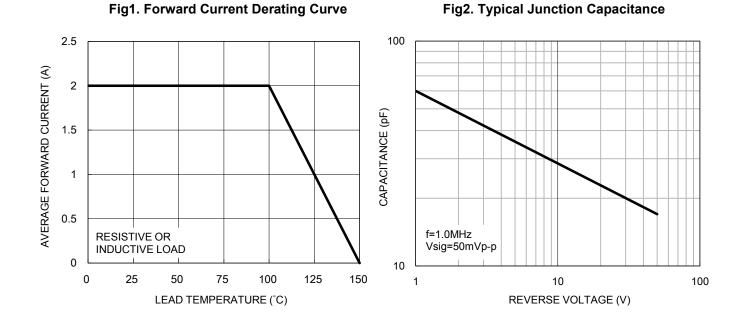


Fig3. Typical Reverse Characteristics

T_J=125[°]C

T_J=25[°]C

120

100

100

10

1

0.1

0

20

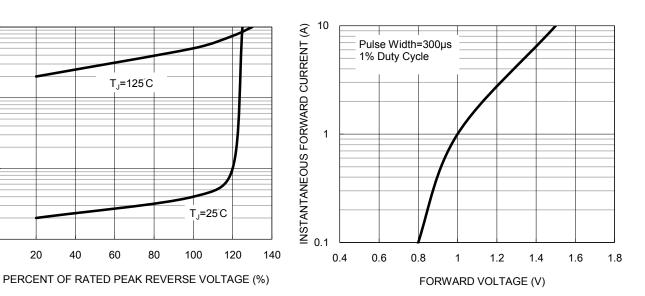
40

60

80

INSTANTANEOUS REVERSE CURRENT (µA)







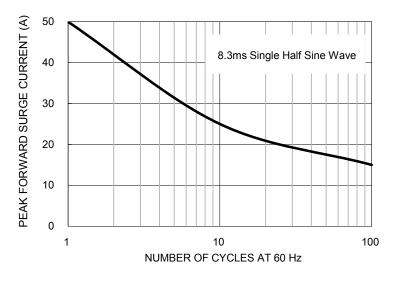
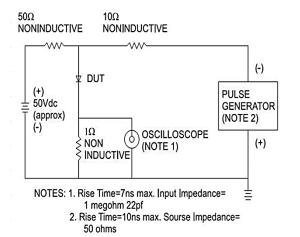
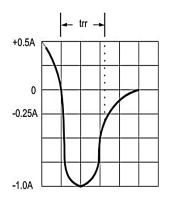


Fig5. Maximum Non-repetitive Forward Surge Current

Fig6. Reverse Recovery Time Characteristic And Test Circuit Diagram



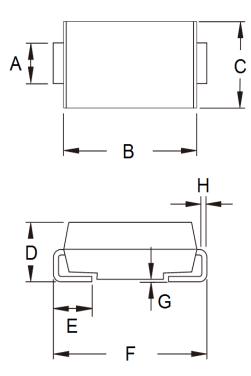




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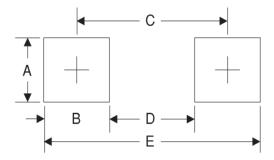
PACKAGE OUTLINE DIMENSIONS

DO-214AA (SMB)



DIM.	Unit (mm) Min Max		Unit (inch)	
Dilvi.			Min	Max
А	1.95	2.20	0.077	0.087
В	4.05	4.60	0.159	0.181
С	3.30	3.95	0.130	0.156
D	1.95	2.65	0.077	0.104
E	0.75	1.60	0.030	0.063
F	5.10	5.60	0.201	0.220
G	0.05	0.20	0.002	0.008
Н	0.15	0.31	0.006	0.012

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.3	0.091
В	2.5	0.098
С	4.3	0.169
D	1.8	0.071
E	6.8	0.268

MARKING DIAGRAM



P/N

Marking CodeGreen Compound G

YW = Date Code

F = Factory Code



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