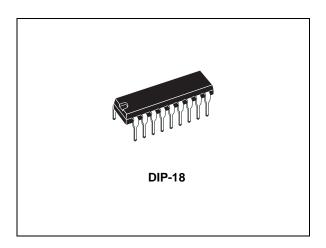


ULN2801A, ULN2802A, ULN2803A, ULN2804A

Eight Darlington arrays

Datasheet - production data



Features

- Eight Darlington transistors with common emitters
- Output current to 500 mA
- Output voltage to 50 V
- Integral suppression diodes
- · Versions for all popular logic families
- · Output can be paralleled
- Inputs pinned opposite outputs to simplify board layout

Description

The ULN2801A, ULN2802A, ULN2803A and ULN2804A each contain eight Darlington transistors with common emitters and integral suppression diodes for inductive loads. Each Darlington features a peak load current rating of 600 mA (500 mA continuous) and can withstand at least 50 V in the OFF state. Outputs may be paralleled for higher current capability.

Four versions are available to simplify interfacing to standard logic families: the ULN2801A is designed for general purpose applications with a current limit resistor; the ULN2802A has a 10.5 k Ω input resistor and Zener for 14-25 V PMOS; the ULN2803A has a 2.7 k Ω input resistor for 5 V TTL and CMOS; the ULN2804A has a 10.5 k Ω input resistor for 6-15 V CMOS.

All types are supplied in an 18-lead plastic DIP with a copper lead form and feature the convenient input-opposite-output pinout to simplify board layout.

Table 1. Device summary

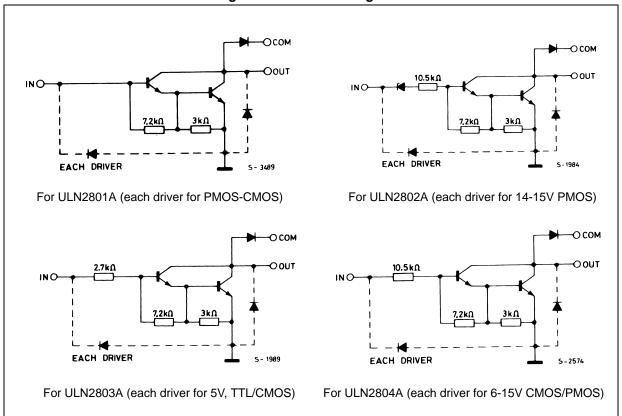
Order codes	Package
ULN2801A	
ULN2802A	DIP-18
ULN2803A	DIF-10
ULN2804A	

Contents

1	Diagram 3
2	Pin configuration
3	Maximum ratings
4	Electrical characteristics 6
5	Test circuits
6	Typical performance characteristics
7	Package mechanical data
8	Revision history

1 Diagram

Figure 1. Schematic diagrams



2 Pin configuration

18 OUT 1 IN 1 IN 2 OUT 2 IN 3 16 OUT 3 15 OUT 4 IN 4 IN 5 OUT 5 13 OUT 6 IN 6 IN 7 OUT 7 IN 8 OUT 8 10 COMMON FREE WHEELING DIODES GND 5-3490/1

Figure 2. Pin connections (top view)

3 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
Vo	Output voltage	50	V	
VI	Input voltage (for ULN2802A - ULN2803A - ULN2804A)	30	V	
I _C	I _C Continuous collector current		mA	
I _B	Continuous base current	25	mA	
D	Power Dissipation (one Darlington pair)	1	W	
P _{TOT}	Power Dissipation (total package)	2.25		
T _A Operating ambient temperature range		- 20 to 85	°C	
T _{STG}	Storage temperature range	- 55 to 150	°C	
TJ	Junction temperature	-20 to 150	°C	

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R_{thJA}	Thermal resistance junction-ambient	55	°C/W

4 Electrical characteristics

 $T_A = 25$ °C unless otherwise specified.

Table 4. Electrical characteristics

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
		V _{CE} = 50V				
I _{CEX}	Output leakage current	T _A = 70 °C, V _{CE} = 50 V (<i>Figure 3</i>)			50	
		$T_A = 70$ °C for ULN2802A, $V_{CE} = 50$ V, $V_I = 6$ V (<i>Figure 4</i>)			100	μΑ
		T_A = 70°C for ULN2804A, V_{CE} = 50 V, V_I = 1 V (<i>Figure 4</i>)			500	
		I _C = 100 mA, I _B = 250 μA		0.9	1.1	V
$V_{CE(SAT)}$	Collector-emitter saturation voltage (<i>Figure 5</i>)	I _C = 200 mA, I _B = 350 μA		1.1	1.3	
	Voltage (Figure 6)	I _C = 350 mA, I _B = 500 μA		1.3	1.6	
		for ULN2802A, V _I = 17 V		0.82	1.25	
	Input ourrent (Figure 6)	for ULN2803A, V _I = 3.85 V		0.93	1.35	mA
I _{I(ON)}	Input current (Figure 6)	for ULN2804A, V _I = 5 V		0.35	0.5	
		V _I = 12 V		1	1.45	
I _{I(OFF)}	Input current (Figure 7)	T _A = 70 °C, I _C = 500 μA	50	65		μΑ
V _{I(ON)}	Input voltage (Figure 8)	$\begin{split} &V_{CE} = 2\text{V, for ULN2802A} \\ &I_{C} = 300 \text{ mA} \\ &\text{for ULN2803A} \\ &I_{C} = 200 \text{ mA} \\ &I_{C} = 250 \text{ mA} \\ &I_{C} = 300 \text{ mA} \\ &\text{for ULN2804A} \\ &I_{C} = 125 \text{ mA} \\ &I_{C} = 200 \text{ mA} \\ &I_{C} = 275 \text{ mA} \\ &I_{C} = 350 \text{ mA} \end{split}$			13 2.4 2.7 3 5 6 7 8	>
h _{FE}	DC Forward current gain (Figure 5)	for ULN2801A, V_{CE} = 2 V, I_{C} = 350 mA	1000			
CI	Input capacitance			15	25	рF
t _{PLH}	Turn-on delay time	0.5 V _I to 0.5V _O		0.25	1	μs
t _{PHL}	Turn-off delay time	0.5 V _I to 0.5V _O		0.25	1	μs
I _R	Clamp diode leakage current (Figure 9)	V _R = 50 V			50	μΑ
		$T_A = 70 ^{\circ}\text{C}, V_R = 50 \text{V}$			100	
V_{F}	Clamp diode forward voltage (Figure 10)	I _F = 350 mA		1.7	2	V

5 Test circuits

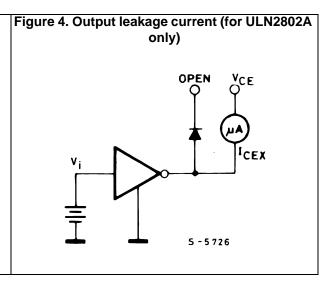


Figure 5. Collector-emitter saturation voltage

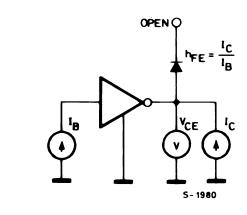


Figure 6. Input current (ON)

OPEN

OPEN

OPEN

S-1986

Figure 7. Input current (OFF)

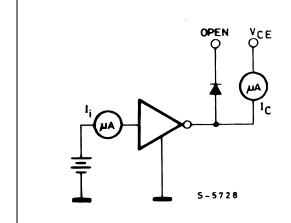


Figure 8. Input voltage

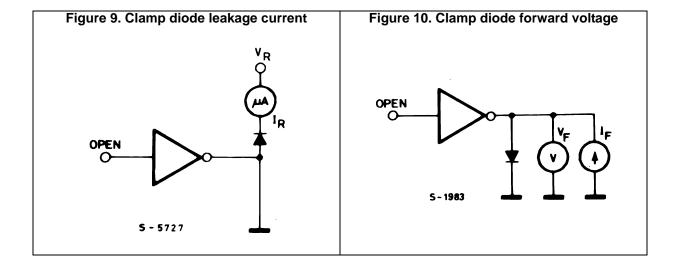
OPEN

V_i = V

V_{CE} I_C

V 4

5/



6 Typical performance characteristics

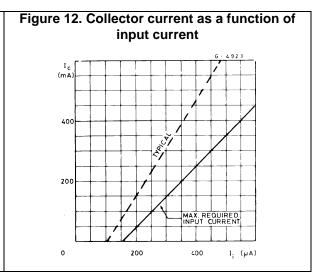


Figure 13. Allowable average power dissipation as a function of T_A

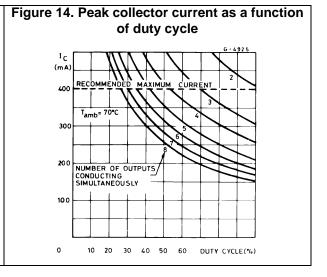


Figure 15. Peak collector current as a function of duty cycle

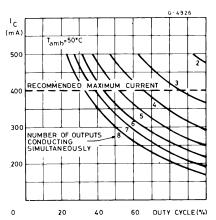


Figure 16. Input current as a function of input voltage (for ULN2802A)

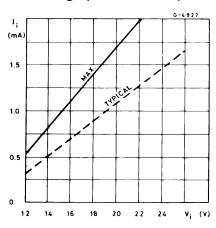


Figure 17. Input current as a function of input voltage (for ULN2804A)

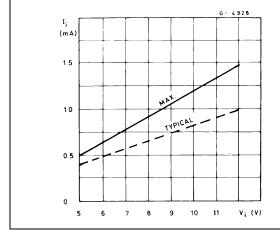
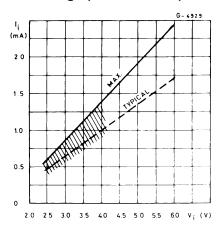


Figure 18. Input current as a function of input voltage (for ULN2803A)



DocID1536 Rev 4

10/14

7 Package mechanical data

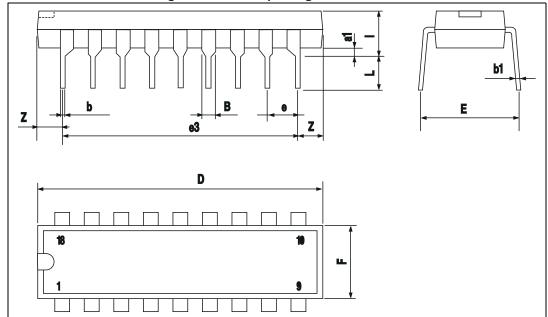
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.



Table 5. DIP-18 mechanical data

Dim.	mm.				
	Min.	Тур.	Max.		
a1	0.254				
В	1.39		1.65		
b		0.46			
b1		0.25			
D			23.24		
E		8.5			
е		2.54			
e3		20.32			
F			7.1		
I			3.93		
L		3.3			
Z		1.27	1.59		

Figure 19. DIP-18 package dimensions



8 Revision history

Table 6. Document revision history

Date	Revision	Changes
18-Sep-2003	1	First release
10-Mar-2010	2	Updated package mechanical data
19-Nov-2012	3	Modified input voltage values Table 4 on page 6.
27-Jun-2018	4	Updated: I _{I(ON)} test condition in <i>Table 4: Electrical characteristics</i> .

IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2018 STMicroelectronics - All rights reserved

577

14/14 DocID1536 Rev 4