

#### Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



December 2014

# 4N29M, 4N30M, 4N32M, 4N33M, H11B1M, TIL113M 6-Pin DIP General Purpose Photodarlington Optocoupler

#### **Features**

- High Sensitivity to Low Input Drive Current
- Meets or Exceeds All JEDEC Registered Specifications
- Safety and Regulatory Approvals:
  - UL1577, 4,170 VAC<sub>RMS</sub> for 1 Minute
- DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

# **Applications**

- Low Power Logic Circuits
- Telecommunications Equipment
- Portable Electronics
- Solid State Relays
- Interfacing Coupling Systems of Different Potentials and Impedances

# **Description**

The 4N29M, 4N30M, 4N32M, 4N33M, H11B1M, and TIL113M have a gallium arsenide infrared emitter optically coupled to a silicon planar photodarlington.

#### **Schematic**

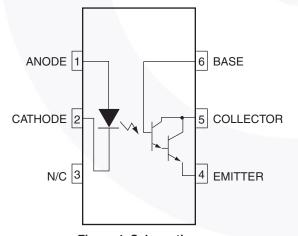


Figure 1. Schematic

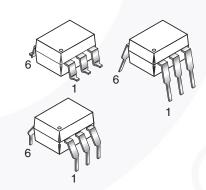


Figure 2. Package Outlines

# Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

| Parameter                                  | Characteristics        |           |
|--|------------------------|-----------|
| Installation Classifications per DIN VDE   | < 150 V <sub>RMS</sub> | I–IV      |
| 0110/1.89 Table 1, For Rated Mains Voltage | < 300 V <sub>RMS</sub> | I–IV      |
| Climatic Classification                    |                        | 55/100/21 |
| Pollution Degree (DIN VDE 0110/1.89)       |                        | 2         |
| Comparative Tracking Index                 |                        | 175       |

| Symbol                | Parameter  | Value             | Unit              |
|-----------------------|--|-------------------|-------------------|
| \/                    | Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC                      | 1360              | V <sub>peak</sub> |
| V <sub>PR</sub>       | Input-to-Output Test Voltage, Method B, V <sub>IORM</sub> x 1.875 = V <sub>PR</sub> , 100% Production Test with t <sub>m</sub> = 1 s, Partial Discharge < 5 pC | 1594              | V <sub>peak</sub> |
| V <sub>IORM</sub>     | Maximum Working Insulation Voltage   | 850               | V <sub>peak</sub> |
| V <sub>IOTM</sub>     | Highest Allowable Over-Voltage   | 6000              | V <sub>peak</sub> |
|                       | External Creepage  | ≥ 7               | mm                |
|                       | External Clearance   | ≥ 7               | mm                |
|                       | External Clearance (for Option TV, 0.4" Lead Spacing)  | ≥ 10              | mm                |
| DTI                   | Distance Through Insulation (Insulation Thickness)   | ≥ 0.5             | mm                |
| T <sub>S</sub>        | Case Temperature <sup>(1)</sup>  | 175               | °C                |
| I <sub>S,INPUT</sub>  | Input Current <sup>(1)</sup>   | 350               | mA                |
| P <sub>S,OUTPUT</sub> | Output Power <sup>(1)</sup>  | 800               | mW                |
| R <sub>IO</sub>       | Insulation Resistance at T <sub>S</sub> , V <sub>IO</sub> = 500 V <sup>(1)</sup>   | > 10 <sup>9</sup> | Ω                 |

#### Note:

1. Safety limit values – maximum values allowed in the event of a failure.

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

| Symbol              | Parameter  | Value              | Unit  |
|---------------------|--|--------------------|-------|
| TOTAL DEVICE        |  |                    |       |
| T <sub>STG</sub>    | Storage Temperature                                    | -40 to +125        | °C    |
| T <sub>OPR</sub>    | Operating Temperature                                  | -40 to +100        | °C    |
| T <sub>J</sub>      | Junction Temperature                                   | -40 to +125        | °C    |
| T <sub>SOL</sub>    | Lead Solder Temperature                                | 260 for 10 seconds | °C    |
| D                   | Total Device Power Dissipation @ T <sub>A</sub> = 25°C | 270                | mW    |
| $P_{D}$             | Derate Above 25°C                                      | 3.3                | mW/°C |
| EMITTER             |  |                    |       |
| I <sub>F</sub>      | Continuous Forward Current                             | 80                 | mA    |
| $V_{R}$             | Reverse Voltage  | 3                  | V     |
| I <sub>F</sub> (pk) | Forward Current – Peak (300 µs, 2% Duty Cycle)         | 3.0                | Α     |
| D                   | LED Power Dissipation @ T <sub>A</sub> = 25°C          | 120                | mW    |
| $P_{D}$             | Derate above 25°C                                      | 2.0                | mW/°C |
| DETECTOR            |  |                    |       |
| BV <sub>CEO</sub>   | Collector-Emitter Breakdown Voltage                    | 30                 | V     |
| BV <sub>CBO</sub>   | Collector-Base Breakdown Voltage                       | 30                 | V     |
| BV <sub>ECO</sub>   | Emitter-Collector Breakdown Voltage                    | 5                  | V     |
| P <sub>D</sub>      | Detector Power Dissipation @ T <sub>A</sub> = 25°C     | 150                | mW    |
|                     | Derate Above 25°C                                      | 2.0                | mW/°C |
| I <sub>C</sub>      | Continuous Collector Current                           | 150                | mA    |

## **Electrical Characteristics**

 $T_A = 25^{\circ}C$  Unless otherwise specified.

## **Individual Component Characteristics**

| Symbol            | Parameter   | Test Conditions                             | Device             | Min. | Тур.  | Max. | Unit |
|-------------------|---|---|--------------------|------|-------|------|------|
| EMITTER           |   |   |                    |      |       |      |      |
|                   |   | I <sub>F</sub> = 10 mA                      | 4NXXM              |      | 1.2   | 1.5  | V    |
| V <sub>F</sub>    | Input Forward Voltage <sup>(2)</sup>                  |   | H11B1M,<br>TIL113M | 0.8  | 1.2   | 1.5  | V    |
|                   |   | V <sub>R</sub> = 3.0 V                      | 4NXXM              |      | 0.001 | 100  | μΑ   |
| I <sub>R</sub>    | Reverse Leakage Current <sup>(2)</sup>                | V <sub>R</sub> = 6.0 V                      | H11B1M,<br>TIL113M |      | 0.001 | 10   | μA   |
| С                 | Capacitance <sup>(2)</sup>                            | V <sub>F</sub> = 0V, f = 1.0 MHz            | All                |      | 150   |      | pF   |
| DETECTO           | DR  |   |                    |      |       |      |      |
| BV <sub>CEO</sub> | Collector-Emitter Breakdown Voltage <sup>(2)</sup>    | I <sub>C</sub> = 1.0 mA, I <sub>B</sub> = 0 | 4NXXM,<br>TIL113M  | 30   | 60    |      | V    |
| 020               |   |   | H11B1M             | 25   | 60    |      | V    |
| BV <sub>CBO</sub> | Collector-Base Breakdown<br>Voltage <sup>(2)</sup>    | I <sub>C</sub> = 100 μA, I <sub>E</sub> = 0 | All                | 30   | 100   |      | V    |
|                   | Emitter Callector Brackdown                           |   | 4NXXM              | 5.0  | 10    |      | V    |
| BV <sub>ECO</sub> | Emitter-Collector Breakdown<br>Voltage <sup>(2)</sup> | $I_E = 100 \mu A, I_B = 0$                  | H11B1M,<br>TIL113M | 7    | 10    |      | V    |
| I <sub>CEO</sub>  | Collector-Emitter Dark<br>Current <sup>(2)</sup>      | V <sub>CE</sub> = 10 V, Base Open           | All                |      | 1     | 100  | nA   |

#### Notes:

2. Indicates JEDEC registered data.

# **Electrical Characteristics** (Continued)

 $T_A = 25$ °C Unless otherwise specified.

#### **Transfer Characteristics**

| Symbol              | Parameter  | Test Conditions   | Device                      | Min.     | Тур. | Max. | Unit   |
|---------------------|--|---|-----------------------------|----------|------|------|--------|
| DC CHARA            | CTERISTICS                                       |   |                             |          |      | •    | •      |
| I <sub>C(CTR)</sub> | Collector Output<br>Current <sup>(3)(4)(5)</sup> | I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V,   | 4N32M,<br>4N33M             | 50 (500) |      |      | mA (%) |
|                     |  | I <sub>B</sub> = 0  | 4N29M,<br>4N30M             | 10 (100) |      |      | mA (%) |
|                     |  | I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V  | H11B1M                      | 5 (500)  |      |      | mA (%) |
|                     |  | I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 1 V   | TIL113M                     | 30 (300) |      |      | mA (%) |
|                     | Saturation Voltage <sup>(3)(5)</sup>             | I <sub>F</sub> = 8 mA, I <sub>C</sub> = 2.0 mA  | 4NXXM                       |          |      | 1.0  | V      |
| $V_{CE(SAT)}$       |  |   | TIL113M                     |          |      | 1.25 | V      |
|                     |  | I <sub>F</sub> = 1 mA, I <sub>C</sub> = 1 mA  | H11B1M                      |          |      | 1.0  | V      |
| AC CHARA            | CTERISTICS                                       |   |                             |          |      |      | •      |
| t <sub>on</sub>     | Turn-on Time                                     | $I_F = 200 \text{ mA}, I_C = 50 \text{ mA}, V_{CC} = 10 \text{ V}, R_L = 100 \Omega$      | 4NXXM,<br>TIL113M           |          |      | 5.0  | μs     |
|                     |  | $I_F$ = 10 mA, $V_{CE}$ = 10 V, $R_L$ = 100 $\Omega$                                      | H11B1M                      |          | 25   |      | μs     |
| <sup>†</sup> off    | Turn-off Time                                    | $I_F = 200 \text{ mA}, I_C = 50 \text{ mA},$<br>$V_{CC} = 10 \text{ V}, R_L = 100 \Omega$ | 4N32M,<br>4N33M,<br>TIL113M |          |      | 100  | μs     |
|                     |  |   | 4N29M,<br>4N30M             |          |      | 40   | μs     |
|                     |  | $I_F$ = 10 mA, $V_{CE}$ = 10 V, $R_L$ = 100 $\Omega$                                      | H11B1M                      |          | 18   |      | μs     |
| BW                  | Bandwidth <sup>(6)(7)</sup>                      |   |                             |          | 30   |      | kHz    |

#### Notes:

- 3. Indicates JEDEC registered data.
- 4. The current transfer  $ratio(I_C/I_F)$  is the ratio of the detector collector current to the LED input current.
- 5. Pulse test: pulse width = 300  $\mu$ s, duty cycle  $\leq$  2.0% .
- 6.  $I_F$  adjusted to  $I_C$  = 2.0 mA and  $I_C$  = 0.7 mA rms.
- 7. The frequency at which  $I_{\mbox{\scriptsize C}}$  is 3 dB down from the 1 kHz value.

#### **Isolation Characteristics**

| Symbol           | Characteristic                 | Test Conditions                                    | Min.             | Тур. | Max. | Unit               |
|------------------|--------------------------------|--|------------------|------|------|--------------------|
| V <sub>ISO</sub> | Input-Output Isolation Voltage | t = 1 Minute                                       | 4170             |      |      | VAC <sub>RMS</sub> |
| C <sub>ISO</sub> | Isolation Capacitance          | V <sub>I-O</sub> = 0 V, f = 1 MHz                  |                  | 0.2  |      | pF                 |
| R <sub>ISO</sub> | Isolation Resistance           | V <sub>I-O</sub> = ±500 VDC, T <sub>A</sub> = 25°C | 10 <sup>11</sup> |      |      | Ω                  |

# **Typical Performance Curves**

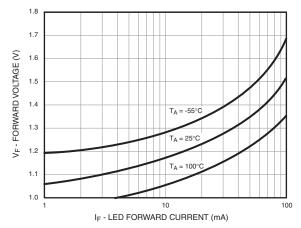


Figure 3. LED Forward Voltage vs. Forward Current

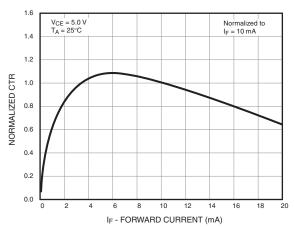


Figure 4. Normalized CTR vs. Forward Current

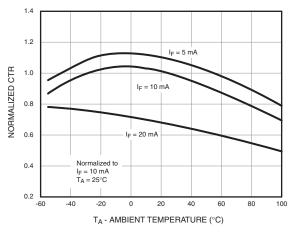


Figure 5. Normalized CTR vs. Ambient Temperature

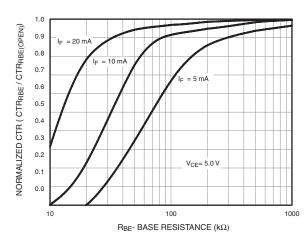


Figure 6. CTR vs. RBE (Unsaturated)

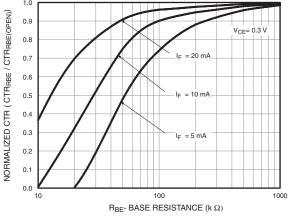


Figure 7. CTR vs. RBE (Saturated)

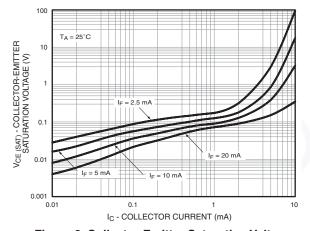


Figure 8. Collector-Emitter Saturation Voltage vs. Collector Current

# **Typical Performance Curves** (Continued)

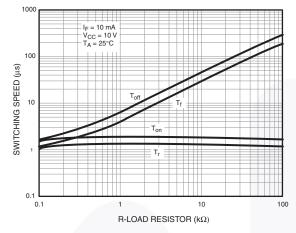


Figure 9. Switching Speed vs. Load Resistor

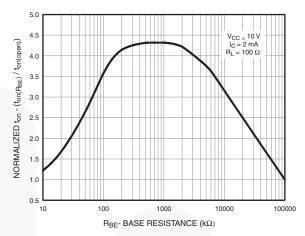


Figure 10. Normalized ton vs. RBE

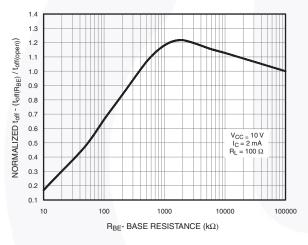
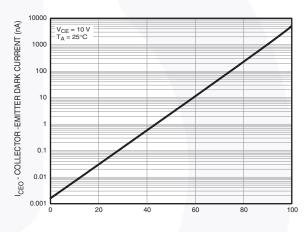


Figure 11. Normalized toff vs. RBE



T<sub>A</sub> - AMBIENT TEMPERATURE (°C)

Figure 12. Dark Current vs. Ambient Temperature

# **Switching Time Test Circuit and Waveform**

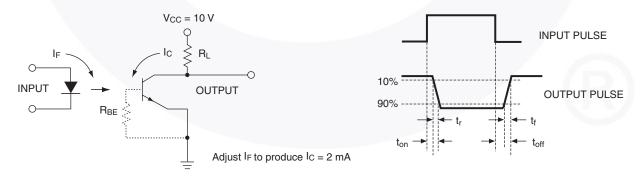


Figure 13. Switching Time Test Circuit and Waveform

## **Reflow Profile**

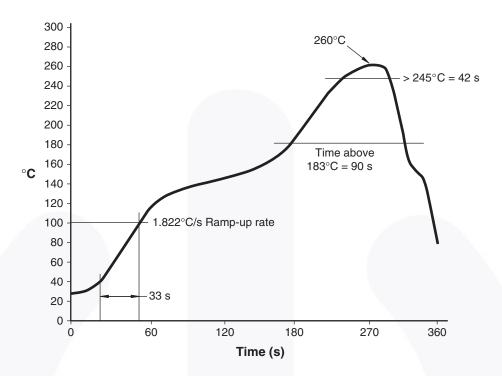


Figure 14. Reflow Profile

# **Ordering Information**

| Part Number | Package  | Packing Method             |
|-------------|--|----------------------------|
| 4N29M       | DIP 6-Pin  | Tube (50 Units)            |
| 4N29SM      | SMT 6-Pin (Lead Bend)                                    | Tube (50 Units)            |
| 4N29SR2M    | SMT 6-Pin (Lead Bend)                                    | Tape and Reel (1000 Units) |
| 4N29VM      | DIP 6-Pin, DIN EN/IEC60747-5-5 Option                    | Tube (50 Units)            |
| 4N29SVM     | SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option        | Tube (50 Units)            |
| 4N29SR2VM   | SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option        | Tape and Reel (1000 Units) |
| 4N29TVM     | DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option | Tube (50 Units)            |

#### Note:

8. The product orderable part number system listed in this table also applies to the 4N30M, 4N32M, 4N33M, H11B1M, and TIL113M devices.

# **Marking Information**

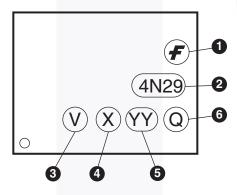


Figure 15. Top Mark

## **Table 1. Top Mark Definitions**

| 1 | Fairchild Logo  |
|---|---|
| 2 | Device Number   |
| 3 | DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option) |
| 4 | One-Digit Year Code, e.g., "4"  |
| 5 | Digit Work Week, Ranging from "01" to "53"                                      |
| 6 | Assembly Package Code   |







# NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION
- D) DRAWING FILENAME AND REVSION: MKT-N06BREV4.







LAND PATTERN RECOMMENDATION





- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION
- D) DRAWING FILENAME AND REVSION: MKT-N06CREV4.









#### NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION
- D) DRAWING FILENAME AND REVSION: MKT-N06Drev4



ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdt/Patent-Marking.pdf">www.onsemi.com/site/pdt/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor:

TIL113SM TIL113M