Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max T_A = +25°C
60V	2Ω @ V_{GS} = 10V	380mA
	3Ω @ V_{GS} = 5V	310mA

Description

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Power Management Functions
- Backlighting

Features and Benefits

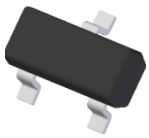
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Up To 2kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

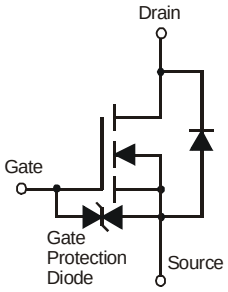
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.008 grams (Approximate)

ESD protected up to 2kV

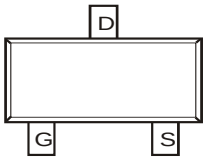
SOT23



Top View



Equivalent Circuit



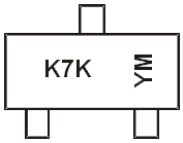
Top View

Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
2N7002K-7	Standard	SOT23	3000/Tape & Reel
2N7002KQ-7	Automotive	SOT23	3000/Tape & Reel
2N7002K-13	Standard	SOT23	10000/Tape & Reel
2N7002KQ-13	Automotive	SOT23	10000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K7K = Product Type Marking Code
YM or $\bar{Y}$M= Date Code Marking
Y or $\bar{Y}$ = Year (ex: F = 2018)
M = Month (ex: 9 = September)

Date Code Key

Year	2006	~	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	T	~	F	G	H	I	J	K	L	M	N	O

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 7) V_{GS} = 10V	Steady State	T_A = +25°C T_A = +70°C	I_D	380 300	mA
	t<5s	T_A = +25°C T_A = +70°C	I_D	430 340	mA
Continuous Drain Current (Note 7) V_{GS} = 5V	Steady State	T_A = +25°C T_A = +70°C	I_D	310 240	mA
	t<5s	T_A = +25°C T_A = +70°C	I_D	350 270	mA
Maximum Continuous Body Diode Forward Current (Note 7)			I_S	0.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 7)			I_{DM}	1.2	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P_D	370	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	357	°C/W
	t<5s		292	
Total Power Dissipation (Note 7)		P_D	540	mW
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	240	°C/W
	t<5s		197	
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	91	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 10µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1.0	µA	V_{DS} = 60V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.0	1.6	2.5	V	V_{DS} = 10V, I_D = 1mA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	— —	2.0 3.0	Ω	V_{GS} = 10V, I_D = 0.5A V_{GS} = 5V, I_D = 0.05A
Forward Transfer Admittance	$\|Y_{fs}\|$	80	—	—	ms	V_{DS} =10V, I_D = 0.2A
Diode Forward Voltage	V_{SD}	—	0.75	1.1	V	V_{GS} = 0V, I_S = 115mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	—	30	50	pF	V_{DS} = 25V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	4.2	25	pF	
Reverse Transfer Capacitance	C_{rss}	—	2.9	5.0	pF	
Gate Resistance	R_g	—	133	—	Ω	f = 1MHz , V_{GS} = 0V, V_{DS} = 0V
Total Gate Charge	Q_g	—	0.3	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 250mA
Gate-Source Charge	Q_{gs}	—	0.2	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.08	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.9	—	ns	V_{DD} = 30V, V_{GS} = 10V, R_G = 25Ω, I_D = 200mA
Turn-On Rise Time	t_R	—	3.4	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	15.7	—	ns	
Turn-Off Fall Time	t_F	—	9.9	—	ns	

Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.
7. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

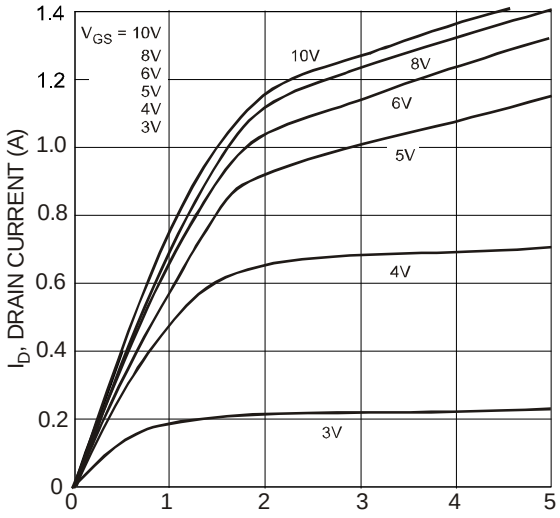


Fig. 1 Typical Output Characteristics

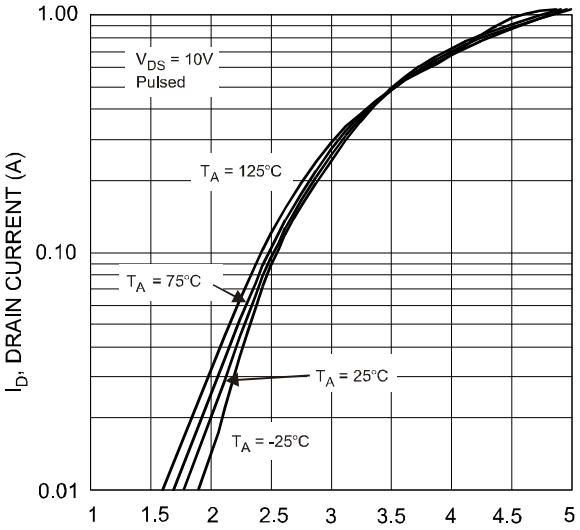


Fig. 2 Typical Transfer Characteristics

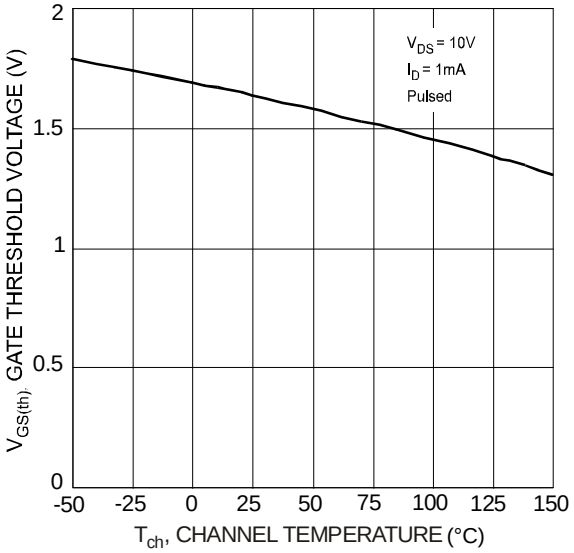


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

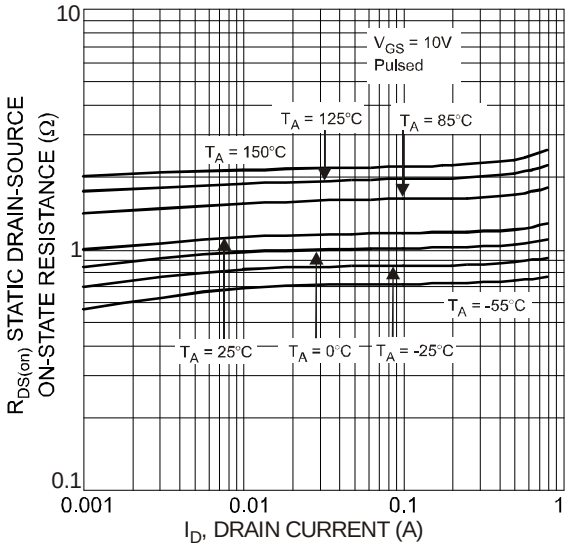


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

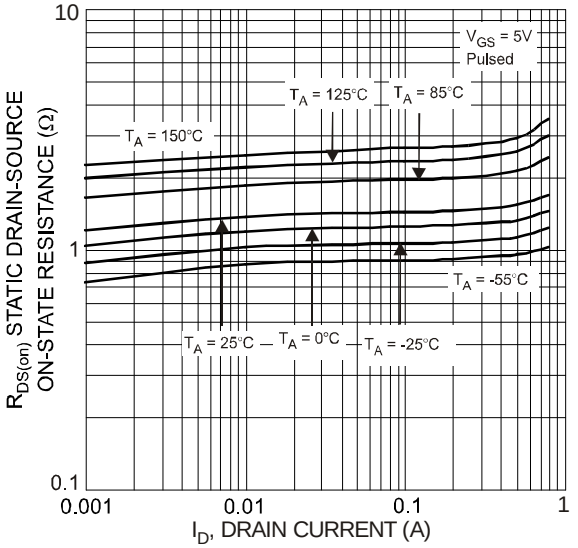


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

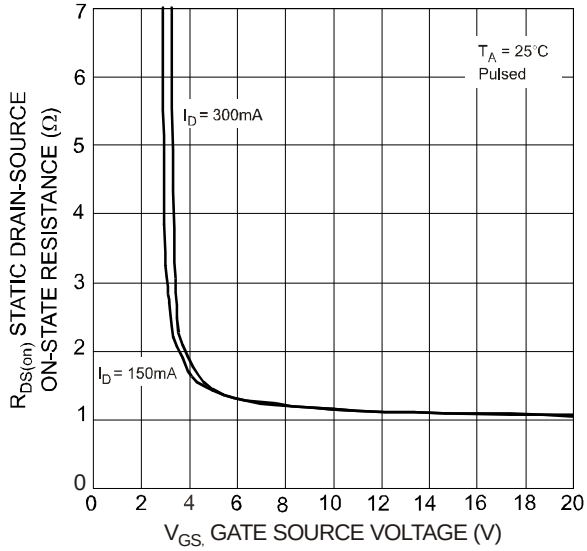


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage

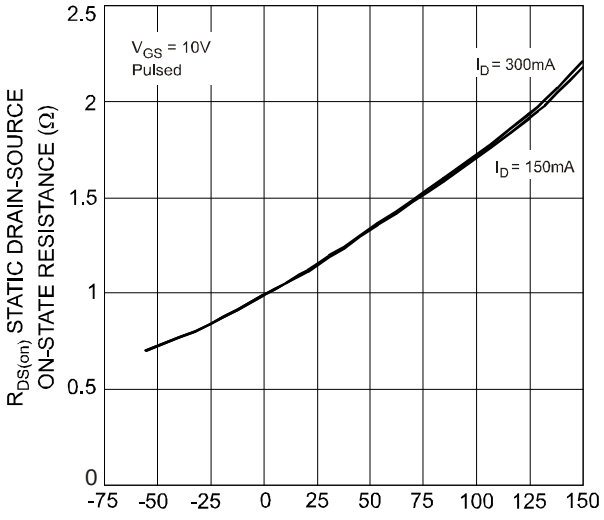


Fig. 7 Static Drain-Source On-State Resistance vs. Channel Temperature

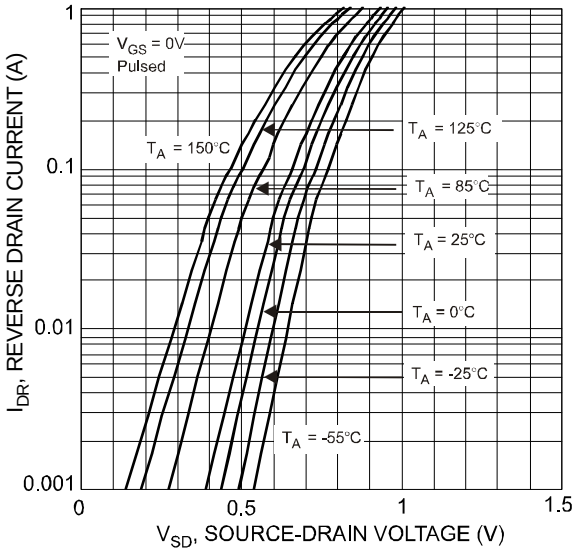


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

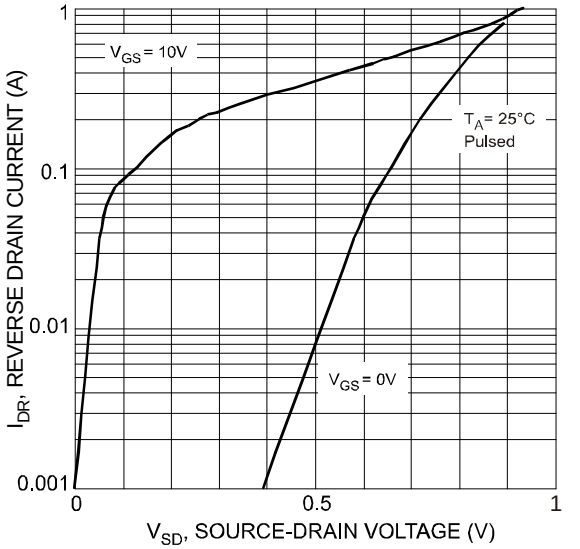


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

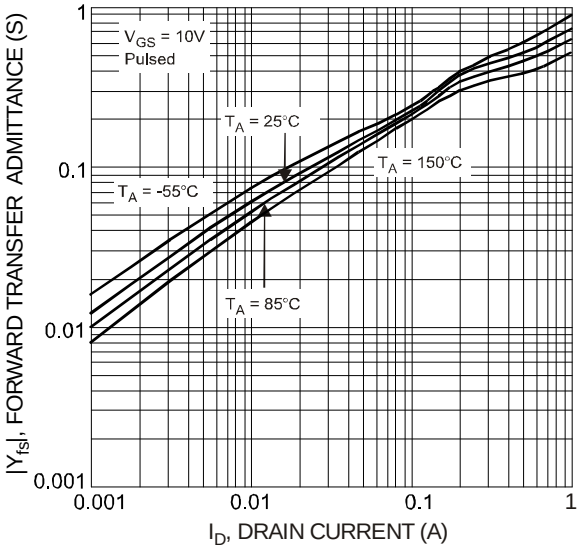


Fig.10 Forward Transfer Admittance vs. Drain Current

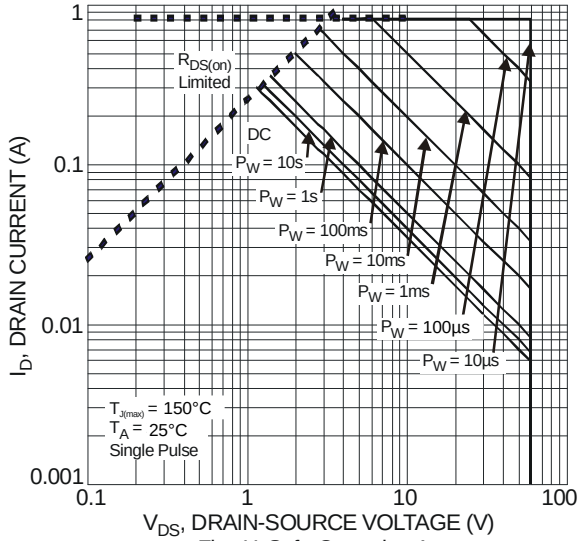


Fig. 11 Safe Operation Area

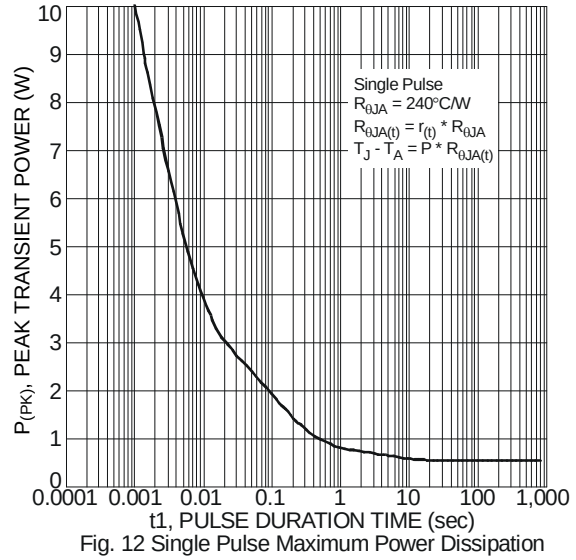


Fig. 12 Single Pulse Maximum Power Dissipation

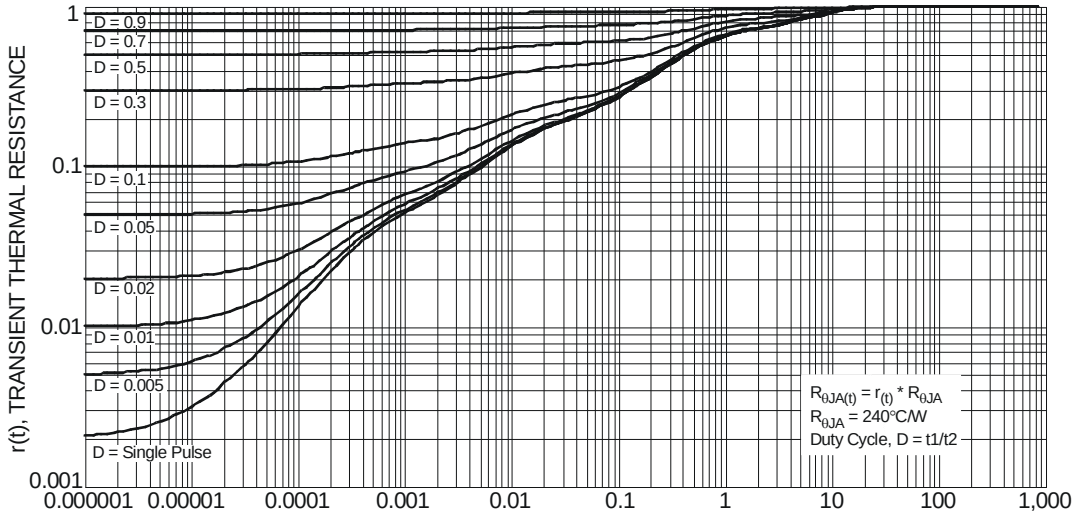
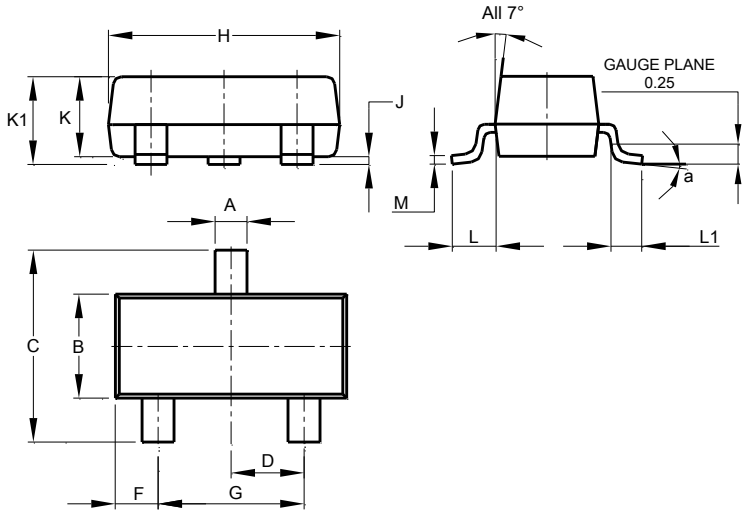


Fig. 13 Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

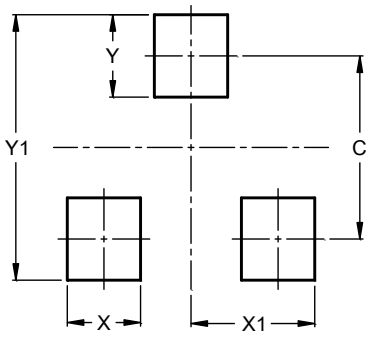


SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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