



P-Channel 1.5 V (G-S) MOSFET

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)				
- 8	0.023 at $V_{GS} = -4.5 \text{ V}$	- 7					
	0.029 at V _{GS} = - 2.5 V	- 6.2	28				
	0.036 at V _{GS} = - 1.8 V	- 5.2	20				
	0.048 at V _{GS} = - 1.5 V	- 5					

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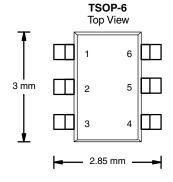
FEATURES

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET: 1.5 V Rated
- Ultra-Low On-Resistance
- 100 % R_q Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

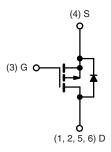
· Load Switch for Portable Devices



Ordering Information: Si3499DV-T1-E3 (Lead (Pb)-free)

Si3499DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 99xxx



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	6 (T _A = 25 °C, unle	ess otherwise	noted)		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 8		V
Gate-Source Voltage		V _{GS}	± 5		
O D . O (T 450.00)3	T _A = 25 °C	I _D	- 7	- 5.3	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		- 3.6	- 3.9	
Pulsed Drain Current		I _{DM}	- 20		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7	- 0.9	
	T _A = 25 °C	D	2	1.1	١٨/
Maximum Power Dissipation ^a	T _A = 85 °C	P_{D}	1	0.6	W
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55	to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian una lumation ta Analogată	t ≤ 5 s	R _{thJA}	45	62.5		
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	90	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	25	30		

a. Surface mounted on 1" x 1" FR4 board.

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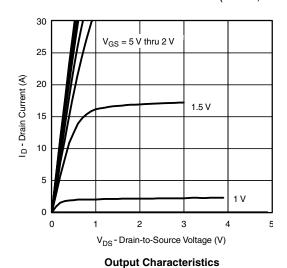


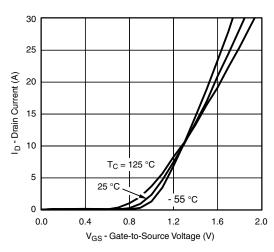
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.35		- 0.75	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	1	V _{DS} = - 8 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 10		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			Α	
		V _{GS} = - 4.5 V, I _D = - 7 A		0.019	0.023		
Due to Course On Otata Bastatana a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -6.2 \text{ A}$		0.024	0.029	Ω	
Drain-Source On-State Resistance ^a		V _{GS} = - 1.8 V, I _D = - 5.2 A		0.028	0.036		
		V _{GS} = - 1.5 V, I _D = - 3 A		0.035	0.048		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 7 A		28		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7 A$, $V_{GS} = 0 V$		- 0.63	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			28	42		
Gate-Source Charge	Q _{gs}	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7 \text{ A}$		2.9		nC	
Gate-Drain Charge	Q _{gd}			5.8		1	
Gate Resistance	R_g		4	8.5	13	Ω	
Turn-On Delay Time	t _{d(on)}			27	40		
Rise Time	t _r	V_{DD} = - 4 V, R_L = 4 Ω		65	100		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		210	315	ns	
Fall Time	t _f			110	165		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dI/dt = 100 A/μs		40	70		

- Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

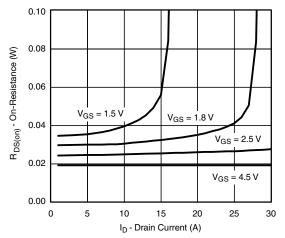




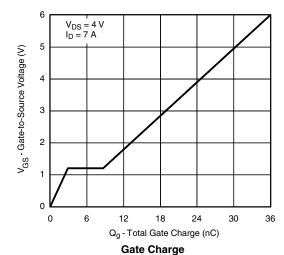
Transfer Characteristics

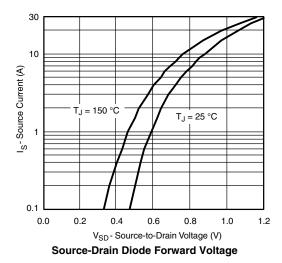


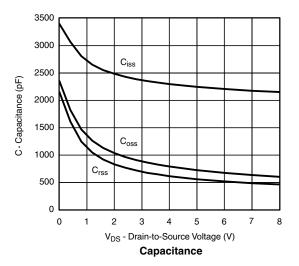
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

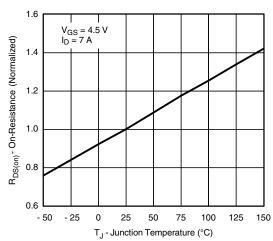


On-Resistance vs. Drain Current

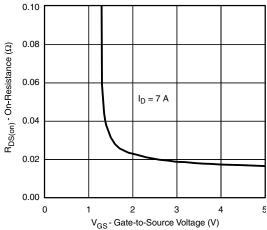








On-Resistance vs. Junction Temperature

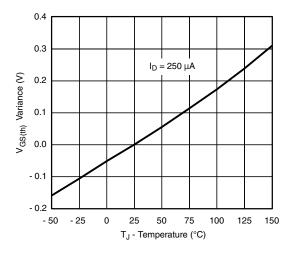


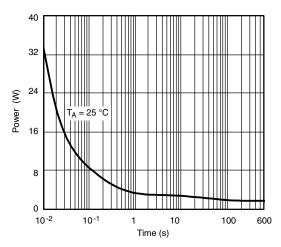
On-Resistance vs. Gate-to-Source Voltage

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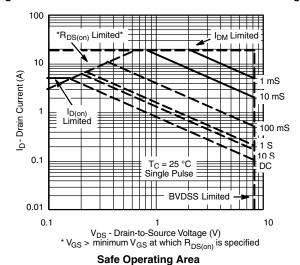
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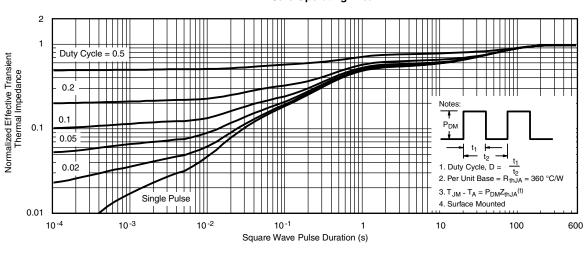




Threshold Voltage

Single Pulse Power

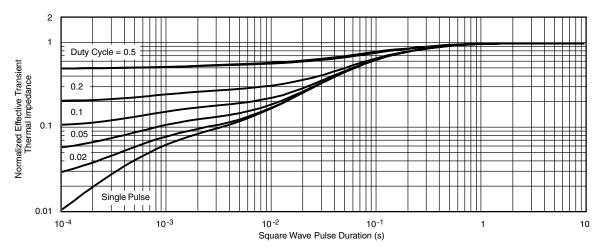




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73138.

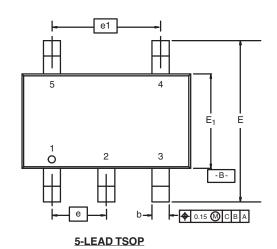


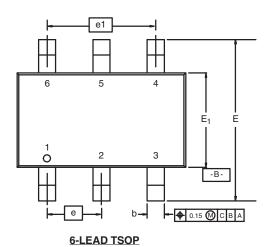


TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

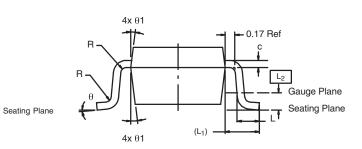
a 0.08 C





D -A-

-C- A₁

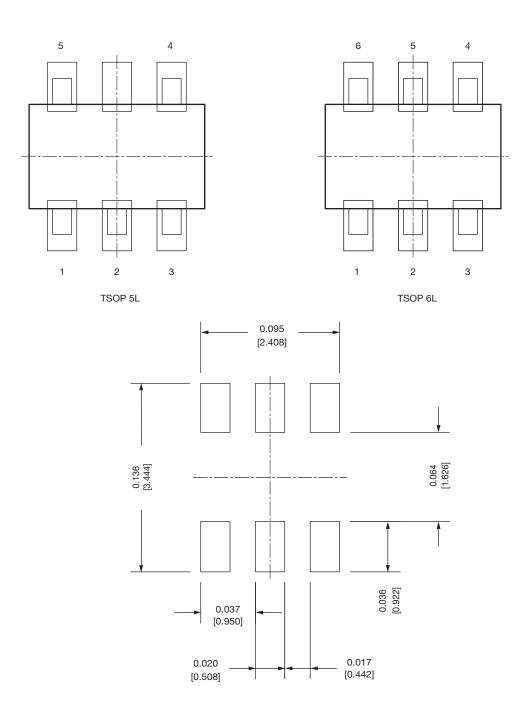


	MILLIMETERS			INCHES				
Dim	Min	Nom	Max	Min	Nom	Max		
Α	0.91	-	1.10	0.036	-	0.043		
A ₁	0.01	-	0.10	0.0004	-	0.004		
A ₂	0.90	-	1.00	0.035	0.038	0.039		
b	0.30	0.32	0.45	0.012	0.013	0.018		
С	0.10	0.15	0.20	0.004	0.006	0.008		
D	2.95	3.05	3.10	0.116	0.120	0.122		
Е	2.70	2.85	2.98	0.106	0.112	0.117		
E ₁	1.55	1.65	1.70	0.061	0.065	0.067		
е		0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071 0.075 0		0.079		
L	0.32	-	0.50	0.012	-	0.020		
L ₁	0.60 Ref			0.024 Ref				
L ₂	0.25 BSC			0.010 BSC				
R	0.10	-	-	0.004	-	-		
θ	0°	4°	8°	0°	4°	8°		
θ_1	7° Nom 7° Nom							
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540								

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Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

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Revision: 24-Oct-2022 1 Document Number: 72610





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