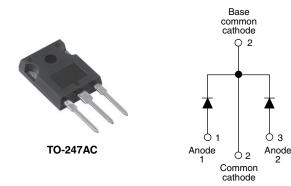


**Vishay Semiconductors** 

# Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY							
Package	TO-247AC						
I <sub>F(AV)</sub>	2 x 30 A						
V <sub>R</sub>	45 V						
V <sub>F</sub> at I <sub>F</sub>	0.55 V						
I <sub>RM</sub> max.	150 mA at 125 °C						
T <sub>J</sub> max.	150 °C						
Diode variation	Common cathode						
E <sub>AS</sub>	27 mJ						

### FEATURES

- 150 °C T<sub>J</sub> operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### DESCRIPTION

The VS-MBR6045WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	60	A							
V <sub>RRM</sub>		45	V							
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2900	A							
V <sub>F</sub>	30 Apk, T <sub>J</sub> = 125 °C (per leg)	0.55	V							
TJ		- 55 to 150	°C							

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-MBR6045WTPbF	VS-MBR6045WT-N3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>	45	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	40	v				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	TEST CONDITIONS		UNITS			
Maximum average	6 1 5				30				
forward current See fig. 5	per device	I <sub>F(AV)</sub>	$f_0$ 50 % duty cycle at T <sub>C</sub> = 122 °C, rectangular waveform						
Maximum peak one cycle non-repetitive surge current pe			5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	2900	A			
See fig. 7	rieg	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	360				
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 4 A, L = 3.4 mH		27	mJ			
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		6	А			

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		30 A	T <sub>.1</sub> = 25 °C	0.62	v			
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	60 A	1j=23 0	0.75				
		30 A	T <sub>J</sub> = 125 °C	0.55				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	<b>m</b> 1			
See fig. 2		T <sub>J</sub> = 125 °C	VR = haled VR	150	mA			
Threshold voltage	V <sub>F(TO)</sub>			0.27	V			
Forward slope resistance	r <sub>t</sub>	ij = ij maximum	$T_J = T_J$ maximum		mΩ			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal ran	1400	pF				
Typical series inductance per leg	Ls	Measured lead to lead 5 m	7.5	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

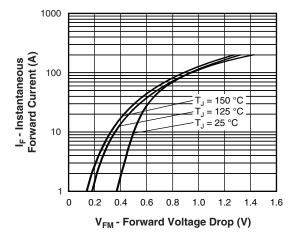
#### Note

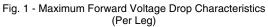
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

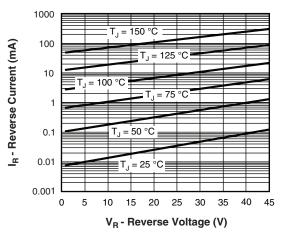
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	1.0				
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	0.5	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24				
Approvimate weight				6	g			
Approximate weight				0.21	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
	maximum			12 (10)	(lbf ⋅ in)			
Marking device			Case style TO-247AC (JEDEC)	MBR60	)45WT			

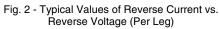


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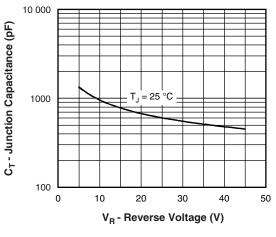
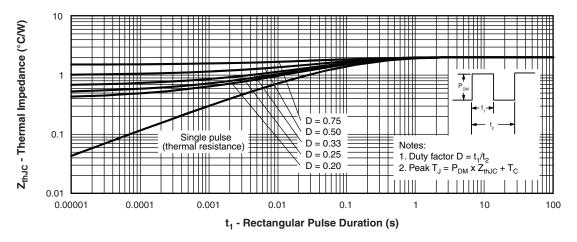


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



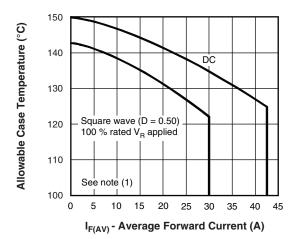


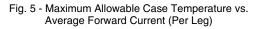
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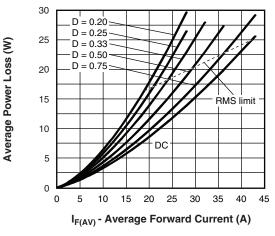
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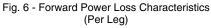


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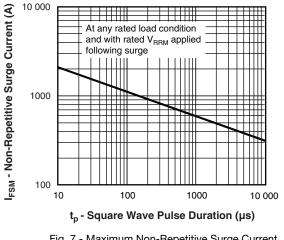


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

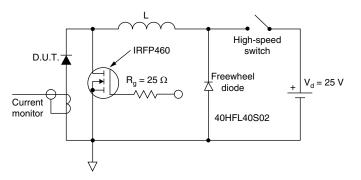


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R \text{ at } V_{R1}$  = 100 % rated  $V_R$ 

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 $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);





## Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	VS-	MBR	60	45	WT	PbF
	1	2	3	4	5	6
	1 · 2 ·	- Sch	ottky M	niconduc BR serie	s	oduct
	3 · 4 ·			ng (60 = ng (45 =	,	
	5 -	- Circ	uit conf	iguratior	า:	
	6	Cer	nter tap	(dual) T	D-247	
	7.	- Env	vironmer	ntal digit		
		• 5		ad (Db)	froo on	

- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-MBR6045WTPbF	25	500	Antistatic plastic tube					
VS-MBR6045WT-N3	25	500	Antistatic plastic tube					

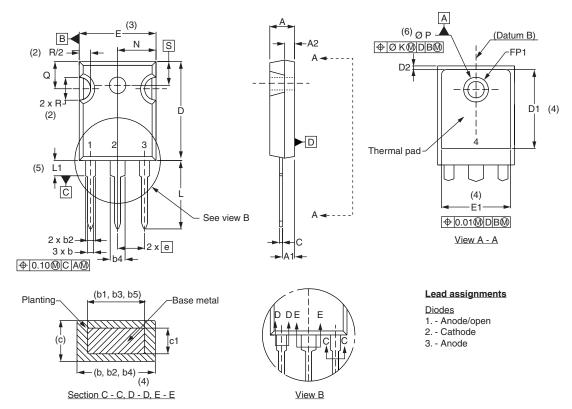
LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?95223							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					

## **Outline Dimensions**





### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		ERS INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209		D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102		E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098		E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055		e	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053		FK	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133		ΦP	3.56	3.66	0.14	0.144	
с	0.38	0.86	0.015	0.034		Φ <b>P1</b>	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	1.78	0.216	
D1	13.08	_	0.515	-	4	S	5.51	BSC	0.217	BSC	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC outline TO-247 with exception of dimension c

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