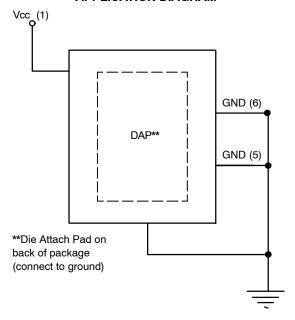
ESD Protection Diode

Low Clamping Voltage

Features

- Unidirectional High Voltage ESD Protection
- Provides ESD Protection to IEC61000-4-2 Level 4: ±30 kV Contact Discharge
- IEC 61000-4-5 (lighting)
- High Voltage Zener Diode Protects Supply Rail up to 160 A (8/20 µs)
- These Devices are Pb-Free and are RoHS Compliant

APPLICATION DIAGRAM





ON Semiconductor®

www.onsemi.com



UDFN6 CASE 517CS

BLOCK DIAGRAM



MARKING DIAGRAM



A3 = Specific Device Code
M = Date Code
Pb-Free Package

ORDERING INFORMATION

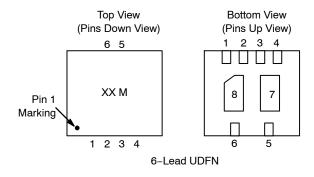
Device	Package	Shipping [†]		
NSPM5131MUTBG	UDFN6	3000/Tape &		
	(Pb-Free)	Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Table 1. PIN DESCRIPTIONS

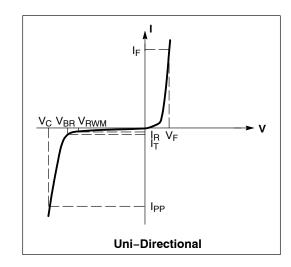
4-Channel, 6-Lead, UDFN-8 Package					
Pin	Name	Туре	Description		
1	V _{CC}	HV V _{DD}	HV ESD Channel		
2	N/C		No Connect		
3	N/C	No Connect			
4	N/C		No Connect		
5	GND		Ground		
6	GND		Ground		
7	GND		Ground		
8	GND		Ground		

PACKAGE / PINOUT DIAGRAMS



ELECTRICAL CHARACTERISTICS

Symbol	Parameter				
I _{PP}	Maximum Reverse Peak Pulse Current				
V _C	Clamping Voltage @ I _{PP}				
V _{RWM}	Working Peak Reverse Voltage				
I _R	Maximum Reverse Leakage Current @ V _{RWM}				
V _{BR}	Breakdown Voltage @ I _T				
Ι _Τ	Test Current				
ΘV _{BR}	Maximum Temperature Coefficient of V _{BR}				
I _F	Forward Current				
V _F	Forward Voltage @ I _F				



SPECIFICATIONS

Table 2. MAXIMUM RATINGS

Parameter	Rating	Units
Operating Temperature Range	-55 to +125	°C
Storage Temperature Range	-65 to +150	°C
Peak Current (t _p = 8/20 μs)	160	Α

Stresses at or above those listed in Maximum Ratings table may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Also, due to variations in test equipment, stresses shown above are averages.

ELECTRICAL CHARACTERISTICS

		V _{RWM} (V) (Note 1) I _R @ V _{RW}		Breakdown Volta		tage	V _C @ I _{PP} (8 x 20 μs) (Note 3)		
	Device		I _R @ V _{RWM} (μ A)	V _{BI}	R V (Note	2)	@ I _T (mA)	V _C (V)	I _{PP} (A)
Device Name	Marking	Max	Max	Min	Nom	Max		Max	
NSPM5131	A3	13.5	1	13.6	15.5	17.5	1	21.5	100

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 1. A transient suppressor is normally selected according to the working peak reverse voltage (V_{RWM}), which should be equal to or greater than the DC or continuous peak operating voltage level.
- 2. V_{BR} measured at pulse test current I_T at an ambient temperature of 25°C.
- 3. Surge current waveform per Figure 2.

TYPICAL CHARACTERISTICS

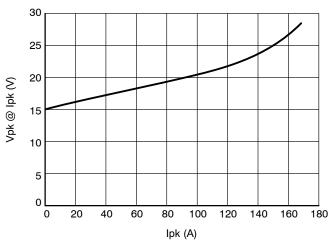


Figure 1. Clamping Voltage vs. Peak Pulse Current (t_p = 8/20 μ s)

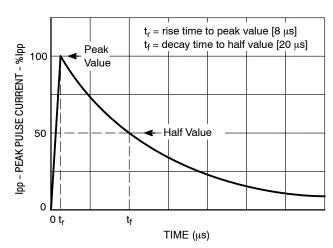
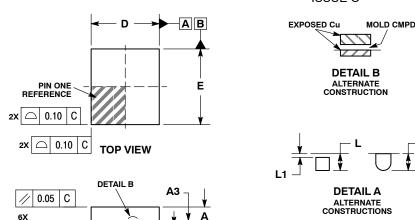


Figure 2. IEC61000-4-5 8/20 μs Pulse Waveform

PACKAGE DIMENSIONS

UDFN6, 1.8 x 2, 0.4P CASE 517CS **ISSUE 0**



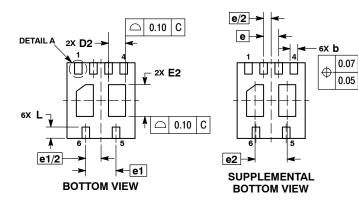
A1

SIDE VIEW

NOTES

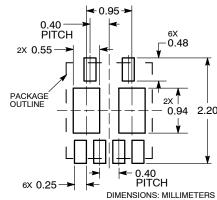
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION b APPLIES TO PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.30mm FROM THE TERMINAL TIP.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.45	0.55		
A1	0.00 0.05			
A3	0.125 REF			
b	0.15	0.25		
D	1.80 BSC			
D2	0.35 0.55			
E	2.00 BSC			
E2	0.74	0.94		
е	0.40 BSC			
e1	0.80 BSC			
e2	0.95 BSC			
L	0.20	0.40		
L1		0.15		



SEATING PLANE





*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. Coverage may be accessed at www.onsemi.com/site/par/-atent_-warking.pgr. On Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

 \Box 0.05 С

NOTE 4

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

CAB

С моте з

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Phone: 81-3-5817-1050

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor:

NSPM5131MUTBG