

Silicon Carbide (SiC) Module – EliteSiC, 40 mΩ SiC M1 MOSFET, 1200 V + 40 A, 1200 V SiC Diode, Three Channel Full SiC Boost, Q1 Package

Product Preview

NXH40B120MNQ1SNG

The NXH40B120MNQ1SNG is a power module containing a three channel boost stage. The integrated SiC MOSFETs and SiC Diodes provide lower conduction losses and switching losses, enabling designers to achieve high efficiency and superior reliability.

Features

- 1200 V 40 mΩ SiC MOSFETs
- Low Reverse Recovery and Fast Switching SiC Diodes
- 1200 V Bypass and Anti-parallel Diodes
- Low Inductive Layout
- Solderable Pins
- Thermistor
- This Device is Pb-Free, Halogen Free/BFR Free and is RoHS Compliant

Typical Applications

- Solar Inverters
- Uninterruptable Power Supplies

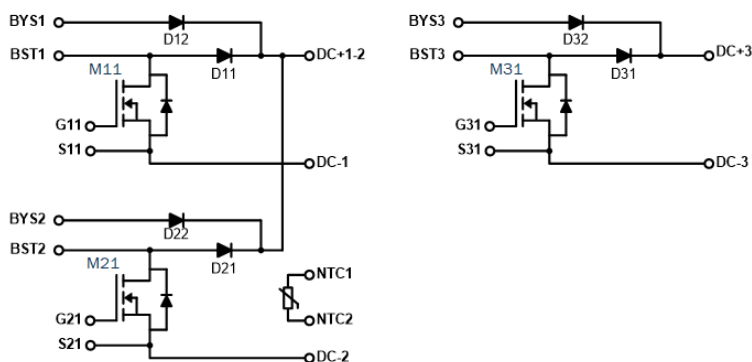
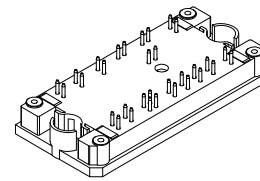


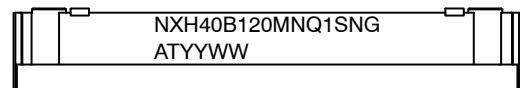
Figure 1. NXH80B120MNQ0SNG Schematic Diagram

This document contains information on a product under development. onsemi reserves the right to change or discontinue this product without notice.



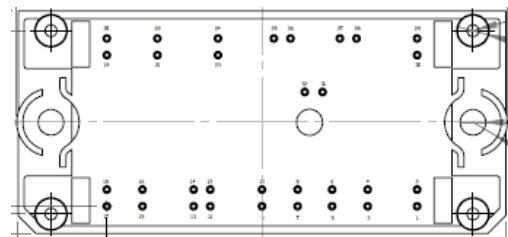
PIM32, 71x37.4
(SOLDER PIN)
CASE 180BQ

MARKING DIAGRAM



NXH40B120MNQ1SNG = Device Code
G = Pb-Free Package
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

NXH40B120MNQ1SNG

ABSOLUTE MAXIMUM RATINGS (Note 1) $T_J = 25^\circ\text{C}$ unless otherwise noted

Rating	Symbol	Value	Unit
BOOST SiC MOSFET (M11, M21, M31)			
Drain–Source Voltage	V_{DS}	1200	V
Gate–Source Voltage	V_{GS}	-15/+25	V
Continuous Drain Current (@ $V_{GS} = 20\text{ V}$, $T_C = 80^\circ\text{C}$)	I_D	44	A
Pulsed Drain Current @ $T_C = 80^\circ\text{C}$ ($T_J = 175^\circ\text{C}$)	$I_{D(\text{Pulse})}$	132	A
Maximum Power Dissipation @ $T_C = 80^\circ\text{C}$	P_{tot}	156	W
Minimum Operating Junction Temperature	$T_{J\text{MIN}}$	-40	$^\circ\text{C}$
Maximum Operating Junction Temperature	$T_{J\text{MAX}}$	175	$^\circ\text{C}$

BOOST DIODE (D11, D21, D31)

Peak Repetitive Reverse Voltage	V_{RRM}	1200	V
Continuous Forward Current @ $T_C = 80^\circ\text{C}$	I_F	53	A
Surge Forward Current (60 Hz single half-sine wave)	I_{FRM}	159	A
Maximum Power Dissipation @ $T_C = 80^\circ\text{C}$ ($T_J = 175^\circ\text{C}$)	P_{tot}	153	W
Minimum Operating Junction Temperature	$T_{J\text{MIN}}$	-40	$^\circ\text{C}$
Maximum Operating Junction Temperature	$T_{J\text{MAX}}$	175	$^\circ\text{C}$

BYPASS DIODE (D12, D22, D32)

Peak Repetitive Reverse Voltage	V_{RRM}	1200	V
Continuous Forward Current @ $T_C = 80^\circ\text{C}$ ($T_J = 150^\circ\text{C}$)	I_F	75	A
Repetitive Peak Forward Current ($T_J = 150^\circ\text{C}$, t_p limited by $T_{J\text{max}}$)	I_{FRM}	225	A
Power Dissipation Per Diode @ $T_C = 80^\circ\text{C}$ ($T_J = 150^\circ\text{C}$)	P_{tot}	97	W
Minimum Operating Junction Temperature	$T_{J\text{MIN}}$	-40	$^\circ\text{C}$
Maximum Operating Junction Temperature	$T_{J\text{MAX}}$	150	$^\circ\text{C}$

THERMAL PROPERTIES

Storage Temperature range	T_{stg}	-40 to 125	$^\circ\text{C}$
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INSULATION PROPERTIES

Isolation test voltage, $t = 1\text{ sec}$, 60 Hz	V_{is}	3000	V_{RMS}
Creepage distance		12.7	mm

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to [ELECTRICAL CHARACTERISTICS](#), [RECOMMENDED OPERATING RANGES](#) and/or APPLICATION INFORMATION for Safe Operating parameters.

RECOMMENDED OPERATING RANGES

Parameter	Symbol	Min	Max	Unit
Module Operating Junction Temperature	T_J	-40	150	$^\circ\text{C}$

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

NXH40B120MNQ1SNG

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Characteristic	Test Conditions	Symbol	Min	Typ	Max	Unit	
BOOST MOSFET CHARACTERISTICS (M11, M21, M31)							
Zero Gate Voltage Drain Current	V _{GS} = 0 V, V _{DS} = 1200 V, T _J = 25°C	I _{DSS}	–	–	200	μA	
Static Drain-to-Source On Resistance	V _{GS} = 20 V, I _D = 40 A, T _J = 25°C	R _{DS(on)}	–	40	55	mΩ	
	V _{GS} = 20 V, I _D = 40 A, T _J = 175°C		–	60	–		
Gate-Source Leakage Current	V _{GS} = 25 V, V _{DS} = 0 V	I _{GSS}	–	–	1.0	μA	
Turn-on Delay Time	T _J = 25°C V _{DS} = 700 V, V _{GS} = -5 V to 20 V I _D = 40 A, R _G = 4.7 Ω	t _{d(on)}	–	17	–	ns	
Rise Time		t _r	–	7.5	–		
Turn-off Delay Time		t _{d(off)}	–	43.8	–		
Fall Time		t _f	–	17	–		
Turn-on Switching Loss per Pulse		E _{on}	–	255	–		μJ
Turn-off Switching Loss per Pulse		E _{off}	–	125.5	–		
Turn-on Delay Time		T _J = 125°C V _{DS} = 700 V, V _{GS} = -5 V to 20 V I _D = 40 A, R _G = 4.7 Ω	t _{d(on)}	–	15.8		–
Rise Time	t _r		–	7	–		
Turn-off Delay Time	t _{d(off)}		–	46.5	–		
Fall Time	t _f		–	15.3	–		
Turn-on Switching Loss per Pulse	E _{on}		–	216	–	μJ	
Turn-off Switching Loss per Pulse	E _{off}		–	108.5	–		
Input Capacitance	V _{DS} = 800 V, V _{GS} = 0 V, f = 1 MHz		C _{ies}	–	3227	–	pF
Output Capacitance		C _{oes}	–	829	–		
Reverse Transfer Capacitance		C _{res}	–	19	–		
Total Gate Charge	V _{DS} = 600 V, I _D = 20 A, V _{GS} = 20 V	Q _g	–	112	–	nC	
Thermal Resistance – chip-to-heat sink	Thermal grease, Thickness = 2 Mil ±2%	R _{thJH}	–	0.88	–	K/W	
Thermal Resistance – chip-to-case	λ = 2.87 W/mK	R _{thJC}	–	0.61	–	K/W	

BOOST DIODE CHARACTERISTICS (D11, D21, D31)

Diode Reverse Leakage Current	V _R = 1200 V	I _R	–	–	400	μA	
Diode Forward Voltage	I _F = 40 A, T _J = 25°C	V _F	1.2	1.57	1.9	V	
	I _F = 40 A, T _J = 175°C		–	–	–		
Reverse Recovery Time	T _J = 25°C V _{DS} = 700 V, V _{GS} = -5 V to 20 V I _D = 40 A, R _G = 4.7 Ω	t _{rr}	–	16.7	–	ns	
Reverse Recovery Charge		Q _{rr}	–	329.6	–	nC	
Peak Reverse Recovery Current		I _{RRM}	–	34.3	–	A	
Peak Rate of Fall of Recovery Current		di/dt	–	6684	–	A/μs	
Reverse Recovery Energy		E _{rr}	–	176.6	–	μJ	
Reverse Recovery Time		T _J = 125°C V _{DS} = 700 V, V _{GS} = -5 V to 20 V I _D = 40 A, R _G = 4.7 Ω	t _{rr}	–	16.9	–	ns
Reverse Recovery Charge			Q _{rr}	–	361	–	nC
Peak Reverse Recovery Current	I _{RRM}		–	37	–	A	
Peak Rate of Fall of Recovery Current	di/dt		–	8067	–	A/μs	
Reverse Recovery Energy	E _{rr}		–	209.1	–	μJ	

NXH40B120MNQ1SNG

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified) (continued)

Characteristic	Test Conditions	Symbol	Min	Typ	Max	Unit
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BOOST DIODE CHARACTERISTICS (D11, D21, D31)

Thermal Resistance – chip-to-heatsink	Thermal grease, Thickness = 2 Mil ±2%	R _{θJH}	–	0.87	–	K/W
Thermal Resistance – chip-to-case	λ = 2.87 W/mK	R _{θJC}	–	0.62	–	K/W

BYPASS DIODE CHARACTERISTICS (D12, D22, D32)

Diode Reverse Leakage Current	V _R = 1200 V, T _J = 25°C	I _R	–	–	250	μA
Diode Forward Voltage	I _F = 50 A, T _J = 25°C	V _F	0.8	1.13	1.3	V
	I _F = 50 A, T _J = 150°C		–	–	–	
Thermal Resistance – chip-to-heatsink	Thermal grease, Thickness = 2 Mil ±2%	R _{θJH}	–	1.05	–	°C/W
Thermal Resistance – chip-to-case	λ = 2.87 W/mK	R _{θJC}	–	0.72	–	°C/W

THERMISTOR CHARACTERISTICS

Nominal resistance		R ₂₅	–	22	–	kΩ
Nominal resistance	T = 100°C	R ₁₀₀	–	1468	–	Ω
Deviation of R25		ΔR/R	–5	–	5	%
Power dissipation		P _D	–	200	–	mW
Power dissipation constant			–	2	–	mW/K
B-value	B(25/50), tolerance ±3%		–	3950	–	K
B-value	B(25/100), tolerance ±3%		–	3998	–	K

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Marking	Package	Shipping
NXH40B120MNQ1SNG	NXH40B120MNQ1SNG	Q1 3-Channel BOOST – Case 180BQ Solder Pins (Pb – Free)	21 Units / Blister Tray

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS – MOSFET, BOOST DIODE AND BYPASS DIODE

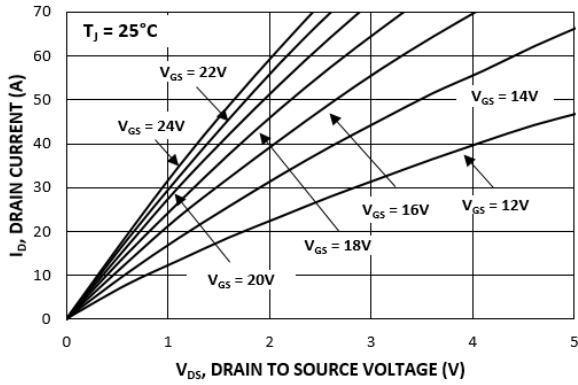


Figure 2. Typical Output Characteristics

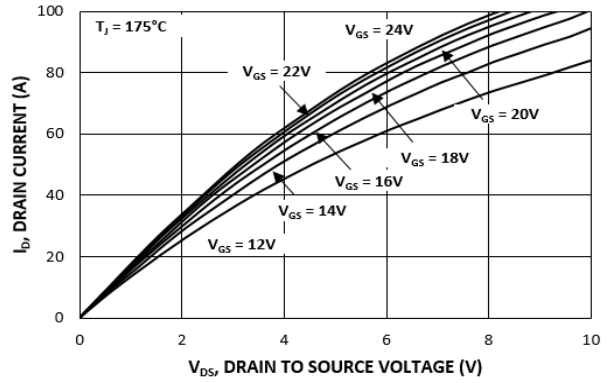


Figure 3. Typical Output Characteristics

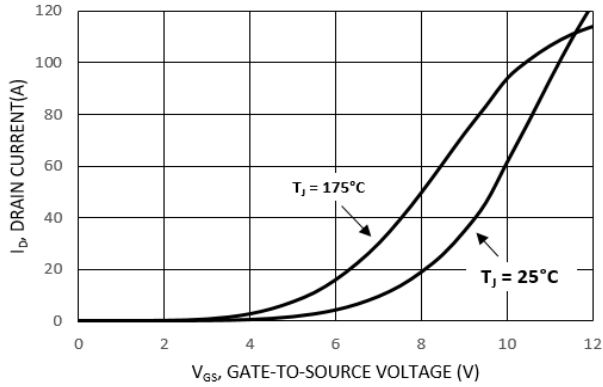


Figure 4. Typical Transfer Characteristics

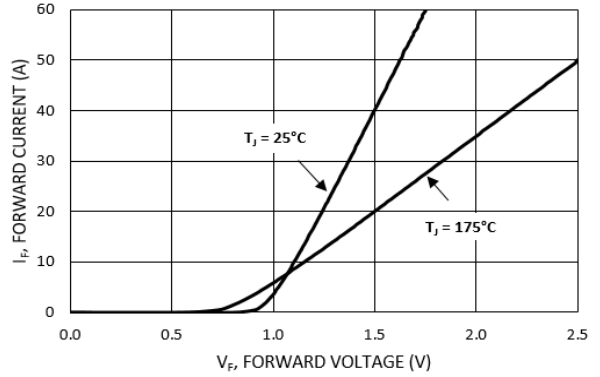


Figure 5. Boost Diode Forward Characteristics

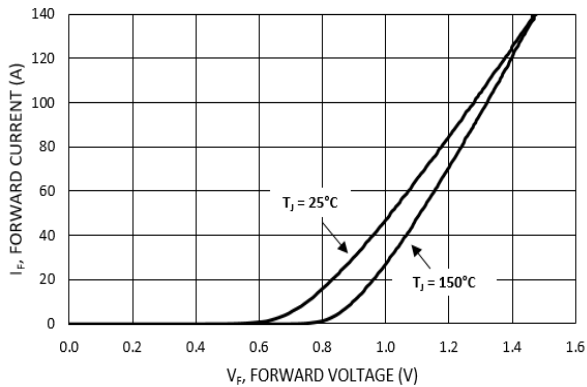


Figure 6. BYPASS Diode Forward Characteristics

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS –MOSFET, BOOST DIODE AND BYPASS DIODE

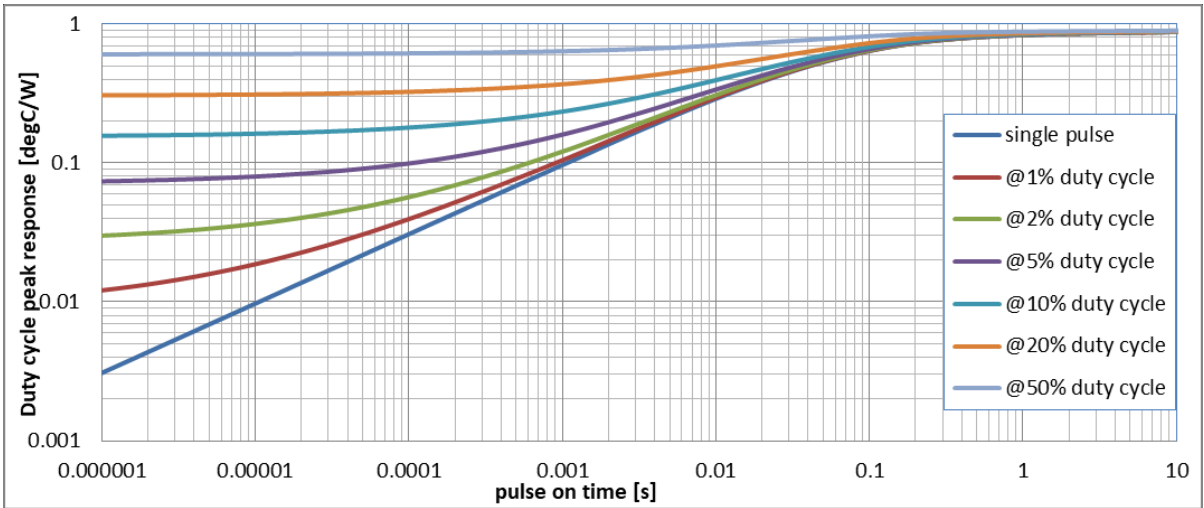


Figure 7. Transient Thermal Impedance (MOSFET)

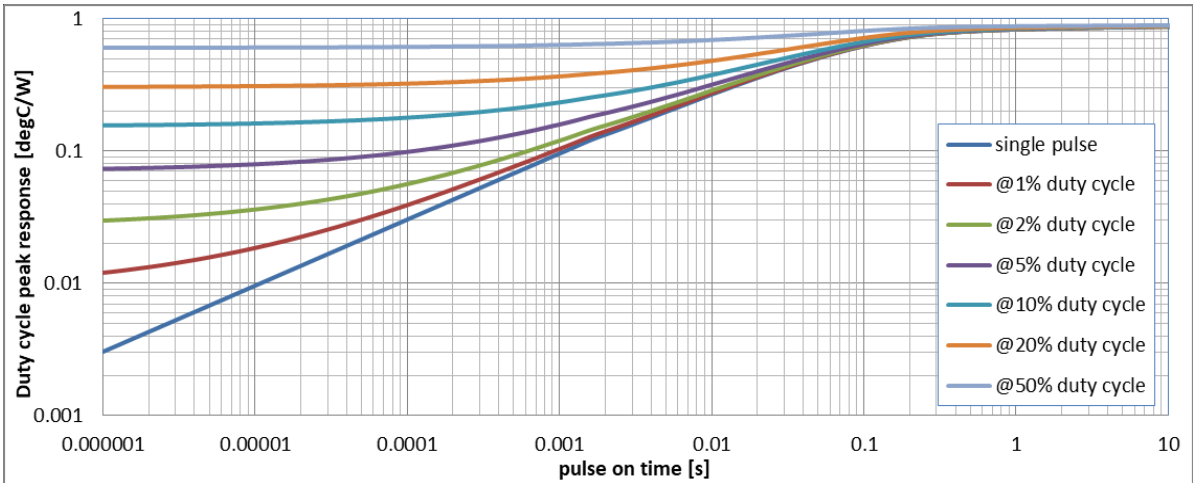


Figure 8. Transient Thermal Impedance (BOOST DIODE)

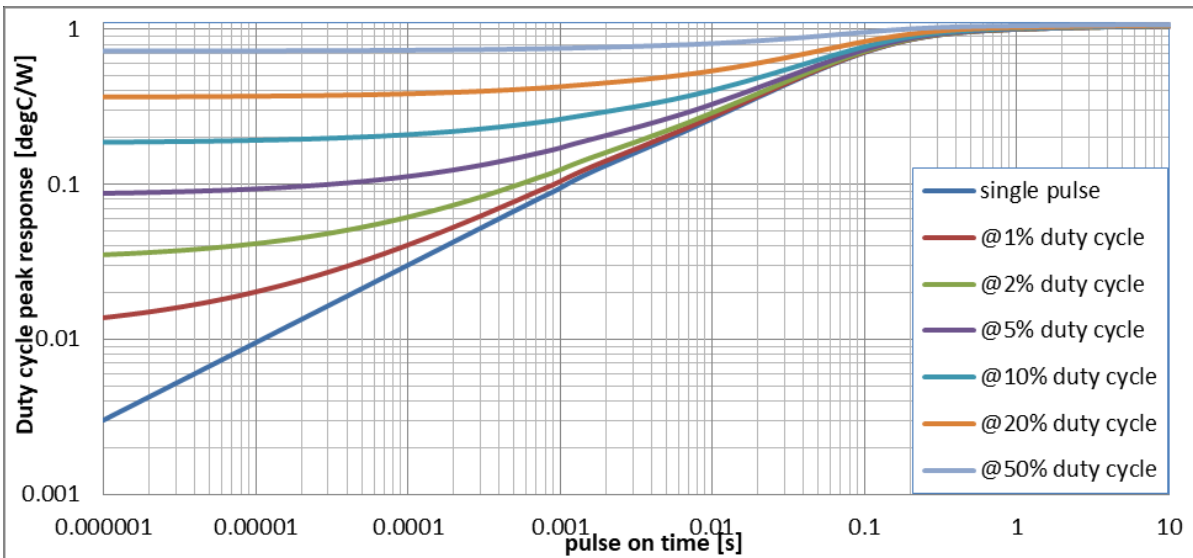


Figure 9. Transient Thermal Impedance (BYPASS DIODE)

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS – MOSFET, BOOST DIODE AND BYPASS DIODE

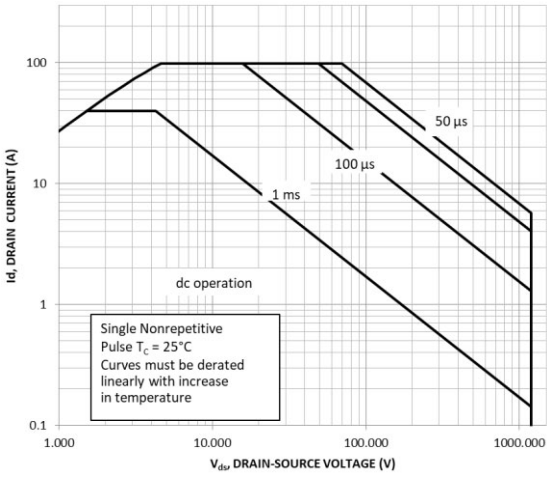


Figure 10. FBSOA

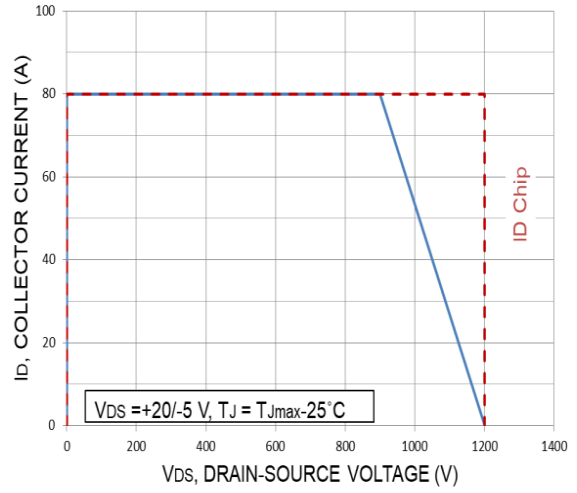


Figure 11. RBSOA

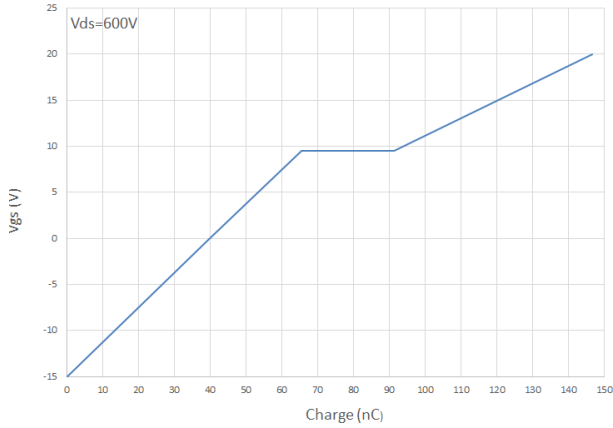


Figure 12. Gate Voltage vs. Gate Charge

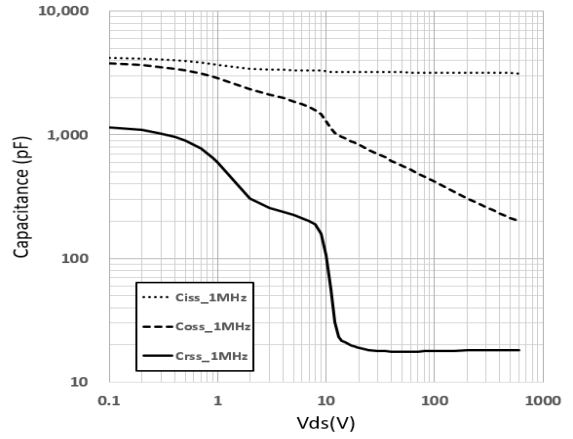


Figure 13. Capacitance Charge

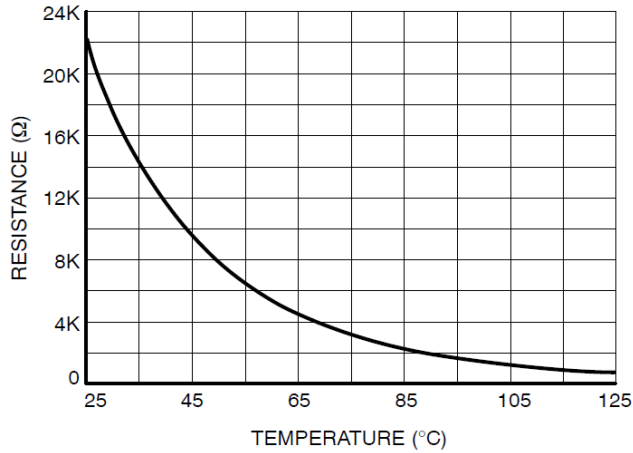


Figure 14. Thermistor Characteristics

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS – MOSFET (M11, M21, M31) AND BOOST DIODE (D11, D21, D31)

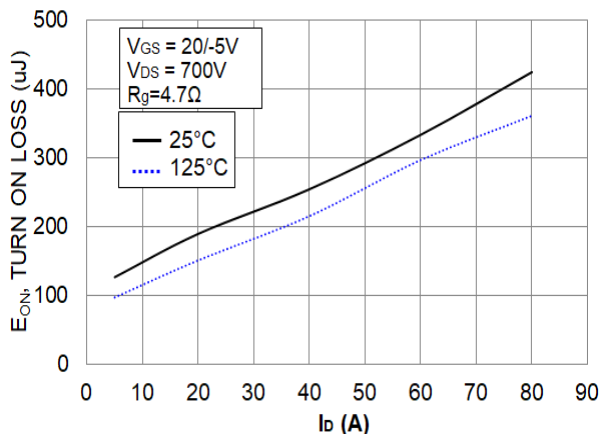


Figure 15. Typical Turn ON Loss vs. I_D

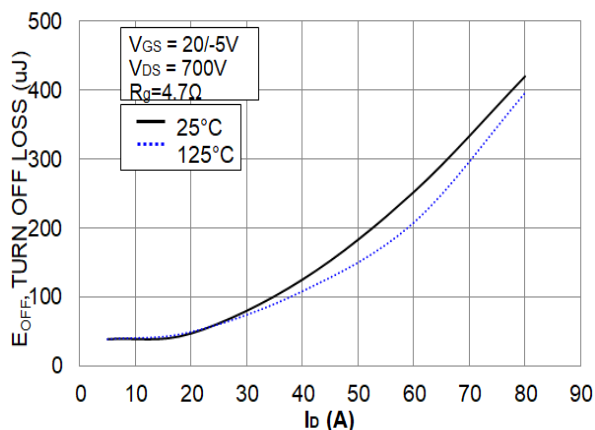


Figure 16. Typical Turn OFF Loss vs. I_D

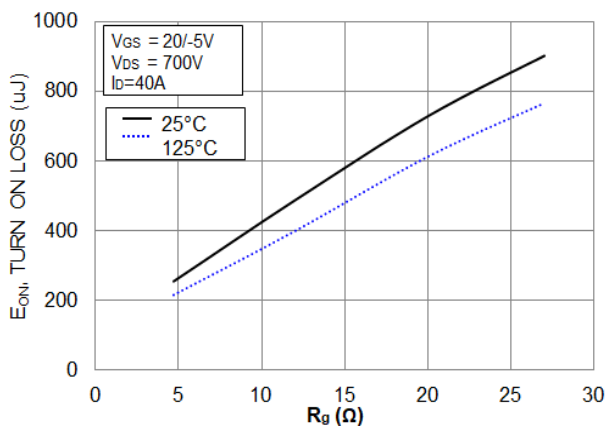


Figure 17. Typical Turn ON Loss vs. R_G

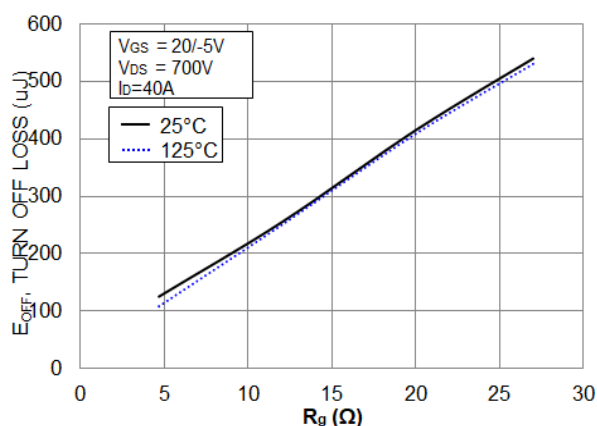


Figure 18. Typical Turn OFF Loss vs. R_G

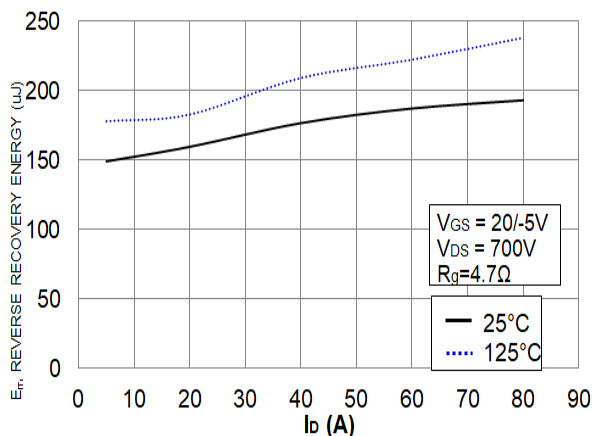


Figure 19. Typical Reverse Recovery Energy Loss vs. I_D

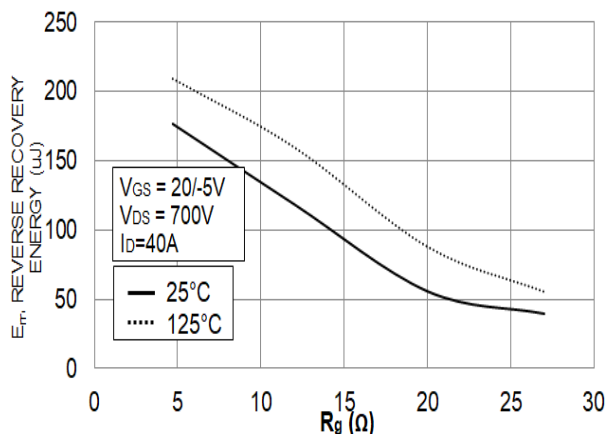


Figure 20. Typical Reverse Recovery Energy Loss vs. R_G

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS – MOSFET (M11, M21 ,M31) AND BOOST DIODE (D11, D21, D31) (continued)

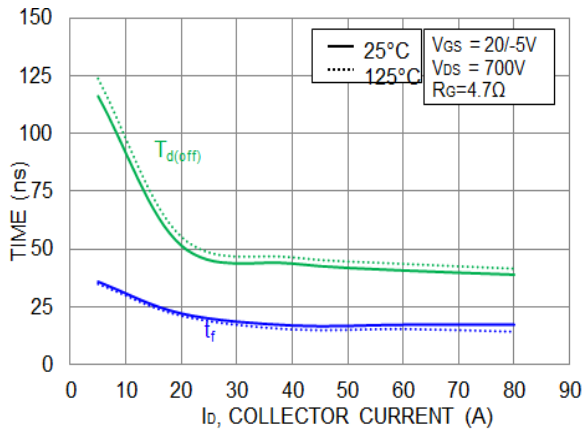


Figure 21. Typical Turn-Off Switching Time vs. I_D

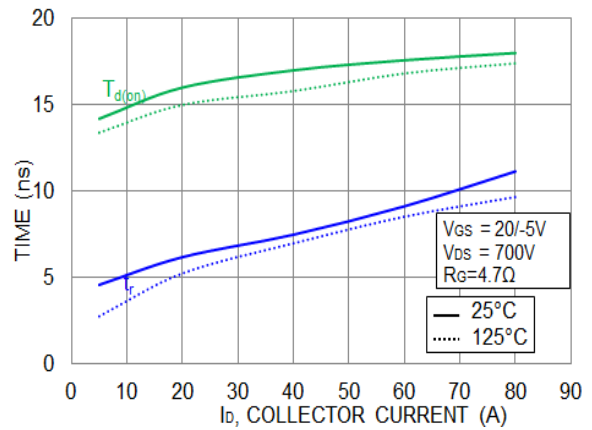


Figure 22. Typical Turn-On Switching Time vs. I_D

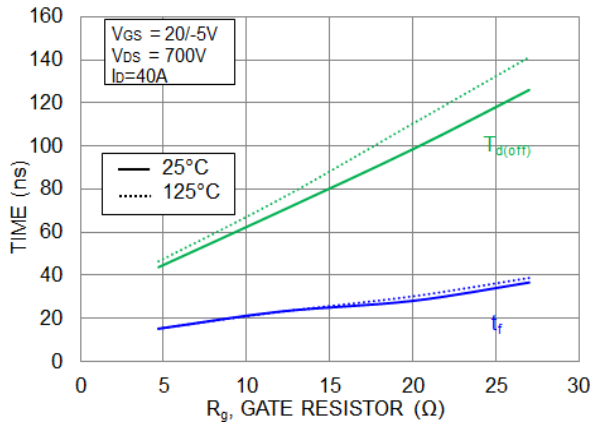


Figure 23. Typical Turn-Off Switching Time vs. R_G

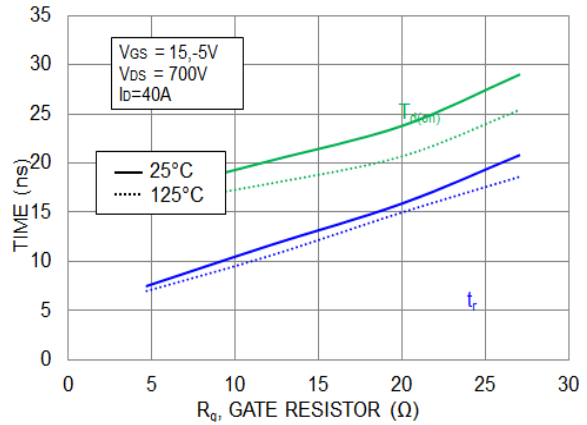


Figure 24. Typical Turn-On Switching Time vs. R_G

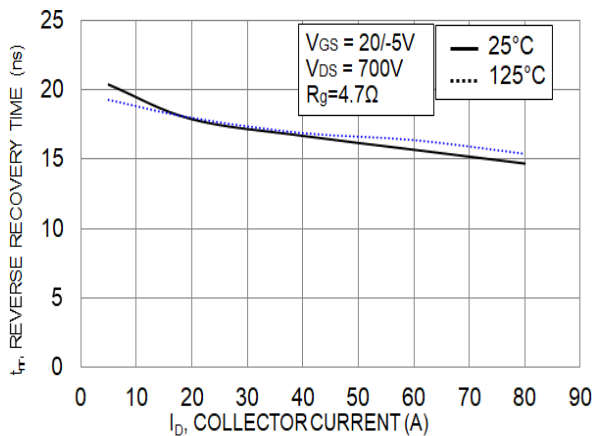


Figure 25. Inverse Diode Typical Reverse Recovery Time vs. I_D

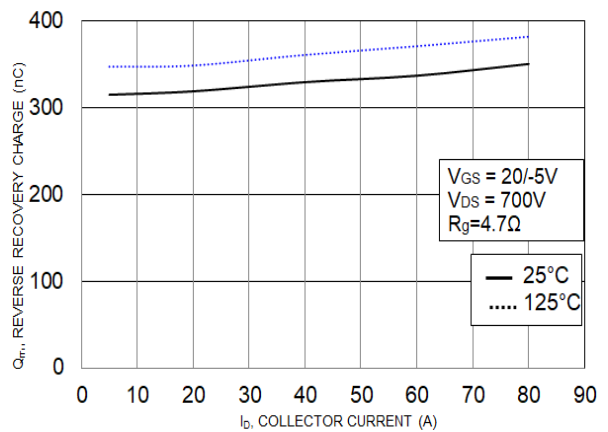


Figure 26. Typical Reverse Recovery Charge vs. I_D

NXH40B120MNQ1SNG

TYPICAL CHARACTERISTICS – MOSFET (M11, M21, M31) AND BOOST DIODE (D11, D21, D31) (continued)

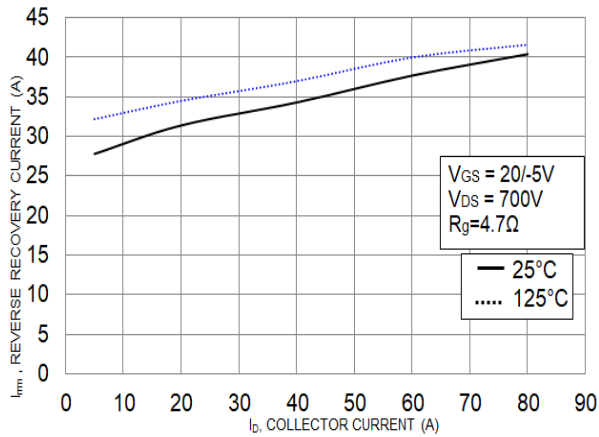


Figure 27. Inverse Diode Typical Reverse Recovery Current vs. I_D

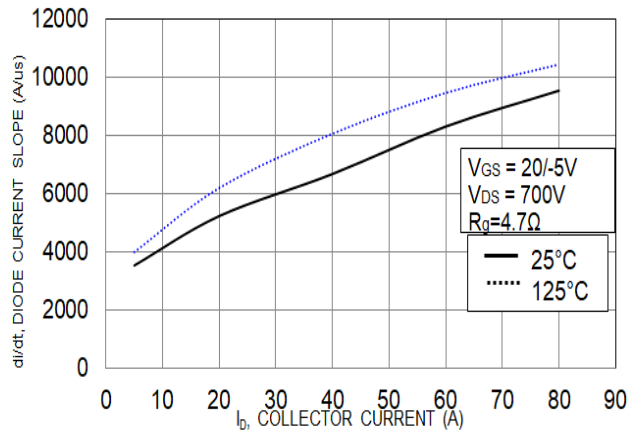


Figure 28. Typical di/dt Current Slope versus I_D

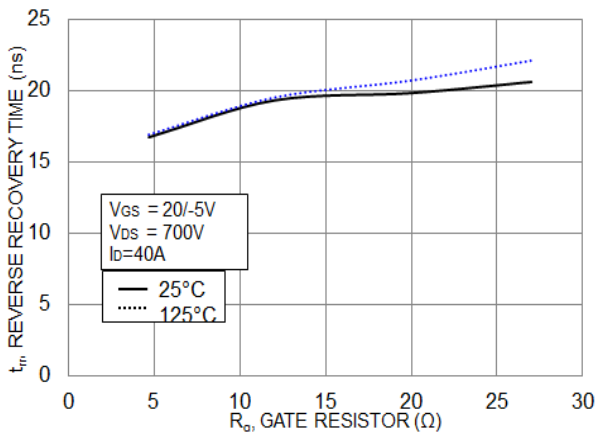


Figure 29. Typical Reverse Recovery Time vs. R_G

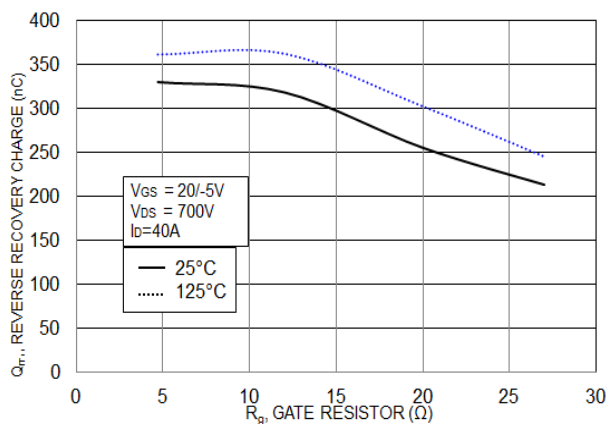


Figure 30. Inverse Diode Typical Reverse Recovery Charge vs. R_G

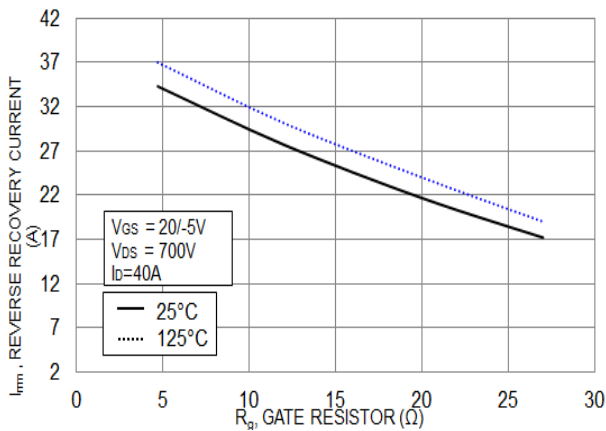


Figure 31. Typical Reverse Recovery Peak Current versus R_G

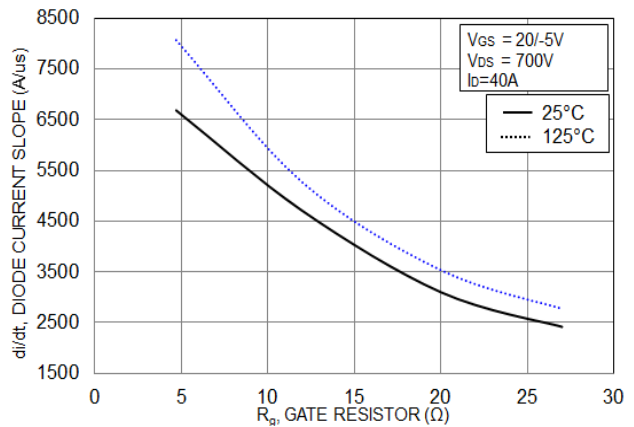


Figure 32. Inverse Diode Typical di/dt vs. R_G

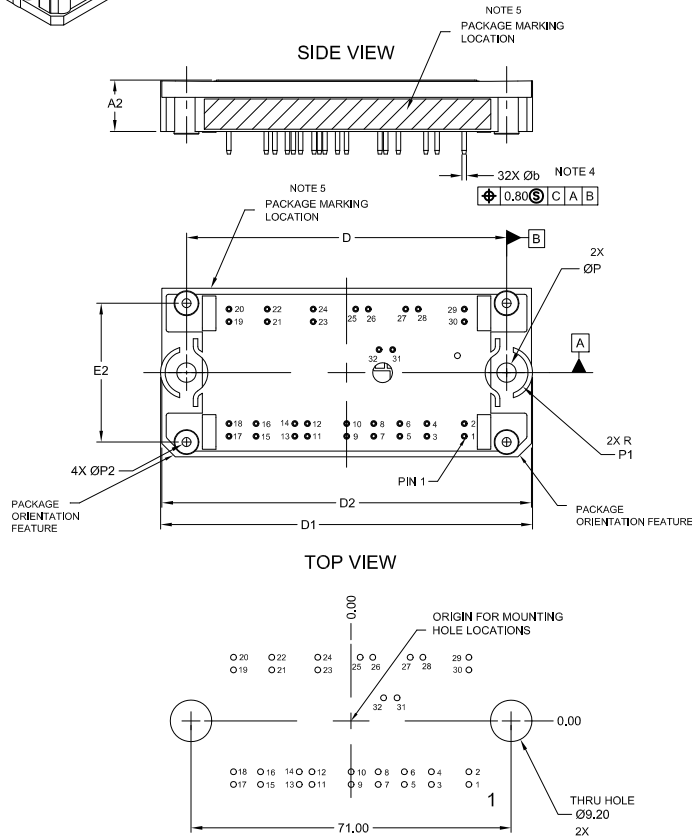
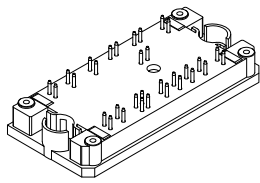
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



PIM32, 71x37.4 (SOLDER PIN) CASE 180BQ ISSUE A

DATE 23 JUL 2021



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	11.70	12.00	12.30
A2	10.90	11.40	11.90
A3	15.90	16.40	16.90
A5	0.00	-	0.45
b	0.90	1.00	1.10
D	70.50	71.00	71.50
D1	82.00	82.50	83.00
D2	81.50	82.00	82.50
E	36.90	37.40	37.90
E2	30.30	30.80	31.30
P	4.30	4.40	4.50
P1	4.55	4.75	4.95
P2	2.00 REF		

NOTE 4

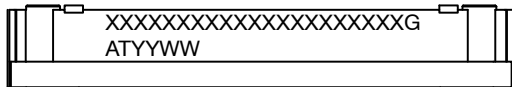
PIN	PIN POSITION		PIN	PIN POSITION	
	X	Y		X	Y
1	26.10	-14.10	17	-26.10	-14.10
2	26.10	-11.30	18	-26.10	-11.30
3	17.80	-14.10	19	-26.10	11.30
4	17.80	-11.30	20	-26.10	14.10
5	11.80	-14.10	21	-17.60	11.30
6	11.80	-11.30	22	-17.60	14.10
7	6.00	-14.10	23	-7.40	11.30
8	6.00	-11.30	24	-7.40	14.10
9	0.00	-14.10	25	2.00	14.10
10	0.00	-11.30	26	4.80	14.10
11	-8.70	-14.10	27	13.10	14.10
12	-8.70	-11.30	28	15.90	14.10
13	-11.50	-14.10	29	26.10	14.10
14	-11.50	-11.30	30	26.10	11.30
15	-20.10	-14.10	31	10.20	5.10
16	-20.10	-11.30	32	7.20	5.10

NOTES:

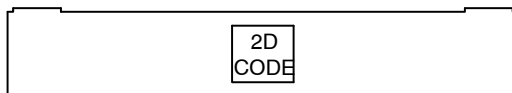
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- CONTROLLING DIMENSION; MILLIMETERS
- DIMENSION *b* APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 1.00 AND 3.00 FROM THE TERMINAL TIP.
- POSITION OF THE CENTER OF THE TERMINALS AND MOUNTING HOLES IS DETERMINED FROM DATUM B THE CENTER OF DIMENSION D, X DIRECTION, AND FROM DATUM A, Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED IN DRAWING, APPLIES TO BOTH TERMINALS AND MOUNTING HOLES IN BOTH DIRECTIONS.
- PACKAGE MARKING IS LOCATED, AS SHOWN, ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.
- MOUNTING RECOMMENDATION IS SHOWN AS VIEWED FROM THE PCB TOP LAYER LOOKING DOWN TO SUBSEQUENT LAYERS.

RECOMMENDED MOUNTING PATTERN*
For additional information on our Pb-Free strategy and soldering details, please download the On Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



FRONTSIDE MARKING



BACKSIDE MARKING

XXXXX = Specific Device Code
G = Pb-Free Package
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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