IHHP-1008AB-01

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Vishay Dale

Low Profile, High Current Inductors



STANDARD ELECTRICAL SPECIFICATIONS								
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC (A) ⁽³⁾		SATURATION CURRENT DC (A) ⁽⁴⁾			
0.23 ξ , 0 Α (μΗ)	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.		
0.22	8.4	10.5	7.40	6.60	7.10	6.50		
0.24	11	13	6.40	5.70	6.60	6.00		
0.33	14	17	5.60	5.00	6.10	5.50		
0.47	21	26	4.50	4.00	5.05	4.55		
1.0	40	48	3.10	2.70	3.90	3.50		
1.5	57	69	2.70	2.43	3.10	2.80		
2.2	79	95	2.30	2.00	2.60	2.30		

Notes

- ⁽¹⁾ All test data is referenced to 25 °C ambient.
- $^{(2)}$ Operating temperature range -55 °C to +125 °C .
- ⁽³⁾ DC current (A) that will cause an approximate ΔT of 40 °C.
- ⁽⁴⁾ DC current (A) that will cause L_0 to drop approximately 30 %.
- The part temperature (ambient + temp. rise) should not exceed (5) 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

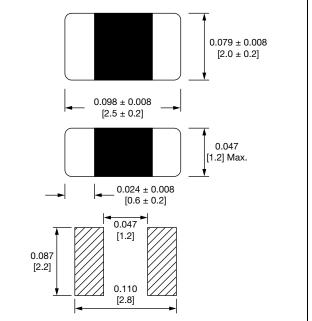
FEATURES

- · Magnetic alloy power choke coil
- Miniature size (2.5 x 2.0) and low profile
- Magnetic shielded
- Low acoustic noise and high efficiency
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

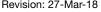
APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- · Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)





DESCRIPTION	N N			
IHHP-1008AB-01	1.0 µH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC [®] LEAD (Pb)-FREE STANDARD
GLOBAL PAR	T NUMBER			
ІНН	P 1 0	0 8 A B	E R 1	R 0 M 0 1
PRODUCT FAN	AILY	SIZE	PACKAGE CODE	INDUCTANCE TOL. SERIES VALUE
Revision: 27-Mar-18		1		Document Number: 343



RoHS COMPLIANT HALOGEN FREE

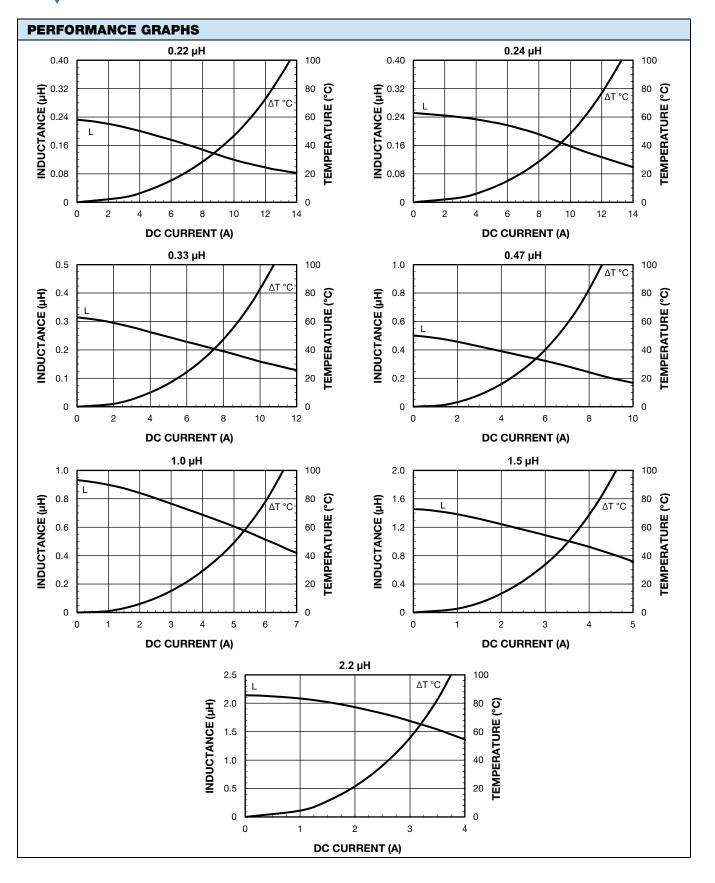
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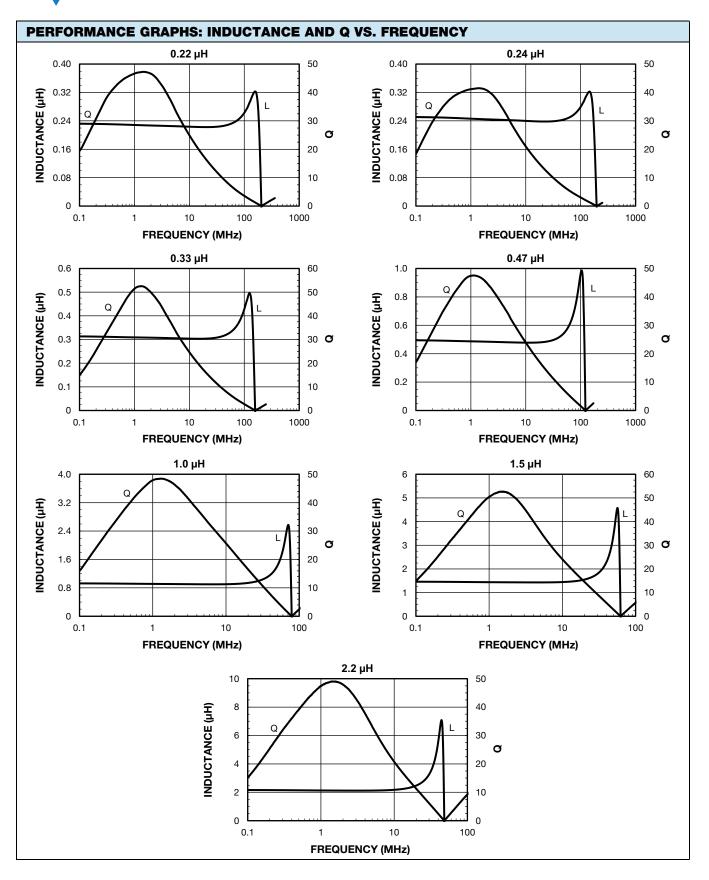
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