Step-up DC/DC Converter with Shutdown Function

No. EA-284-180511

OUTLINE

The R1204x is a low supply current PWM step-up DC/DC converter. Internally, a single IC consists of an NMOS FET, an oscillator, a PWM comparator, a voltage reference unit, an error amplifier, a current limit circuit, an under voltage lockout circuit (UVLO), an over-voltage protection circuit (OVP), a soft-start circuit, a maximum duty cycle limit circuit, and a thermal shutdown protection circuit. By simply using an inductor, a resistor, capacitors and a diode as external components, a high-efficiency step-up DC/DC converter can be easily configured. As protection functions, the IC contains a thermal shutdown protection circuit, a current limit circuit, an OVP circuit, and an UVLO circuit. A thermal shutdown circuit detects overheating of the ICs and stops the operation to protect it from damage. A current limit circuit limits the peak current of Lx, and an OVP circuit detects the over voltage of output, and an UVLO circuit detects the low input voltage.
The R1204x provides the R1204xxxA/D/G/H versions, which are optimized for serial driving of white LEDs with constant current, and the R1204xxxB/C/E/F versions, which are optimized for constant voltage driving. Among the R1204xxxB/C/E/F versions, only the R1204xxxC/F versions are equipped with PWM/VFM auto-switching controls. The LED current can be determined by the value of current setting resistor. The brightness of the LEDs can be quickly adjusted by applying a PWM signal (200 Hz to 300 kHz) to the CE pin. The R1204x is available in DFN(PLP)1820-6 and TSOT-23-6 packages.

FEATURES

- Input Voltage Range.. 2.3 V to 5.5 V
- Supply Current .... Typ. 800 µA
- Standby Current .... Max. 5 µA
- Feedback Voltage .... 0.2 V ±10 mV (R1204xxxxA/D)
  0.4 V ±10 mV (R1204xxxxG/H)
- V ±15 mV (R1204xxxxB/C/E/F)
- Lx Current Limit Function.................................. Min. 700 mA
- Over Voltage Protection.................................... 23 V, 33 V, 42 V (±1.5 V)
- Oscillator Frequency .... Typ. 1.0 MHz (R1204xxxxA/B/C/G)
  Typ. 750 kHz (R1204xxxxD/E/F/H)
- Maximum Duty Cycle .... Min. 91% (R1204xxxxA/B/C/G)
  Min. 92% (R1204xxxxD/E/F/H)
- FET ON Resistance .... Typ. 0.8 Ω
- UVLO Function
- Thermal Protection Function
- LED Dimming Control for R1204xxxxA/D .... by external PWM signal (200 Hz to 300 kHz frequency)
- Packages .... DFN(PLP)1820-6, TSOT-23-6
- Recommended Bypass Capacitor .... 1.0 µF

APPLICATIONS

- Constant voltage power source for hand-held equipment
- OLED power supply for hand-held equipment
- White LED driver for hand-held equipment

SELECTION GUIDE

The package type, the OVP detector threshold, the feedback voltage and the PWM//VFM auto-switching control are user- selectable options as described below.

Selection Guide

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1204Kxy2z-TR	DFN(PLP)1820-6	5,000 pcs	Yes	Yes
R1204Nxy3z-TR-FE	TSOT-23-6	3,000 pcs	Yes	Yes

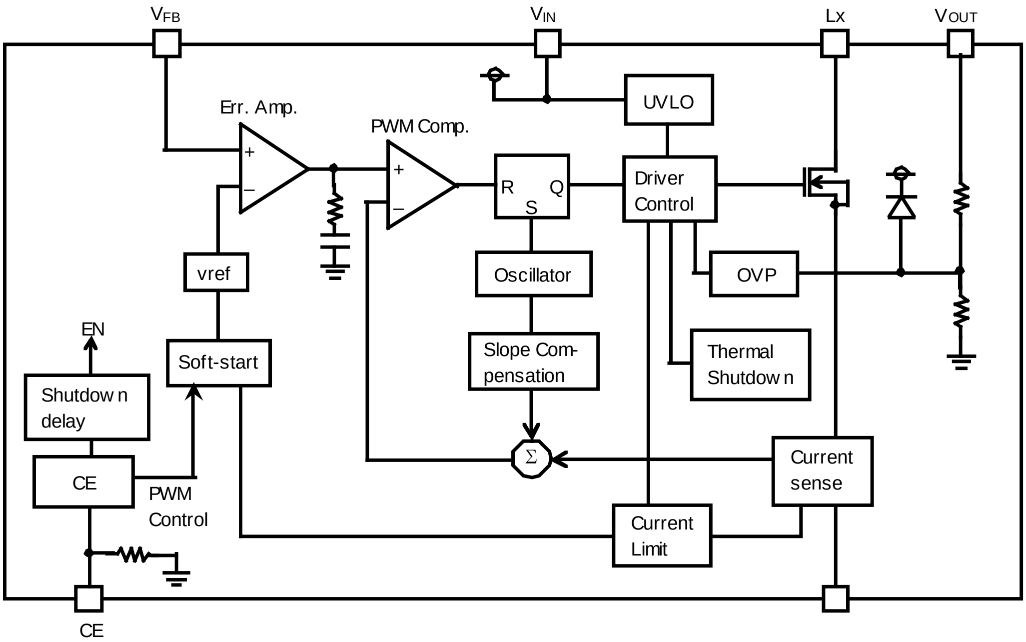
x: OVP Detector Threshold
1: 23 V
2: 33 V
3: 42 V

y: Current Limit
1: Typ. 900 mA

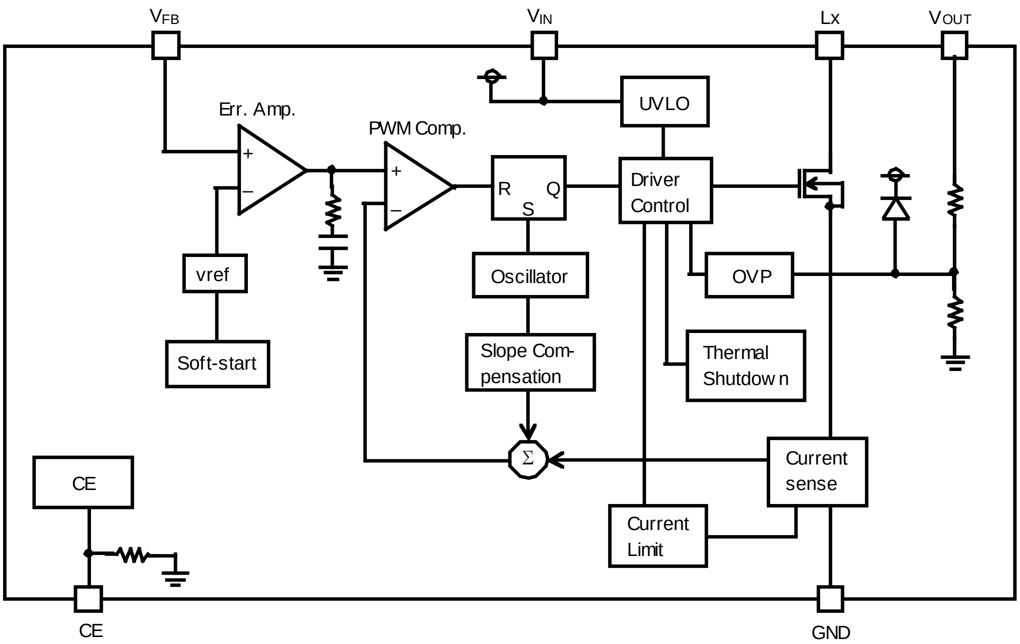
z: Feedback Voltage, Oscillator Frequency, PWM/VFM Auto-Switching Control

z	Feedback Voltage	Oscillator Frequency	PWM/VFM Auto-Switching Control
A	Typ. 0.2 V	Typ. 1 MHz	No
B	Typ. 1 V		No
C	Typ. 1 V		Yes
D	Typ. 0.2 V	Typ. 750 kHz	No
E	Typ. 1 V		No
F	Typ. 1 V		Yes
G	Typ. 0.4 V	Typ. 1 MHz	No
H		Typ. 750 kHz	

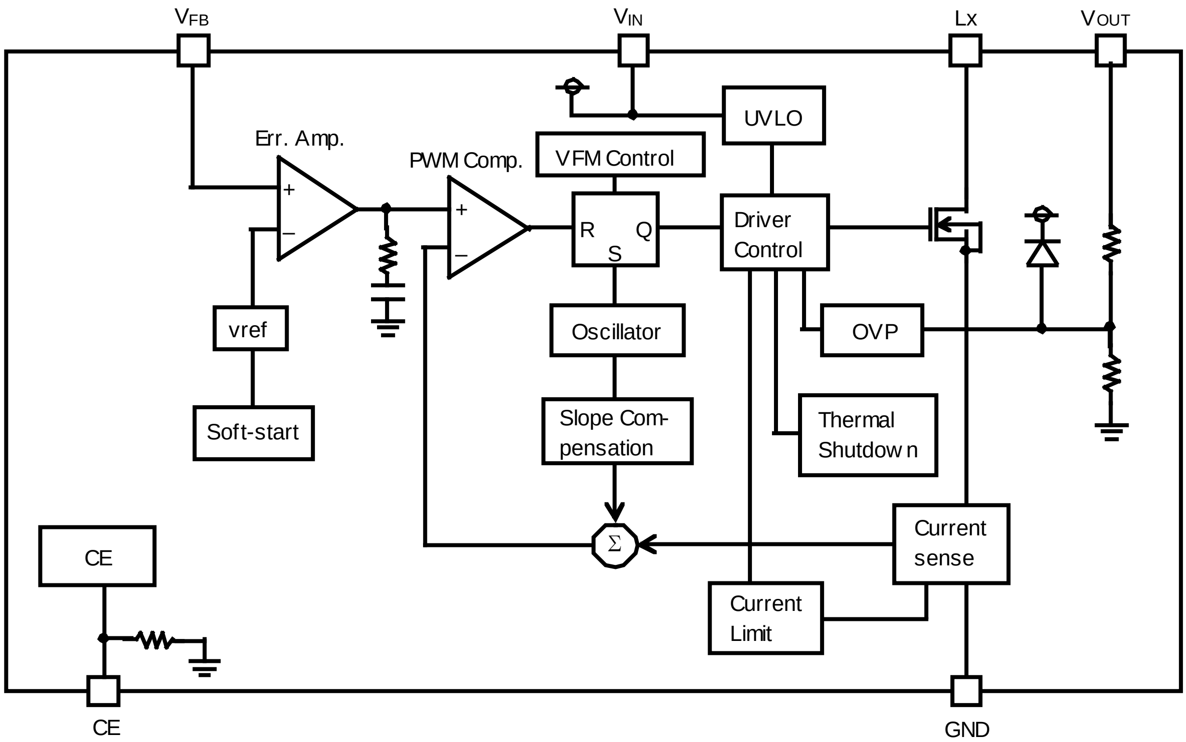
BLOCK DIAGRAMS



R1204xxxxA/D/G/H Block Diagram

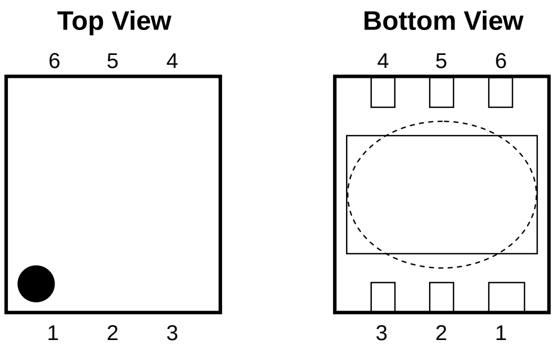


R1204xxxxB/E Block Diagram

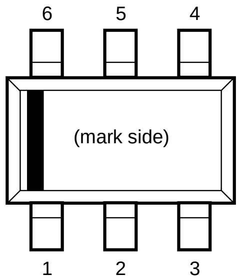


R1204xxxxC/F Block Diagram

PIN DESCRIPTIONS



DFN(PLP)1820-6 Pin Configuration



TSOT-23-6 Pin Configuration

DFN(PLP)1820-6 Pin Description

Pin No	Symbol	Description
1	V_{OUT}	Output Pin
2	L_X	Switching Pin, Open Drain Output
3	GND	Ground Pin
4	V_{IN}	Input Pin
5	CE	Chip Enable Pin, Active-high
6	V_{FB}	Feedback Pin

The exposed tab is substrate level (GND). It is recommended that the exposed tab be connected to the ground plane on the board or otherwise be left open.

TSOT-23-6 Pin Description

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1	L_X	Switching Pin, Open Drain Output
2	GND	Ground Pin
3	V_{FB}	Feedback Pin
4	CE	Chip Enable Pin, Active-high
5	V_{OUT}	Output Pin
6	V_{IN}	Input Pin

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings **(GND = 0 V)**

Symbol	Parameter			Rating	Unit
V_{IN}	V_{IN} Pin Voltage			−0.3 to 6.5	V
V_{CE}	CE Pin Voltage			−0.3 to 6.5	V
V_{FB}	V_{FB} Pin Voltage			−0.3 to 6.5	V
V_{OUT}	V_{OUT} Pin Voltage			−0.3 to 48	V
V_{LX}	L_X Pin Voltage			−0.3 to 48	V
I_{LX}	L_X Pin Current			1200	mA
P_D	Power Dissipation[1]	DFN(PLP)1820-6	JEDEC STD. 51-7	2200	mW
		TSOT-23-6	Standard Test Land Pattern	460	
Tj	Junction Temperature Range			−40 to 125	°C
Tstg	Storage Temperature Range			−55 to 125	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

Symbol	Parameter	Rating	Unit
V_{IN}	Input Voltage	2.3 to 5.5	V
Ta	Operating Temperature Range	−40 to 85	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such ratings by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

[1] Refer to *POWER DISSIPATION* for detailed information.

ELECTRICAL CHARACTERISTICS

R1204xxxxx Electrical Characteristics **(Ta = 25ºC)**

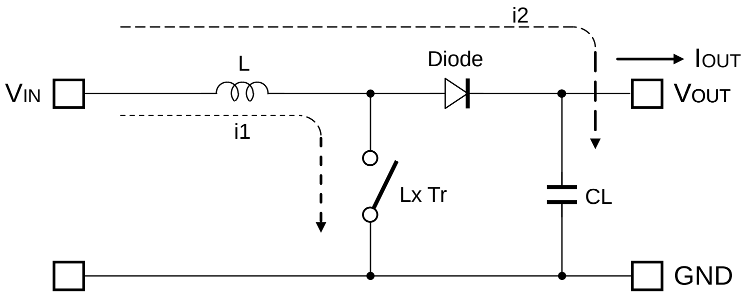
Symbol	Parameter	Test Conditions/Comments		Min.	Typ.	Max.	Unit
I_{DD}	Supply Current	V_{IN} = 5.5 V, V_{FB} = 0 V, L_X at no load			0.8		mA
Istandby	Standby Current	V_{IN} = 5.5 V, V_{CE} = 0 V			1.0	5.0	μA
V_{UVLO1}	UVLO Detector Threshold	V_{IN} falling		1.9	2.0	2.1	V
V_{UVLO2}	UVLO Released Voltage	V_{IN} rising			V_{UVLO1} +0.1		V
V_{CEH}	CE Input Voltage "H"	V_{IN} = 5.5 V		1.5			V
V_{CEL}	CE Input Voltage "L"	V_{IN} = 2.3 V				0.5	V
R_{CE}	CE Pull Down Resistance	V_{IN} = 3.6 V	R1204xxxxA/B/D/E/G/H		1200		kΩ
			R1204xxxxC/F		600		
V_{FB}	V_{FB} Voltage Accuracy	V_{IN} = 3.6 V	R1204xxxxA/D	0.19	0.2	0.21	V
			R1204xxxxG/H	0.39	0.4	0.41	
			R1204xxxxB/C/E/F	0.985	1.000	1.015	
I_{FB}	V_{FB} Input Current	V_{IN} = 5.5 V, V_{FB} = 0 V or 5.5 V		−0.1		0.1	μA
Tstart	Soft-start Time	V_{IN} = 3.6 V, R1204xxxxB/C/E/F			10		ms
R_{ON}	FET ON Resistance	V_{IN} = 3.6 V, I_{LX} = 100 mA			0.8		Ω
I_{LXLEAK}	FET Leakage Current	V_{LX} = 40 V				3.0	μA
I_{LXLIM}	FET Current Limit	V_{IN} = 3.6 V		700	900	1100	mA
fosc	Oscillator Frequency	V_{IN} = 3.6 V, V_{FB} = 0 V	R1204xxxxA/B/C/G	0.9	1.0	1.1	MHz
			R1204xxxxD/E/F/H	675	750	825	kHz
Maxduty	Oscillator Maximum Duty Cycle	V_{IN} = 3.6 V, V_{FB} = 0 V	R1204xxxxA/B/C/G	91			%
			R1204xxxxD/E/F/H	92			%
V_{OVP1}	OVP Detector Threshold	V_{IN} = 3.6 V, V_{OUT} rising	R1204x1xxx	22	23	24.0	V
			R1204x2xxx	31.5	33	34.5	
			R1204x3xxx	40.2	42	43.8	
V_{OVP2}	OVP Released Voltage	V_{IN} = 3.6 V, V_{OUT} falling	R1204x1xxx		V_{OVP1} −0.6		V
			R1204x2xxx		V_{OVP1} −1.2		
			R1204x3xxx		V_{OVP1} −2.4		
T_{TSD}	Thermal Shutdown Temperature	V_{IN} = 3.6 V			150		°C
T_{TSR}	Thermal Shutdown Released Temperature	V_{IN} = 3.6 V			100		°C

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj ≈ Ta = 25ºC).

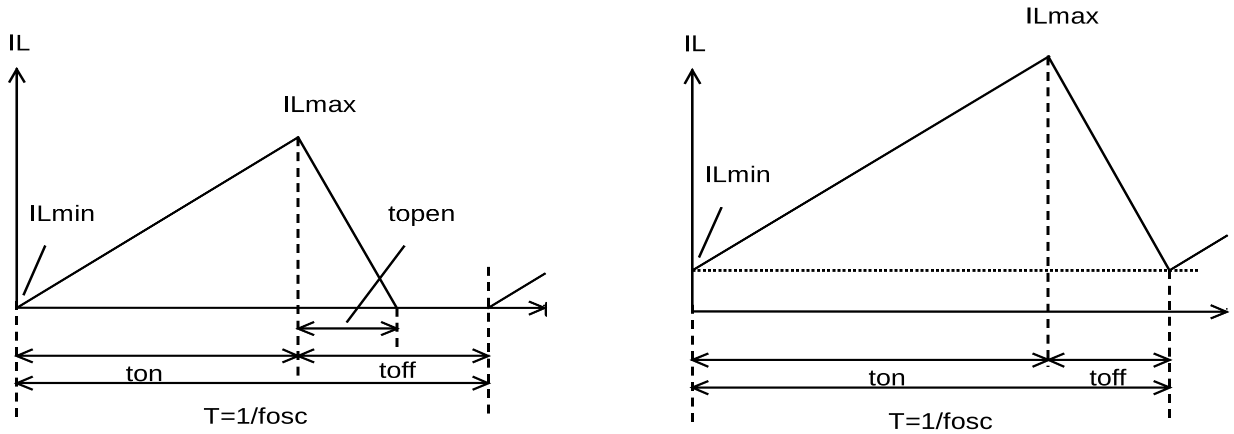
THEORY OF OPERATION

Operation of Step-Up DC/DC Converter and Output Current

<Basic Circuit>



<Current through L>



There are two operation modes of the step-up PWM control-DC/DC converter. That is the continuous mode and discontinuous mode by the continuousness inductor.

When the transistor turns ON, the voltage of inductor L becomes equal to VIN voltage. The increase value of inductor current (i1) will be

$\Delta i1 = V_{IN} \times ton / L$ .... Formula 1

As the step-up circuit, during the OFF time (when the transistor turns OFF) the voltage is continually supply from the power supply. The decrease value of inductor current (i2) will be

$\Delta i2 = (V_{OUT} - V_{IN}) \times topen / L$ .... Formula 2

At the PWM control-method, the inductor current become continuously when topen = toff, the DC/DC converter operate as the continuous mode.

In the continuous mode, the variation of current of i1 and i2 is same at regular condition.

$V_{IN} \times ton / L = (V_{OUT} - V_{IN}) \times toff / L$ .... Formula 3

The duty at continuous mode will be

$duty (\%) = ton / (ton + toff) = (V_{OUT} - V_{IN}) / V_{OUT}$ .... Formula 4

The average of inductor current at tf = toff will be

$IL (Ave.) = V_{IN} \times ton / (2 \times L)$ .... Formula 5

If the input voltage = output voltage, the I_{OUT} will be

$I_{OUT} = V_{IN2} \times ton / (2 \times L \times V_{OUT})$ .... Formula 6

If the I_{OUT} value is large than above the calculated value (Formula 6), it will become the continuous mode, at this status, the peak current (I_{LMAX}) of inductor will be

$I_{LMAX} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times ton / (2 \times L)$ .... Formula 7

$I_{LMAX} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times T \times (V_{OUT} - V_{IN}) / (2 \times L \times V_{OUT})$ .... Formula 8

The peak current value is larger than the I_{OUT} value. In case of this, selecting the condition of the input and the output and the external components by considering of I_{LMAX} value.
The explanation above is based on the ideal calculation, and the loss caused by LX switch and the external components are not included.
The actual maximum output current will be between 50% and 80% by the above calculations. Especially, when the IL is large or V_{IN} is low, the loss of V_{IN} is generated with on resistance of the switch. Moreover, it is necessary to consider Vf of the diode (approximately 0.8 V) about V_{OUT}.

PWM/VFM Auto-Switching Control (R1204xxxxC/F)

In low output current, the IC automatically switches to high-efficiency VFM mode. The minimum Onduty (D_{ON_MIN}) of VFM mode is set to approximately 30% and is fixed inside the IC. If the difference between the voltages of the input and the output is small, or the Onduty in continuous mode (D_{ON_CON}) becomes lower than D_{ON_MIN}, the IC will not shift to PWM mode but will stay with VFM mode instead even in high output current, as a result, the ripple current will be increased. D_{ON_MIN} should be 70% or more (V_{SET} > V_{IN} x 3.33).

Soft-Start Function

(R1204xxxxA/D/G/H)

Unless otherwise V_{OUT} is beyond the threshold (Vf x number of LED lights), current will not flow through LEDs, as a result, V_{FB} voltage will not increase. The IC increases V_{OUT} by controlling the output of error amplifier to “H” and turning the L_X switch on and off for a certain period of time (until the current flow). At the mean time, the inrush current is controlled by gradually increasing the current limit. If V_{OUT} is over the threshold (the current flows), the IC controls the soft-start function by gradually increasing the reference voltage of error amplifier.

(R1204xxxxB/C/E/F)

The IC controls the soft-start function by gradually increasing the reference voltage of error amplifier. Soft-start begins when the output voltage of error amplifier is 0V and ends when it reaches the constant voltage.

Current Limit Function

If the peak current of inductor (I_{LMAX}) exceeds the current limit, current limit function turns the driver off and turns it on in every switching cycle to continually monitor the driver current.

Under Voltage Lock Out Function (UVLO)

UVLO function stops DC/DC operation and prevents malfunction when the supply voltage falls below the UVLO detector threshold.

Over Voltage Protection Circuit (OVP)

OVP circuit monitors the V_{OUT} pin voltage and if it reaches the OVP voltage it will stop oscillation. When the V_{OUT} pin voltage decreases it will restart oscillation, but if the cause of the excess V_{OUT} pin voltage is not removed the OVP circuit will operate repeatedly so as to restrict the V_{OUT} pin voltage.

Thermal Shutdown Function

If the junction temperature exceeds the thermal shutdown temperature, thermal shutdown function turns the driver off. If the junction temperature becomes lower than the thermal shutdown released temperature, the thermal shutdown function resets the IC to restart the operation.

APPLICATION INFORMATION

R1204xxxxA/D/G/H Typical Applications

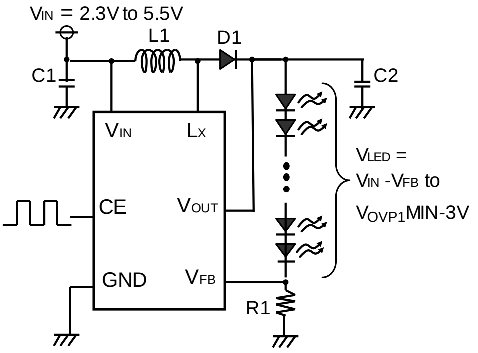


Figure 1.

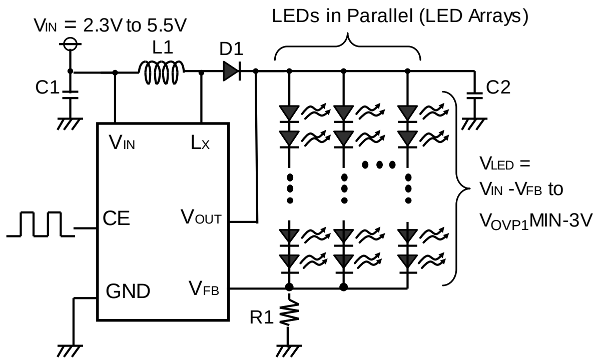


Figure 2.

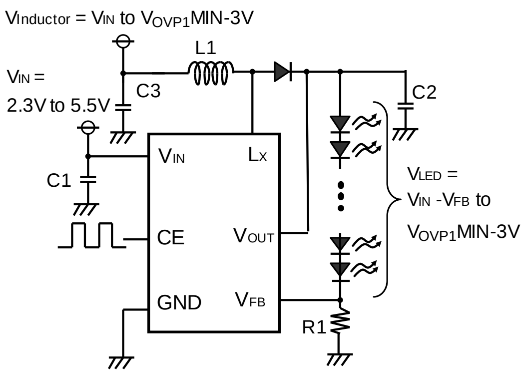


Figure 3.

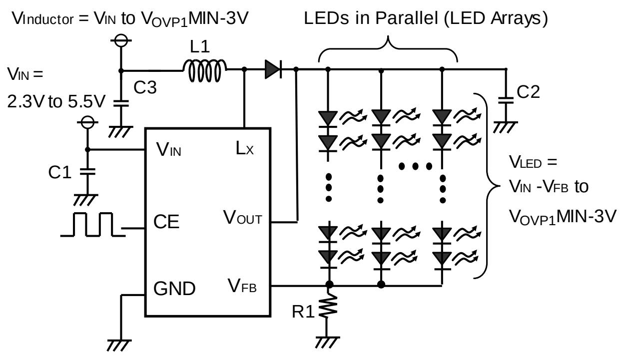


Figure 4.

R1204xxxxB/C/E/F Typical Applications

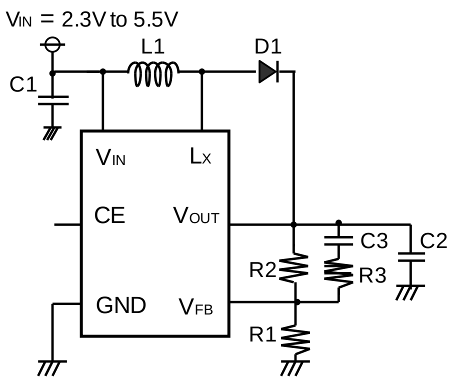


Figure 5.

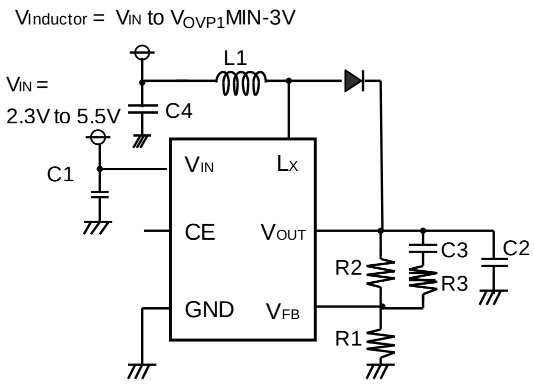


Figure 6.

RICOH

Selection of Inductor

Peak current of inductor (I_{LMAX}) in normal mode when the efficiency is 80% can be calculated by the following formula.

$I_{LMAX} = 1.25 \times I_{OUT} \times V_{OUT} / V_{IN} + 0.5 \times V_{IN} \times (V_{OUT} - V_{IN}) / (L1 \times V_{OUT} \times fosc)$

- When starting up the IC or when adjusting the brightness of LEDs, a large transient current may flow into an inductor (L1).
- I_{LMAX} should be equal or smaller than the current limit of the IC.
- When deciding the rated current of inductor, I_{LMAX} should be considered.
- It is recommended that L1 with 10 µH to 22 µH be used.

Table 1. Peak Current Values for V_{IN}, V_{OUT}, I_{OUT}, and L1

V_{IN} (V)	V_{OUT} (V)	I_{OUT} (mA)	L1 (µH)	I_{LMAX} (mA)
3	21	20	10	280
3	21	20	22	225
3	30	20	10	365
3	30	20	22	305

Table 2. Recommended Inductors

L1 (µH)	Parts No.	Rated Current (mA)	Size (mm)	Versions
10	VLS252010ET-100M	550	2.5 × 2.0 × 1.0	R1204xxxxA/B/C/G
10	VLF302512MT-100M	620	3.0 × 2.5 × 1.2	
10	VLF403212MT-100M	900	4.0 × 3.2 × 1.2	
22	VLF302512MT-220M	430	3.0 × 2.5 × 1.2	R1204xxxxD/E/F/H
22	VLF403212MT-220M	540	4.0 × 3.2 × 1.2	
22	VLF504012MT-220M	800	5.0 × 4.0 × 1.2	

Selection of Capacitor

- Place a 1 µF or more bypass capacitor (C1) as close as possible to the V_{IN} and GND pins

[R1204xxxxA/D/G/H]

- Place a 1 µF or more output capacitor (C2) as close as possible to the V_{OUT} and GND pins.
- In the case of operating the inductor using a separated power supply from the IC, place a 1 µF or more bypass capacitor (C3) as close as possible to Vinductor and the GND pin.
- Note the V_{OUT} that depends on LED used, and select the rating of V_{OUT} or more.

[R1204xxxxB/C/E/F]

- Place 1 µF to 10 µF C2 as close as possible to the V_{OUT} and GND pins.
- In the case of operating the inductor using a separated power supply from the IC, place a 1 µF or more bypass capacitor (C4) as close as possible to Vinductor and the GND pin.

SBD (Schottky Barrier Diode) Selection

・ Choose a diode that has low V_F, low reverse current I_R, and low capacitance.

・ SBD is an ideal type of diode for R1204x since it has low V_F, low reverse current I_R, and low capacitance.

Table 3. Recommended Components for R1204xxxxA/D/G/H

Symbol	Rated Voltage (V)	Parts No.
D1	60	CRS12
C1	6.3	CM105B105K06
C2	50	C2012X5R1H105K C2012X5R1H225K (R1204xxxxG/H: I_{LED} > 22 mA)
C3 (Option: Figure 4)	Select by the input voltage	1 μF or more

Table 4. Recommended Components for R1204xxxxB/C/E/F

Symbol	Rated Voltage (V)	Parts No.
D1	60	CRS12
C1	6.3	CM105B105K06
C2	16	C2012X5R1C475K
	25	C2012X5R1E105K
	50	C2012X5R1H105K
C4 (Option: Figure 6)	Select by the input voltage	1 μF or more

Table 5. Recommended Component Values for R1204xxxxB/C/E/F

V_{SET} (V)	7 < V_{SET} ≤ 10	10 < V_{SET} ≤ 25	25 < V_{SET}
R1 (kΩ)	10	10	10
R2 (kΩ)	(V_{SET} -1) x R1	(V_{SET} -1) x R1	(V_{SET} -1) x R1
R3 (Ω)	0	0	0
C1 (μF)	1.0	1.0	1.0
C2 (μF)	4.7	1.0 × 2	1.0
C3 (pF)	10	10	10
C4 (μF)	1.0	1.0	1.0

Other External Components Settings

Set a capacitor (C3) between the V_{OUT} and V_{FB} pins to improve the response of DC/DC converter by giving high-frequency voltage feedback. Please note that C3 operation could be different from the theory of operation depending on component layouts and parasitic capacitances.

Output Voltage Setting (R1204xxxxB/C/E/F)

The relation between the output voltage (V_{SET}) and the resistors (R1, R2) is calculable by the following formula.

$V_{SET} = V_{FB} \times (R1 + R2) / R1$

The sum of R1 and R2 should be 300 kΩ or less. Ensure the V_{IN} and GND lines are sufficiently robust. If their impedances are too high, noise pickup or unstable operation may result. Set a capacitor (C2) with a suitable voltage resistance (more than 1.5 times of V_{SET}) between the V_{IN} and GND pins, and as close as possible to the pins.

LED Current Setting (R1204xxxxA/D/G/H)

The LED current (I_{LED}) when a ”H” signal is applied to the CE pin (Duty = 100%) can be determined by the value of feedback resistor (R1).

$I_{LED} = 0.2 / R1$ (R1204xxxxA/D)

$I_{LED} = 0.4 / R1$ (R1204xxxxG/H)

LED Dimming Control (R1204xxxxA/D/G/H)

The brightness of the LEDs can be adjusted by applying a PWM signal to the CE pin. By inputting “L” voltage for a certain period of time (Typ. 9 ms (R1204xxxxA/G) / 12 ms (R1204xxxxD/H) or more), the IC goes into standby mode and turns off LEDs. I_{LED} can be controlled by the duty of a PWM signal for the CE pin.
The relation between the high-duty of the CE pin (Hduty) and I_{LED} is calculatable by the following formula.

$I_{LED} = Hduty \times V_{FB} / R1$

The frequency range of a PWM signal should be set within the range of 200 Hz to 300 kHz. In the case of using a 20 kHz or less PWM signal for dimming the LEDs, the increasing or decreasing of the inductor current (IL) may make noise in the audible band. In this case, a high frequency PWM signal should be used.

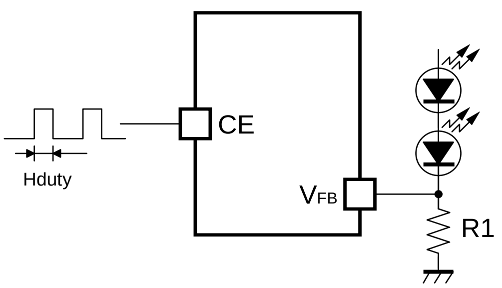


Figure 7. Dimming Control by CE Pin

Low luminance Dimming Accuracy (R1204xxxxG/H)

Low luminance Dimming filtered V_{FB} voltage tolerance depends on the offset voltage of the internal DC/DC converter. By this offset voltage, some voltage difference may be generated between V_{REF} voltage and V_{FB} voltage. Low luminance Dimming Accuracy is shown in Table 5.

Table 6. Low luminance Dimming Accuracy for R1204xxxxG/H (R1 = 20 Ω)

The duty of a PWM signal for the CE pin	I_{LED} Min.	I_{LED} Max.
3.5% (Frequency = 20 kHz to 300 kHz)	0.01 mA[2]	2.1 mA[2]

[2] Guaranteed by design engineering (Ta = 25ºC).

TECHNICAL NOTES

Current Path on PCB

Figure 8 and Figure 9 show flows of current paths of the application circuits when MOSFET is ON and when MOSFET is OFF, respectively.

- Parasitic elements (impedance, inductance or capacitance) in the paths pointed with red arrows in Figure 8 and Figure 9 influence stability of the system and cause noise outbreak. It is recommended that these parasitic elements be minimized.
- In addition, except for the paths of LED load, it is recommended that the all wirings of the current paths be made as short and wide as possible.

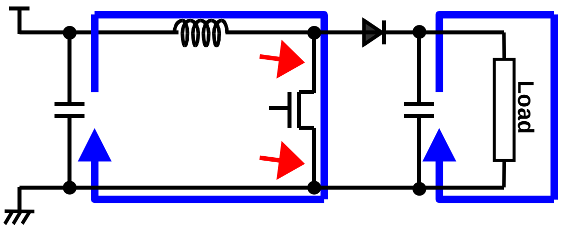


Figure 8. MOSFET-ON

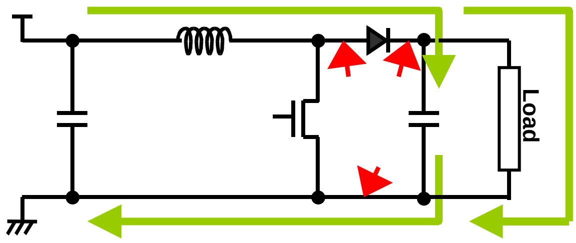


Figure 9. MOSFET-OFF

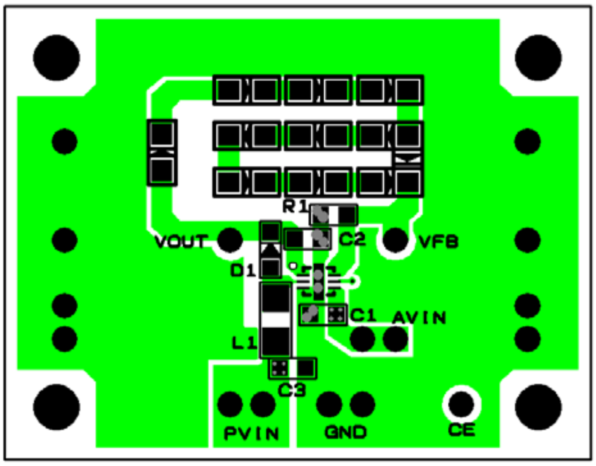
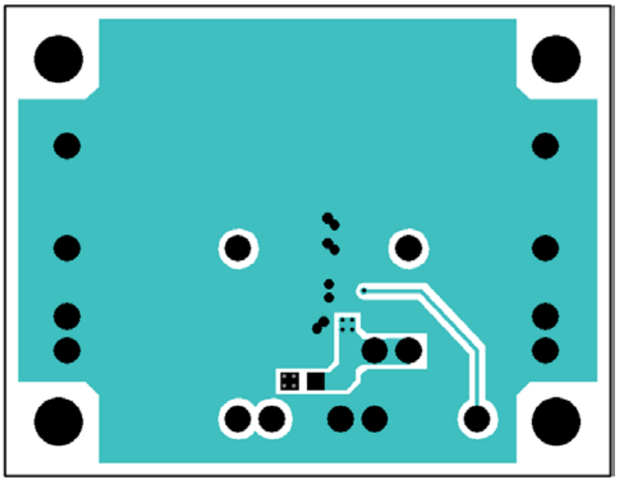
Layout Guide for PCB

- Place C1 as close as possible to the V_{IN} and GND pins. Also, connect the GND pin to the wider GND plane.
- Make the L_X land pattern as small as possible.
- Make the wirings between the L_X pin, the inductor and the diode as short as possible. Also, connect C2 as close as possible to the cathode of the diode.
- Place C2 as close as possible to the GND pin.

PCB Layout

PKG: DFN(PLP)1820-6 pin

R1204Kxx2A/D/G/H

<table>
<tr><th>Topside</th><th>Backside</th></tr>
<tr><td>
</td><td></td></tr>
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R1204Kxx2B/C/E/F

<table>
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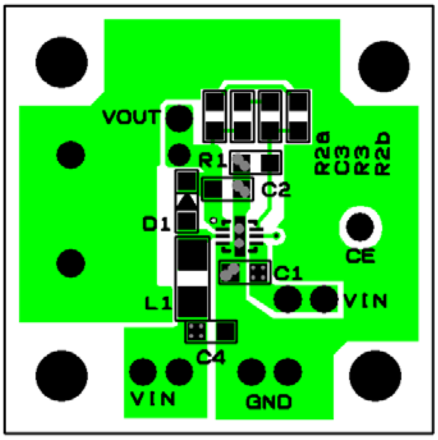
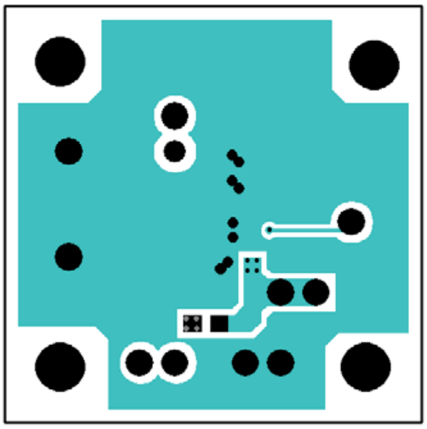
<tr><td>
</td><td></td></tr>
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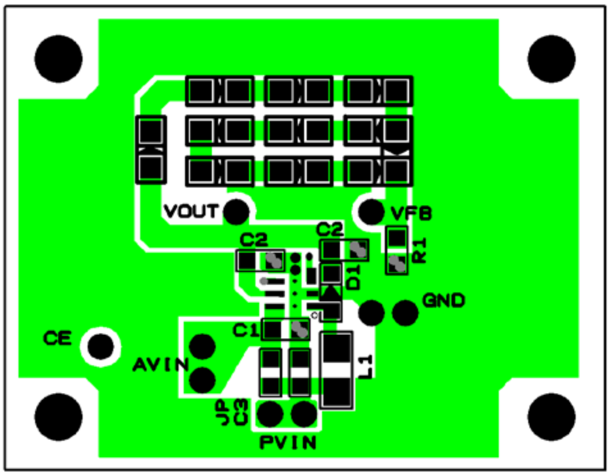
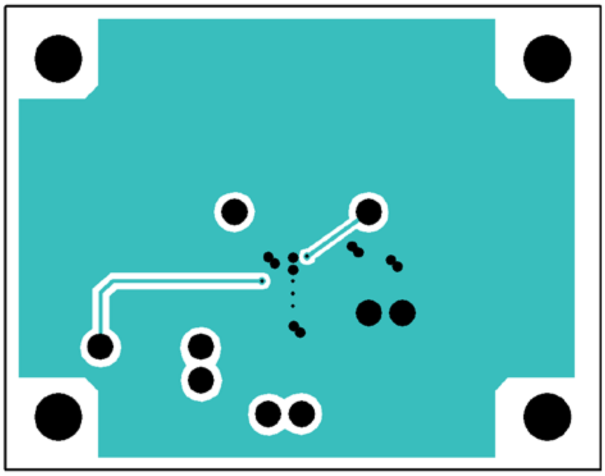
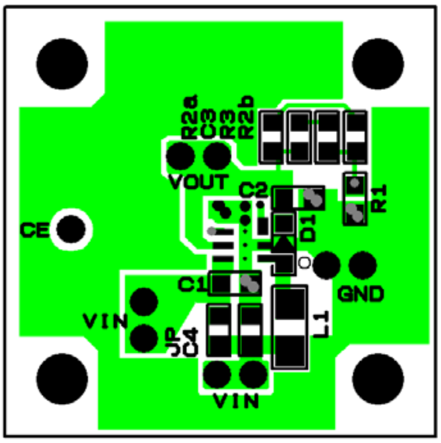
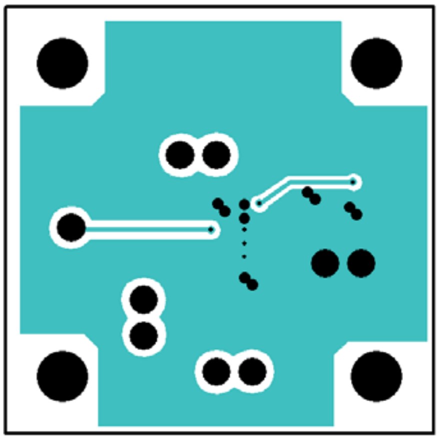
PKG: TSOT-23-6 pin

R1204Nxx3A/D/G/H

Topside	Backside
	

R1204Nxx3B/C/E/F

Topside	Backside
	

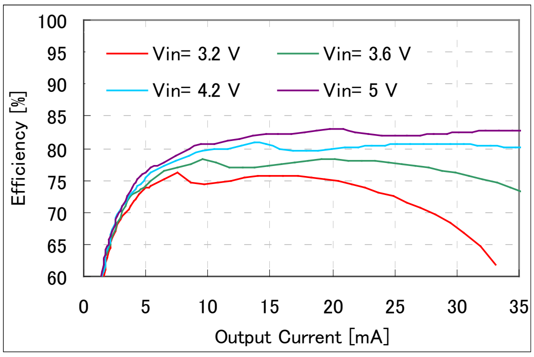
RICOH

TYPICAL CHARACTERISTICS

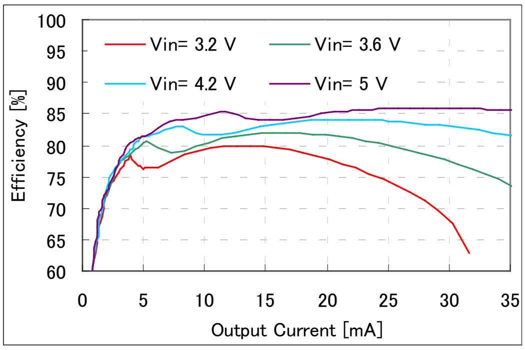
1) Efficiency vs. Output Current of R1204xxxxA/D/G/H
Used LED: NICHIA, NSSW208A (Vf = 3.0 V (I_{LED} = 20 mA))

1-1) Efficiency vs. Output Current with Different Output Voltages, 10 LEDs in Series (V_{OUT} = 30 V (I_{OUT} = 20 mA))

R1204xxxxA/G, L = 10 μH (VLF302512MT-100M)

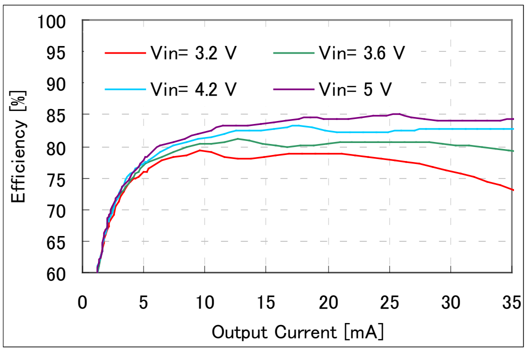


R1204xxxxD/H, L = 22 μH (VLF302512MT-220M)

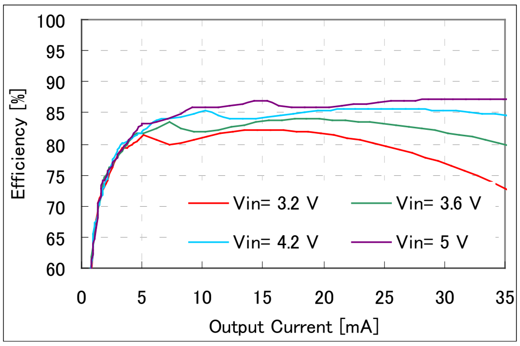


8 LEDs in Series (V_{OUT} = 24 V (I_{OUT} = 20 mA))

R1204xxxxA/G, L = 10 μH (VLF302512MT-100M)

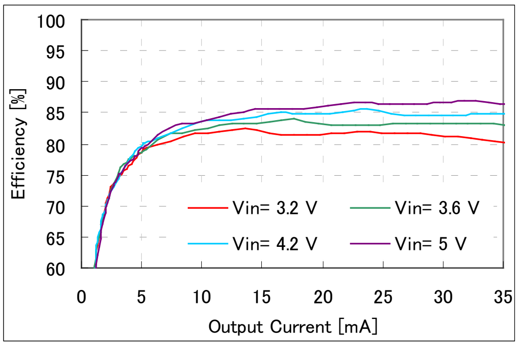


R1204xxxxD/H, L = 22 μH (VLF302512MT-220M)

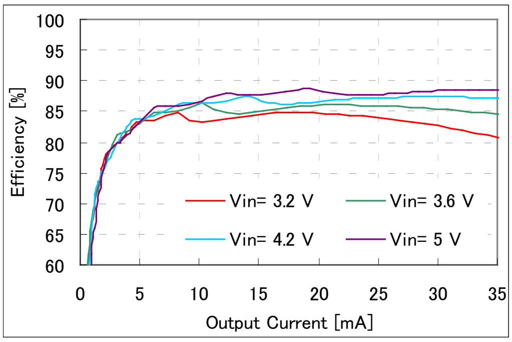


6 LEDs in Series (V_{OUT} = 18 V (I_{OUT} = 20 mA))

R1204xxxxA/G, L = 10 μH (VLF302512MT-100M)



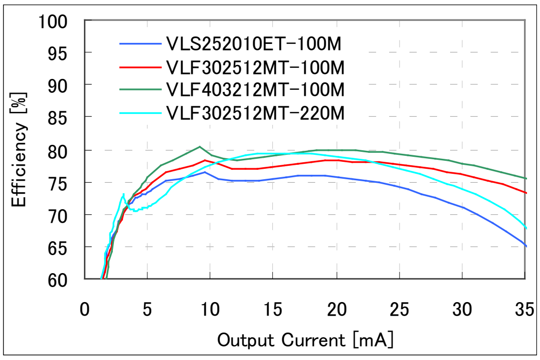
R1204xxxxD/H, L = 22 μH (VLF302512MT-220M)



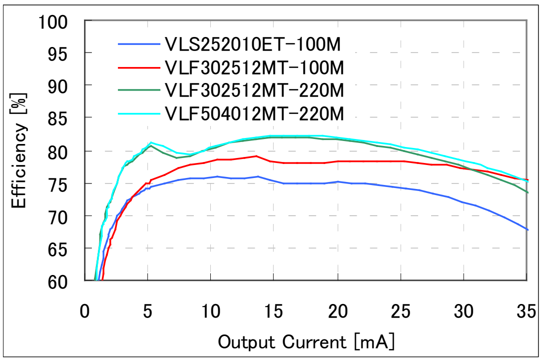
1-2) Efficiency vs. Output Current with Different Inductors (V_{IN} = 3.6 V)

10 LEDs in Series (V_{OUT} = 30 V (I_{OUT} = 20 mA))

R1204xxxxA/G

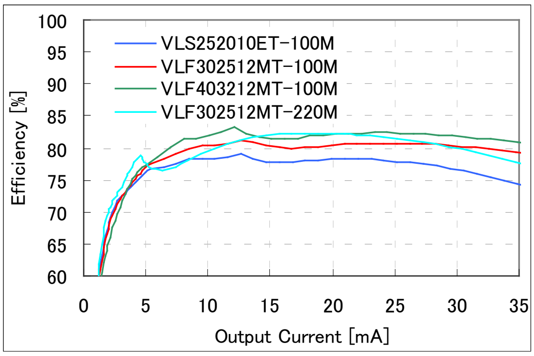


R1204xxxxD/H

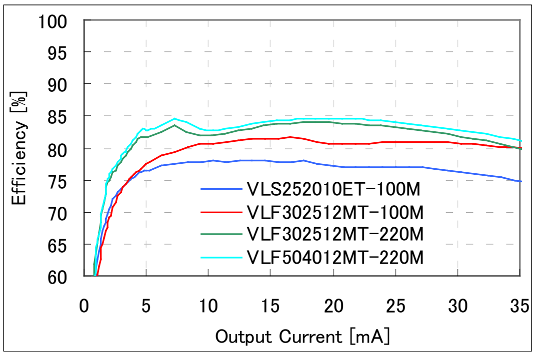


8 LEDs in Series (V_{OUT} = 24 V (I_{OUT} = 20 mA))

R1204xxxxA/G



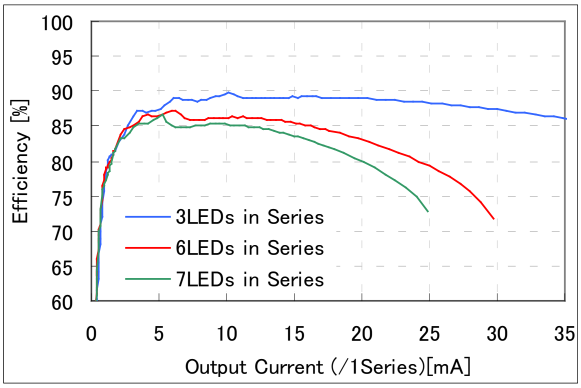
R1204xxxxD/H



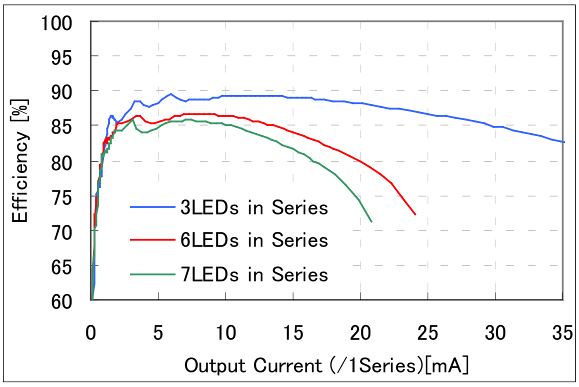
1-3) Efficiency vs. Output Current with Different Numbers of LEDs

LEDs in 3 Parallels (V_{IN} = 3.6 V)

R1204xxxxA/G, L=10μH (VLF302512MT-100M)

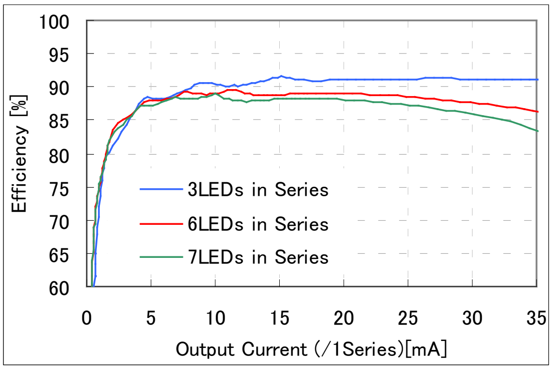


R1204xxxxD/H, L=22μH (VLF302512MT-220M)

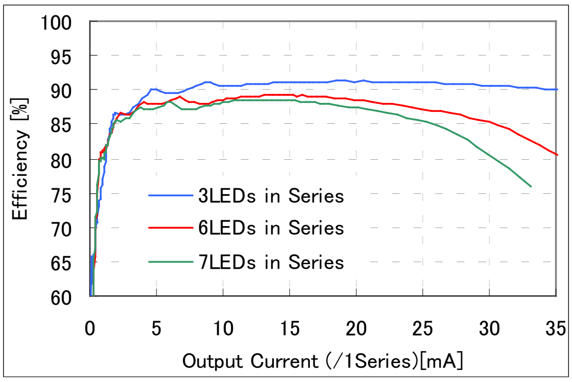


LEDs in 3 Parallels (V_{IN} = 5.0 V)

R1204xxxxA/G, L = 10 µH (VLF302512MT-100M)



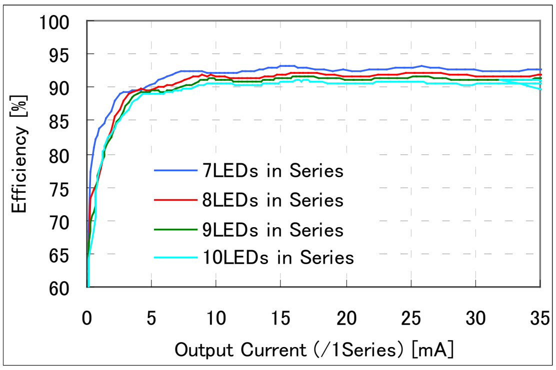
R1204xxxxD/H, L = 22 µH (VLF302512MT-220M)



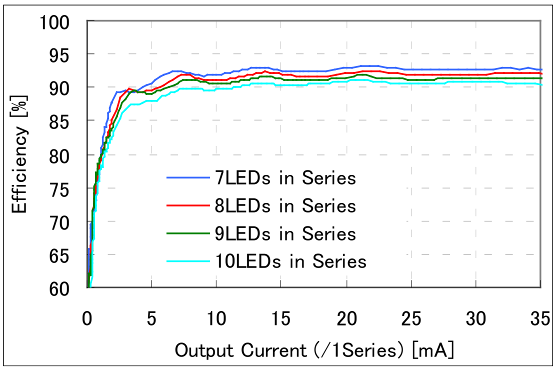
1-4) Efficiency vs. Output Current with Different Numbers of LEDs

LEDs in 3 Parallels (V_{IN} = 3.6 V, Inductor Voltage = 12.0 V)

R1204xxxxA/G, L = 10 µH (VLF302512MT-100M)

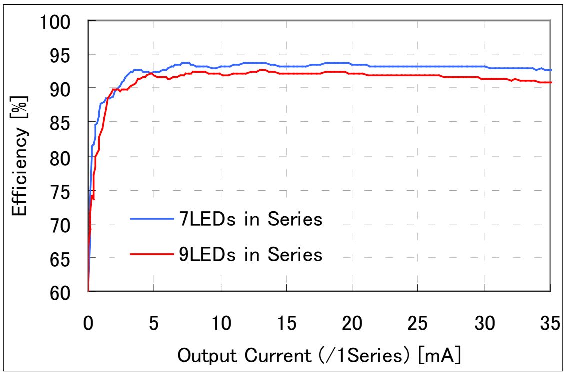


R1204xxxxD/H, L = 22 µH (VLF302512MT-220M)

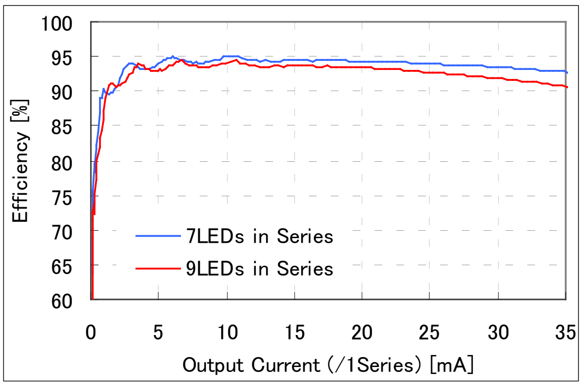


LEDs in 6 Parallels (V_{IN} = 3.6 V, Inductor Voltage = 12.0 V)

R1204xxxxA/G, L = 10 µH (VLF302512MT-100M)



R1204xxxxD/H, L = 22 µH (VLF302512MT-220M)



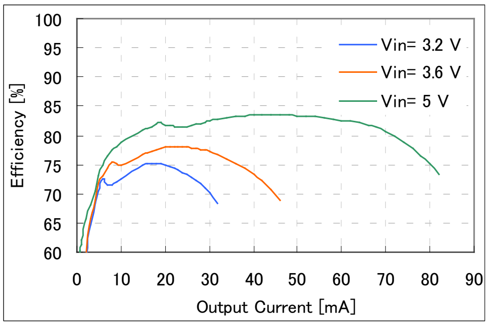
2) Efficiency vs. Output Current of R1204xxxxB/C/E/F

2-1) Efficiency vs. Output Current with Different Output Voltages

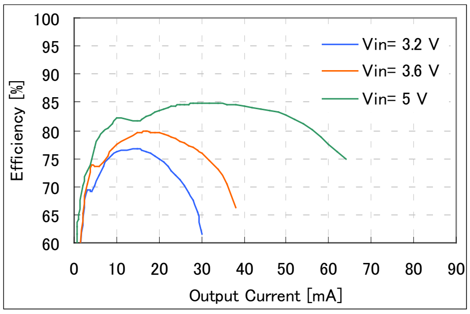
V_{SET} = 31 V

V_{IN} = Inductor Voltages

R1204xxxxC, L = 10 μH (VLF302512MT-100M)

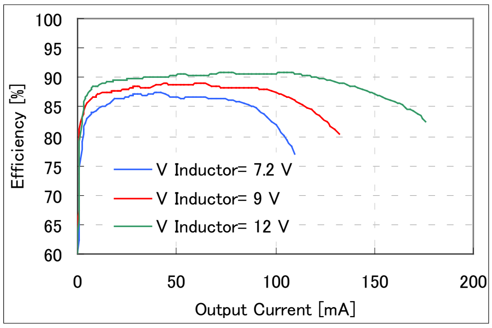


R1204xxxxF, L = 22 μH (VLF302512MT-220M)

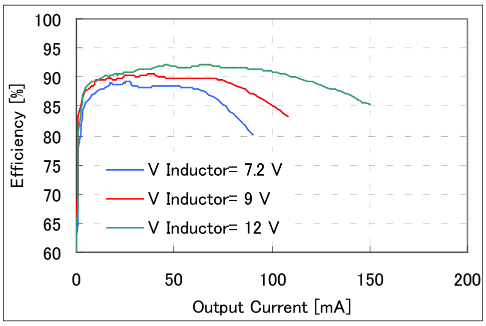


Different V_{IN} / Inductor Voltages (V_{IN} = 3.6 V)

R1204xxxxC, L = 10 μH (VLF302512MT-100M)



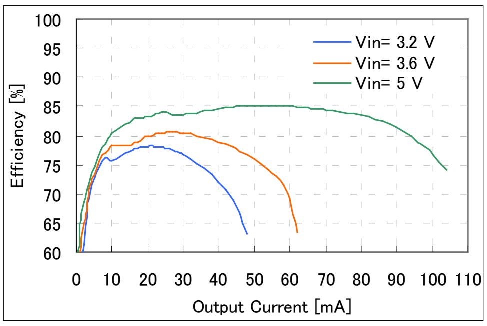
R1204xxxxF, L = 22 μH (VLF302512MT-220M)



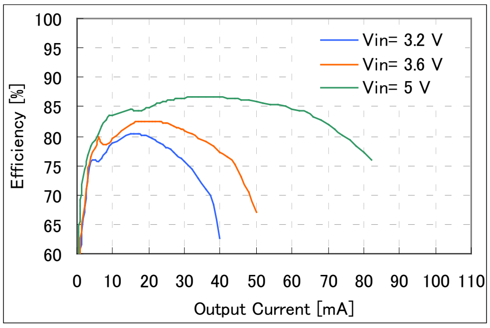
V_{SET} = 25 V

V_{IN} = Inductor Voltages

R1204xxxxC, L = 10 μH (VLF302512MT-100M)

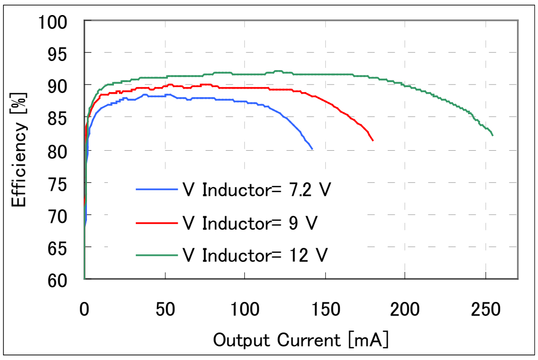


R1204xxxxF, L = 22 μH (VLF302512MT-220M)

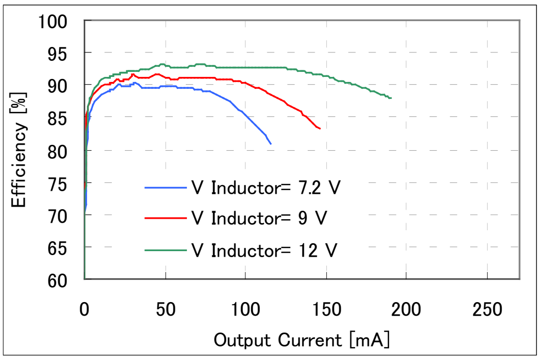


Different V_{IN} / Inductor Voltages (V_{IN} = 3.6 V)

R1204xxxxC, L = 10 μH (VLF302512MT-100M)



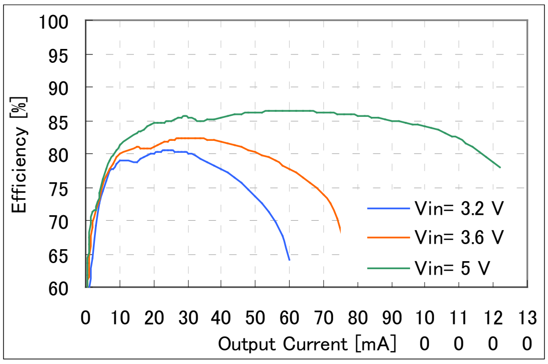
R1204xxxxF, L = 22 μH (VLF302512MT-220M)



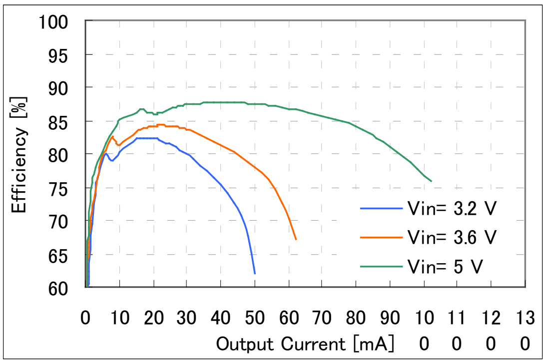
V_{SET} = 21 V

V_{IN} = Inductor Voltage

R1204xxxxC, L = 10 μH (VLF302512MT-100M)

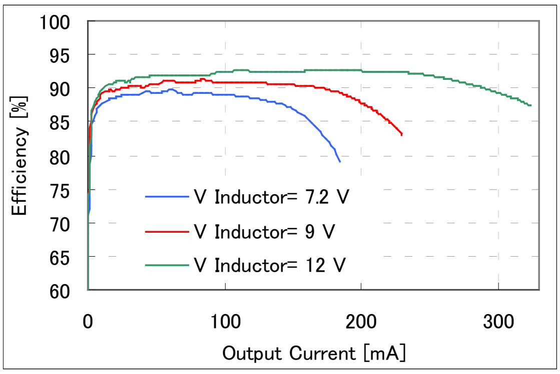


R1204xxxxF, L = 22 μH (VLF302512MT-220M)

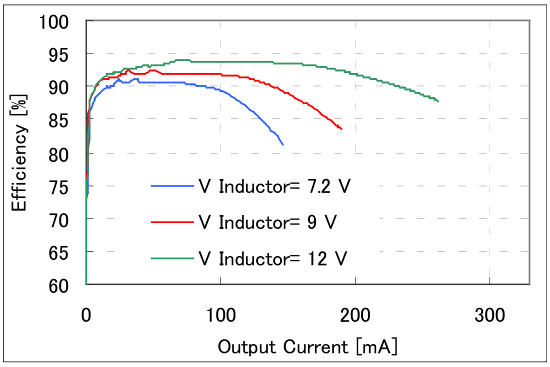


Different V_{IN} / Inductor Voltages (V_{IN} = 3.6 V)

R1204xxxxC, L = 10 μH (VLF302512MT-100M)

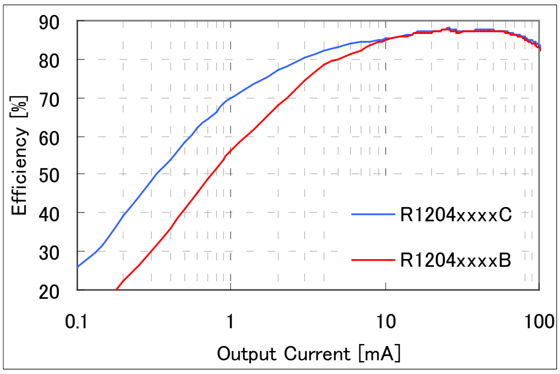


R1204xxxxF, L = 22 μH (VLF302512MT-220M)

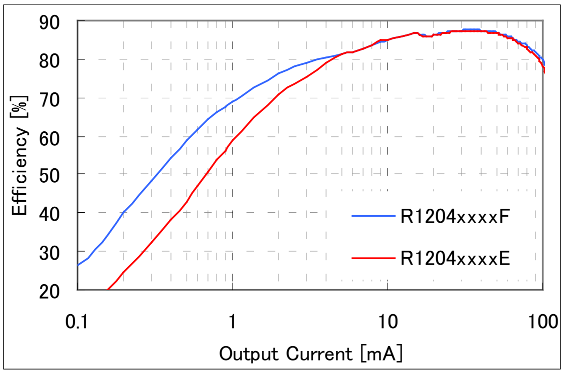


2-2) Efficiency vs. Output Current with PWM Control and PWM/VFM Auto-Switching Control (V_{IN} = 3.6 V, V_{SET} = 12 V)

R1204xxxxB/C, L = 10 μH (VLF302512MT-100M)

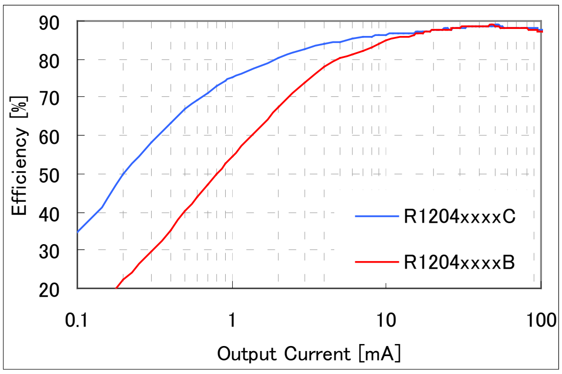


R1204xxxxE/F, L = 22 μH (VLF302512MT-220M)

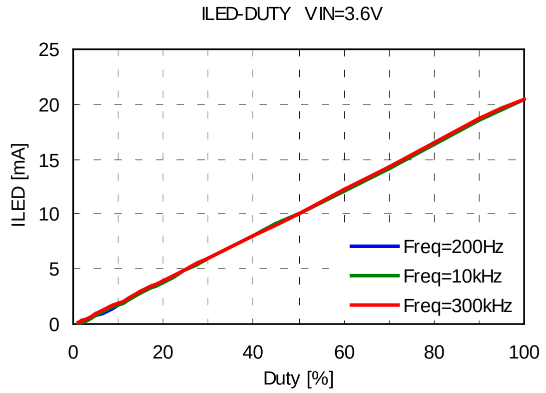


Inductor Voltage = 7.2 V, V_{SET} = 25 V

R1204xxxxB/C, L = 10 μH (VLF302512MT-100M)



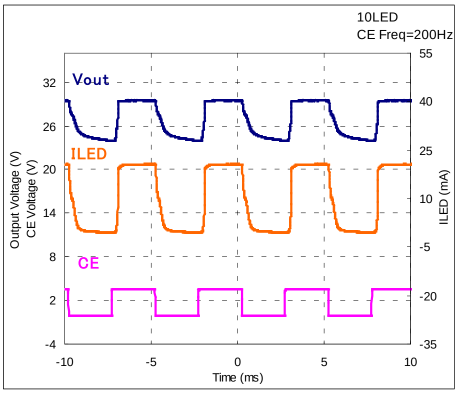
3) Maxduty vs. I_{LED} (R1204xxxxA/D/G/H, 10 LEDs in Series, V_{IN} = 3.6 V)



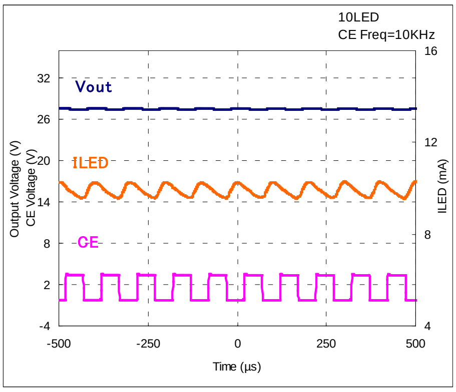
RICOH

4) V_{OUT} / I_{LED} Ripple of R1204xxxxA/D/G/H When Dimming (10 LEDs in Series, L = 10 μH (VLF302512MT-100M))

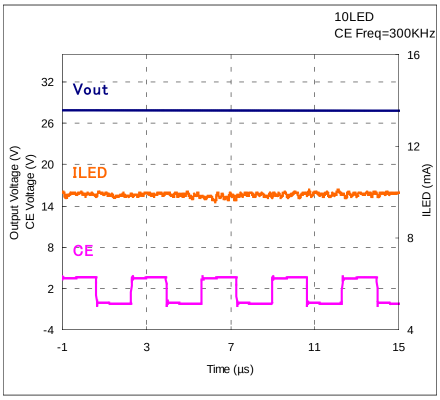
CE Freq = 200 Hz



CE Freq = 10 kHz

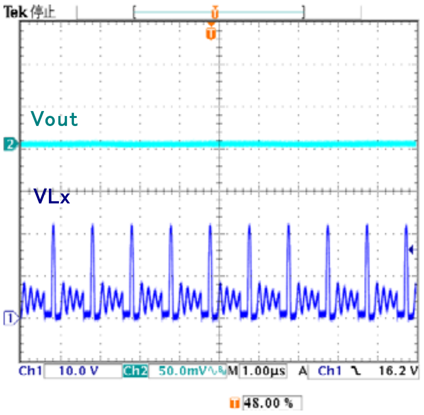


CE Freq = 300 kHz

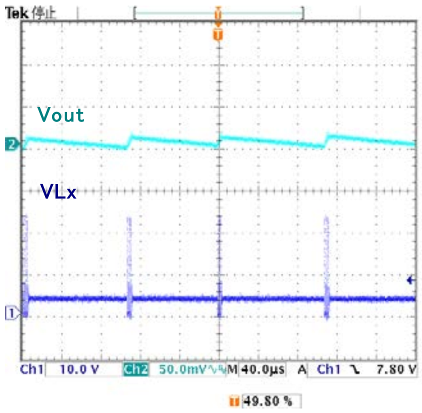


5) V_{OUT} Ripple (V_{IN} = 3.6 V, V_{SET} = 21 V, I_{OUT} = 0 mA, L = 10 μH (VLF302512MT-100M))

PWM Control (R1204xxxxB)



VFM Control (R1204xxxxC)

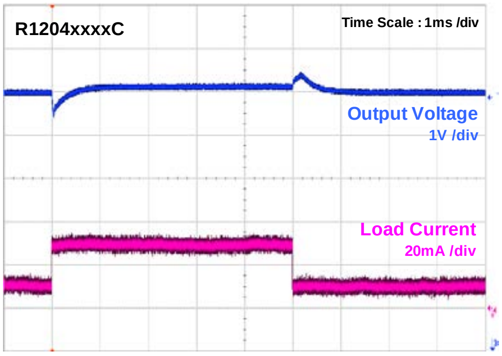


RICOH

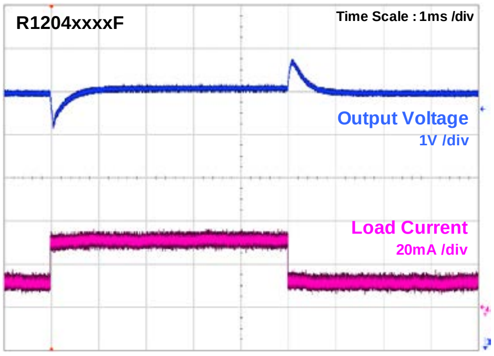
6) Load Transient Response

(V_{IN} = 3.6 V, V_{SET} = 25 V, L = 10 μH (VLF302512MT-100M), I_{OUT} = 10 mA ⇔ 30 mA, Tr = Tf = 0.5 μs)

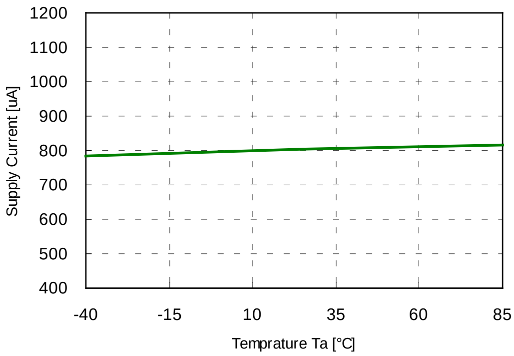
R1204xxxxC



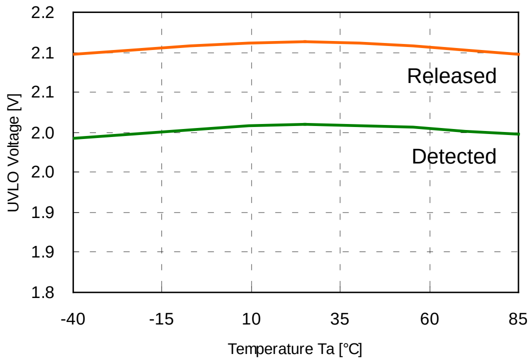
R1204xxxxF



7) Supply Current vs. Ambient Temperature

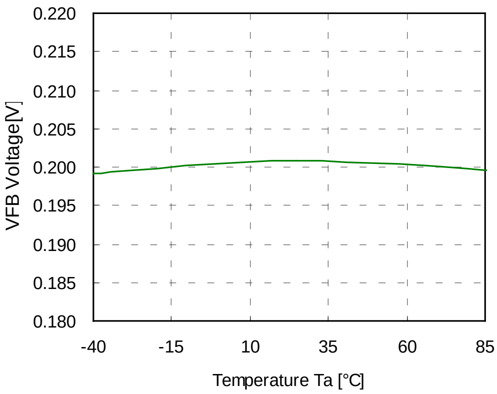


8) UVLO vs. Ambient Temprature

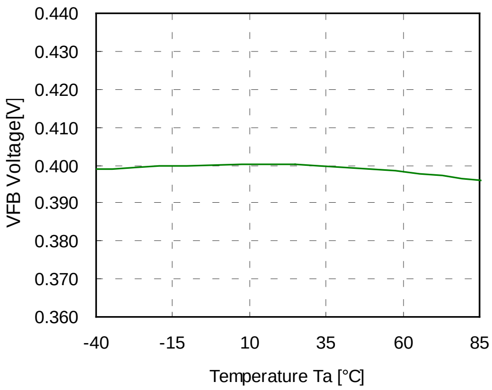


9) VFB Voltage vs. Ambient Temperature

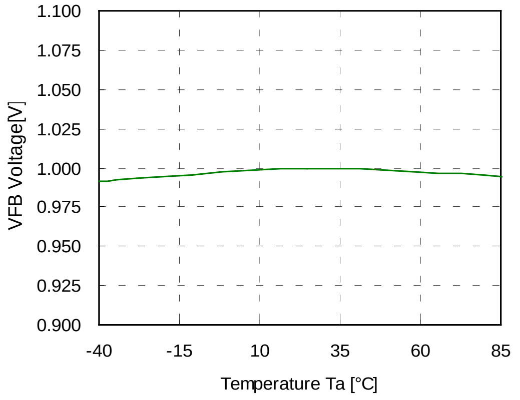
R1204xxxxA/D



R1204xxxxG/H

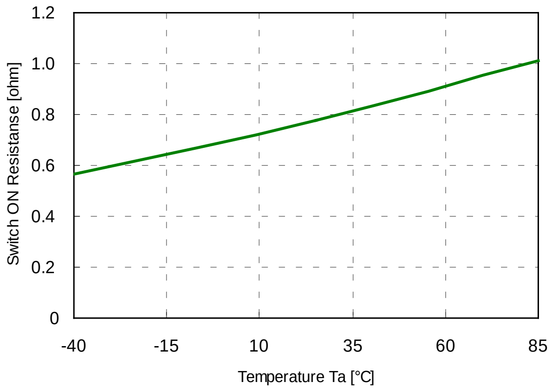


R1204xxxxB/C/E/F

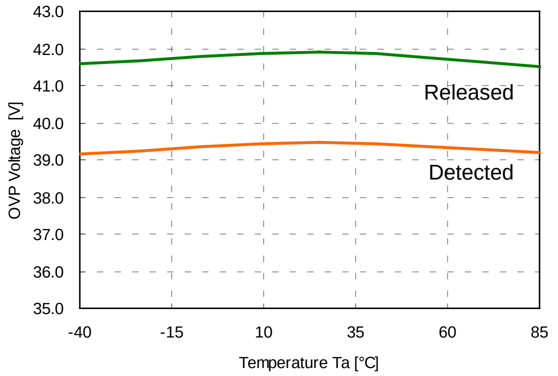


10) Switch ON Resistance vs. Ambient Temperature

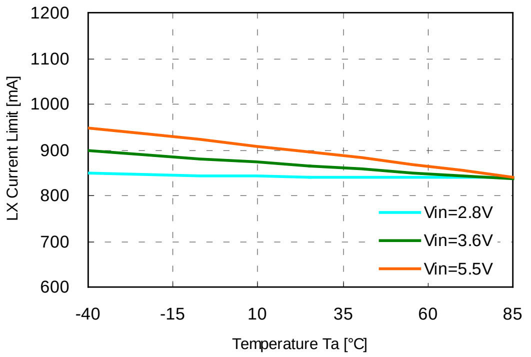
R1204x3xxx



11) OVP Voltage vs. Ambient Temperature

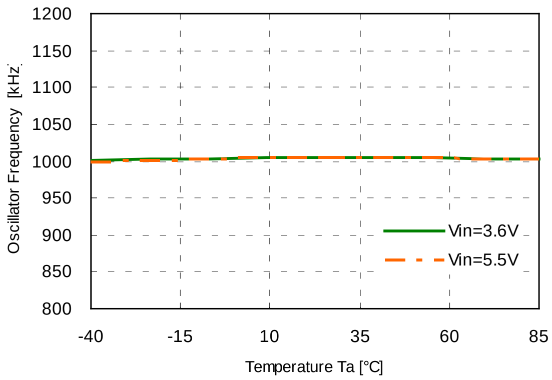


12) LX Limit Current vs. Ambienet Temperature

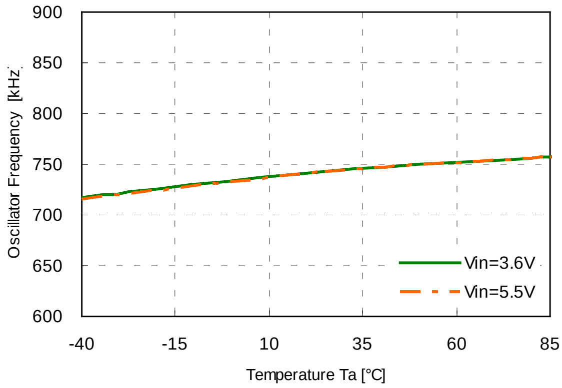


13) Oscillator Frequency vs. Ambiemnt Temperature

R1204xxxxA/B/C/G



R1204xxxxD/E/F/H



The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

Measurement Conditions

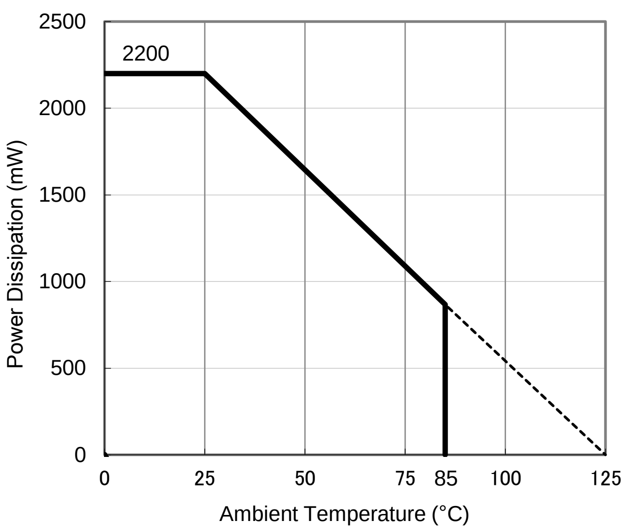
Item	Measurement Conditions
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square
Through-holes	ϕ 0.2 mm × 34 pcs

Measurement Result (Ta = 25°C, Tjmax = 125°C)

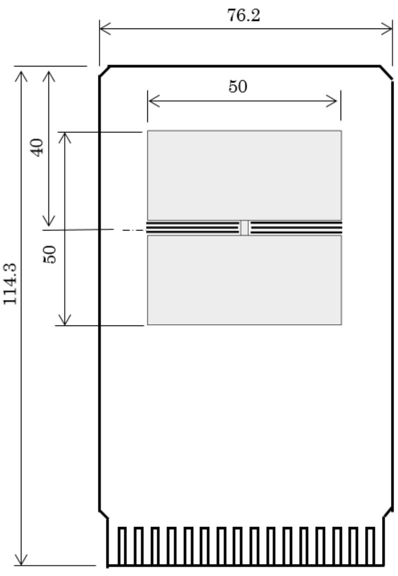
Item	Measurement Result
Power Dissipation	2200 mW
Thermal Resistance (θja)	θja = 45°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 18°C/W

θja: Junction-to-Ambient Thermal Resistance

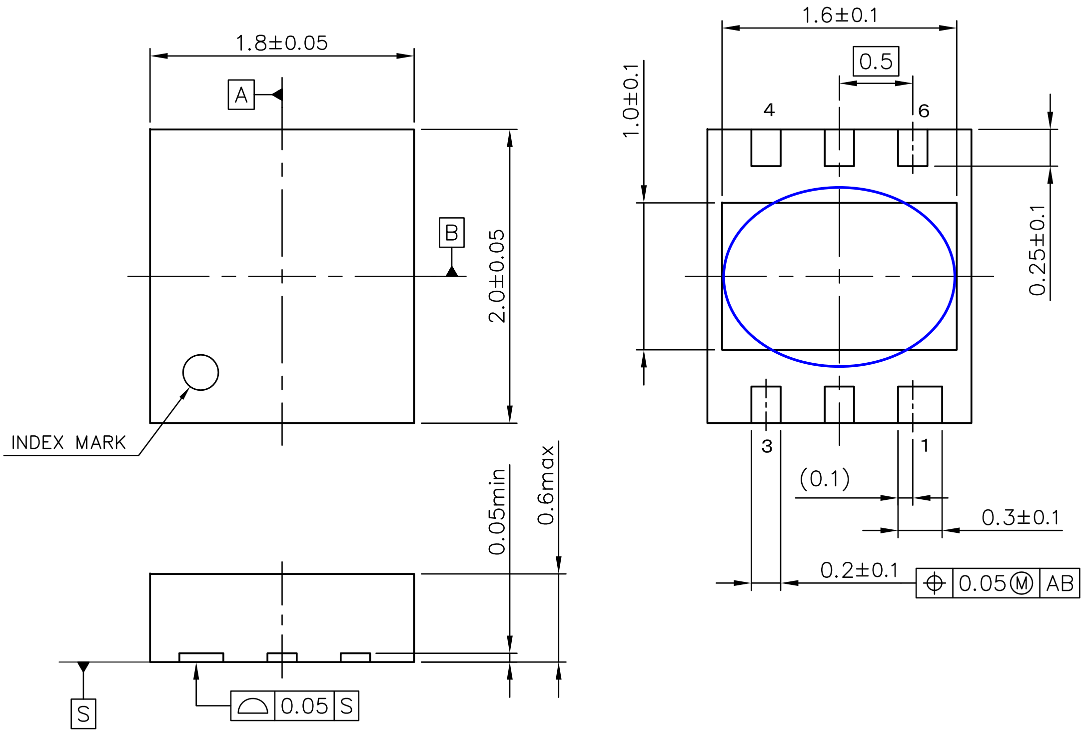
ψjt: Junction-to-Top Thermal Characterization Parameter



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



UNIT: mm

DFN(PLP)1820-6 Package Dimensions

* The tab on the bottom of the package is substrate level (GND). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.

Ver. A

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

Measurement Conditions

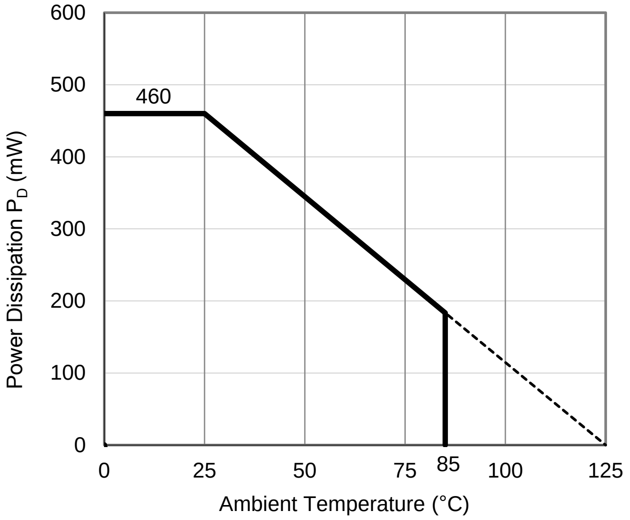
Item	Standard Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-Sided Board)
Board Dimensions	40 mm × 40 mm × 1.6 mm
Copper Ratio	Top Side: Approx. 50% Bottom Side: Approx. 50%
Through-holes	ϕ 0.5 mm × 44 pcs

Measurement Result (Ta = 25°C, Tjmax = 125°C)

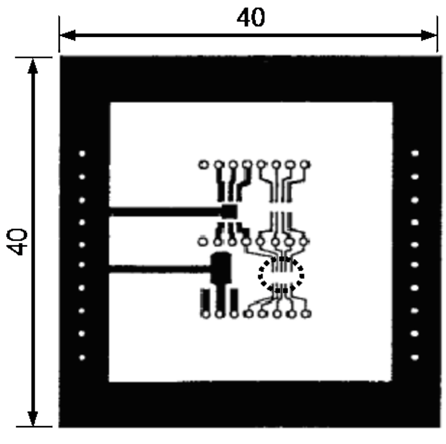
Item	Standard Test Land Pattern
Power Dissipation	460 mW
Thermal Resistance (θja)	θja = 217°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 40°C/W

θja: Junction-to-Ambient Thermal Resistance

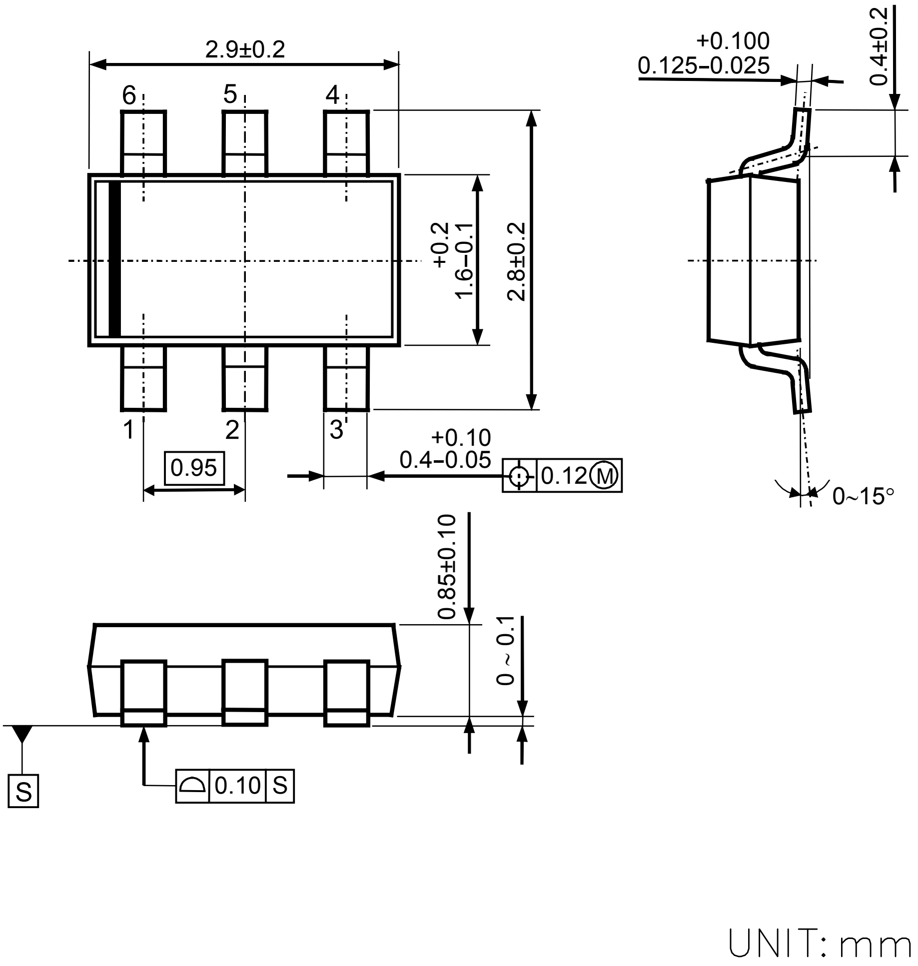
ψjt: Junction-to-Top Thermal Characterization Parameter



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



TSOT-23-6 Package Dimensions

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