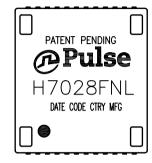
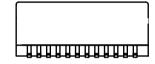




NOTES:

- 1. ROHS COMPLIANT
- 2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
- 3. STORAGE TEMPERATURE: -20°C TO +125°C
- 4. COMPLIANCE TO J-STD:
 - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
 - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
 - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
- 5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e H7028FNL BECOMES H7028FNLT).







FINAL OUTLINE

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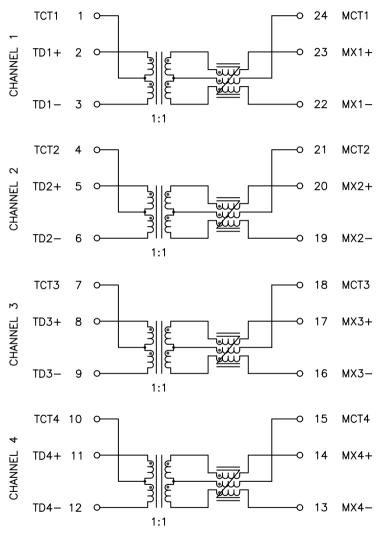
PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,10G,1:1,GEN2,SM,TU	H7028FNL-10	PS-2578.002-B	1 OF 3	H7028FNL	A

E-MAIL: PRODINFONETWORK@PULSEELECTRONICS.COM(US), ASIA@PULSFFLFCTRONICS.COM(Asia) P

PHONE: USA:858 674 8100, GERMANY:49 7032 78060, SINGAPORE:65 6287 8998, SHANGHAI:86 21 62787060, TAIWAN:886 3 4356768







ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS					
OPERATING TEMP	0°C – 70°C					
TURNS RATIO	1.00 ± 2%					
POLARITY	PER SCHEMATIC					
	100 KHz	1-500 MHz				
INSERTION LOSS	-3.0 dB MAX -1.0 dB MAX (6 dB TYP)			(6 dB TYP)		
RETURN LOSS	2-100 MH:	100 MHz-500 MHz		-500 MHz		
$(Z \text{ OUT} = 100 \text{ OHM} \pm 1\%)$	-25 dB MIN	-11	-11 + 17*LOG (f/400)			
INDUCTANCE (OCL)	120 uH TYP @ 100 kHz, 100 mV			Hz, 100 mV		
	1-100 M	1Hz	100	100-500 MHz		
CROSSTALK, ADJACENT CHANNELS	-40 dB MIN		-30	-30 dB MIN		
COMMON MODE	1-100 MHz	100-300) MHz	300-500 MHz		
REJECTION RATIO	-35 dB MIN	-30 dB	MIN	-25 dB MIN		
COMMON TO	1-250 MHz		25	250-500 MHz		
DIFFERENTIAL MODE REJECTION (CDMR)	-30 dB MIN		_:	-22 dB MIN		
INPUT – OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS					

NOTE: f IS FREQUENCY IN MHz.

SCHEMATIC

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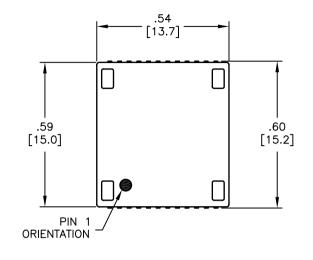
PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,10G,1:1,GEN2,SM,TU	H7028FNL-10	PS-2578.002-B	2 OF 3	H7028FNL	A

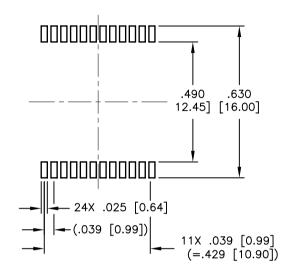
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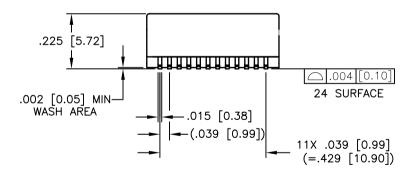


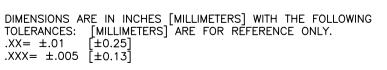


√24X 0°-8°

SUGGESTED LAND PATTERN

- 24X .035 [0.89]





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