



SDM1A30CSP

1A SCHOTTKY BARRIER RECTIFER CHIP SCALE PACKAGE

Product Summary

V _{RRM} (V)	I _O (A)	V _{F MAX} (mV)	I _{R MAX} (μA)
30	1	525	100

Description

The SDM1A30CSP is a 30V 1A Schottky barrier rectifier that is optimized for low forward voltage drop and low-leakage current, housed in a compact chip scale package (CSP) that occupies only 0.6mm² board space. The low thermal resistance enables designers to meet design challenges of increasing efficiency while also reducing board space.

Applications

It is ideally suited for use in portable applications as a:

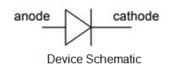
- **Blocking Diode**
- **Boost Diode**
- Switching Diode
- Reverse Protection Diode

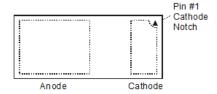
Features and Benefits

- Off Board Profile of 0.275mm More than 30% Thinner than DFN1006
- Low Forward Voltage (V_F) Minimizes Conduction Losses and Improves Efficiency
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure **Temperature Operation**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X3-WLB1006-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208**e4**
- Polarity: Cathode Dot
- Weight: 0.001 grams (Approximate)





Ordering Information (Note 4)

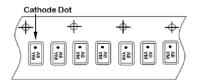
Part Number		Case	Packaging		
	SDM1A30CSP-7	X3-WLB1006-2	5000/Reel		
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.				

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



XH = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M = Month (ex: 9 = September) Dot Denotes Cathode Pin



Date Code Key

Year	201	8	2019		2020	20	21	2022		2023	2	2024
Code	F		G		Н		l	J		K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	30	V
Average Rectified Output Current	lo	1	А
Repetitive Peak Forward Current (Pulse Wave = 1ms, Duty Cycle = 25%)	I _{FRM}	4	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	15	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\Theta JA}$	135	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

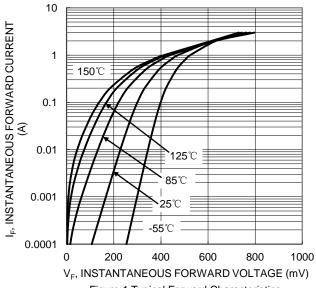
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

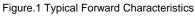
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop		_	395	440		I _F = 0.5A, T _J = +25°C
	VF	_	475	525		I _F = 1.0A, T _J = +25°C
		_	425	_		I _F = 1.0A, T _J = +125°C
Leakage Current (Note 6)		_	6	20	пΔ	V _R = 10V, T _J = +25°C
	I _R	_	20	100		$V_R = 30V, T_J = +25^{\circ}C$
		_	8	_	mA	V _R = 30V, T _J = +125°C
Junction Capacitance	C _T	_	40	_	pF	V _R = 4V, f = 1.0MHz

Notes:

- 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.







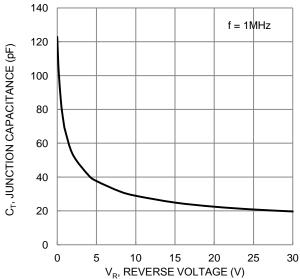


Figure 3. Typical Junction Capacitance

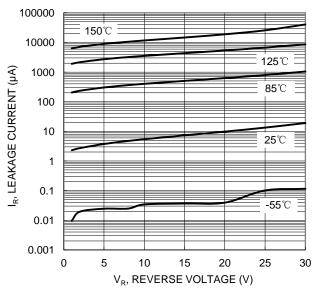


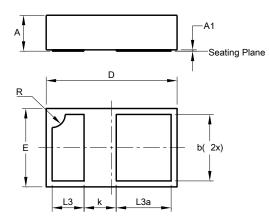
Figure 2. Typical Reverse Characteristics



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1006-2

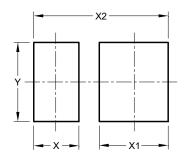


	X3-WLB1006-2							
Dim	Min	Max	Тур					
Α	0.25	0.30	0.275					
A1	0.00	0.01	-					
b	0.450	0.550	0.500					
D	0.95	1.05	1.000					
Е	0.55	0.65	0.600					
k	-	-	0.288					
L3	0.194	0.294	0.244					
L3a	0.350	0.450	0.400					
R	-	-	0.100					
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1006-2



Dimensions	Value		
Dimensions	(in mm)		
Х	0.332		
X1	0.507		
X2	0.989		
Y	0.579		



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