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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild <a href="general-regarding-numbers-n

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July 2018

SSA36 Surface Mount Schottky Barrier Rectifier

Features

- UL Flammability 94V-0 Classification
- MSL 1
- RoHS Compliant / Green Mold Compound



Ordering Information

Part Number	Top Mark	Package	Packing Method
SSA36	SSA36	DO-214AC (SMA)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Recurrent Peak Reverse Voltage	60	V
V _{RMS}	RMS Voltage	42	V
V_{DC}	DC Blocking Voltage	60	V
I _{F(AV)}	Average Forward Current at T _L = 75°C	3	Α
I _{FSM}	Peak Forward Surge Current: 8.3 ms Single Half Sine-Wave Superimposed on Rated Load	80	А
TJ	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
ΨJL	Typical Thermal Characteristics, Junction-to-Lead ⁽²⁾	30	°C/W
$R_{\theta JA}$	Typical Thermal Resistance, Junction-to-Ambient	180	°C/W

Note:

- 1. Per JESD51-3 recommended thermal test board. Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm.
- 2. Thermocouple soldered at cathode lead.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _F	Forward Voltage ⁽³⁾	I _F = 3.0 A			0.75	V
I _R	DC Reverse Current	V _R = 60 V			0.1	mA
		$V_R = 60 \text{ V}, T_A = 100^{\circ}\text{C}$			20	
T _{rr}	Reverse Recovery Time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		10.74		ns

Note:

3. Pulse test with Pulse width = 300 μ s, 1% duty cycle.

Typical Performance Characteristics

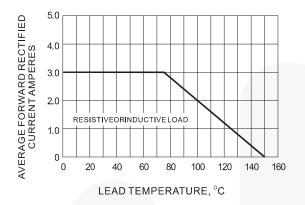
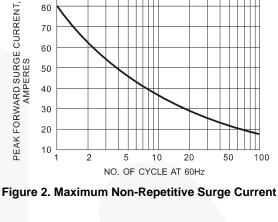


Figure 1. Forward Current Derating Curve



90

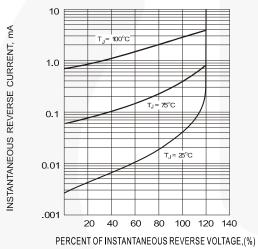


Figure 3. Typical Reverse Characteristic

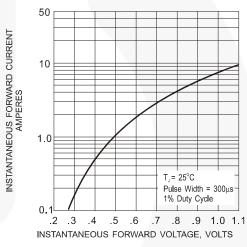


Figure 4. Typical Instantaneous Forward Characteristics

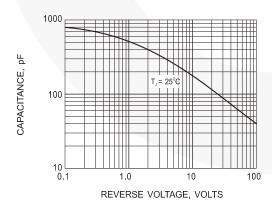


Figure 5. Typical Junction Capacitance

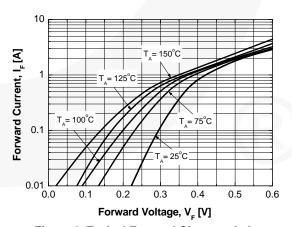


Figure 6. Typical Forward Characteristics

Physical Dimensions

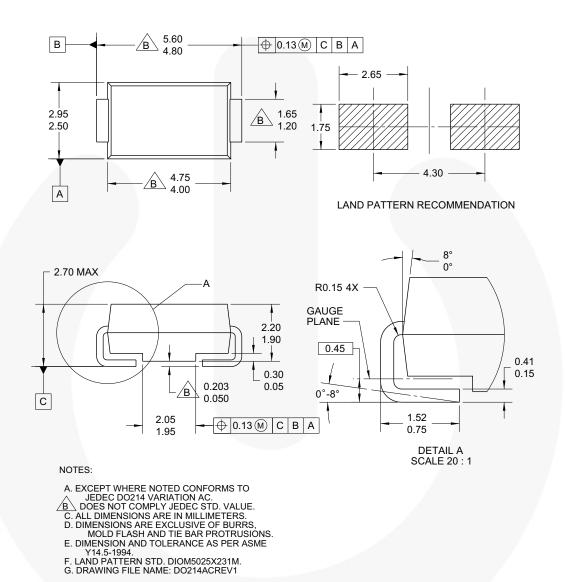


Figure 7. 2-LEAD, SMA, JEDEC DO-214, VARIATION AC





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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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