muRata

Reference Specification

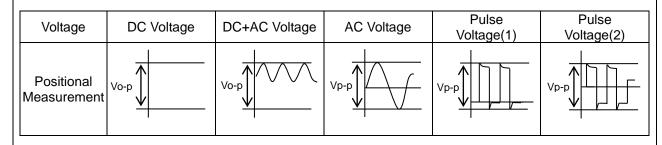
Type KX Safety Standard Certified Lead Type Disc Ceramic Capacitors for General Purpose

Product specifications in this catalog are as of Mar. 2021, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.



2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. TEST CONDITION FOR WITHSTANDING VOLTAGE

(1) TEST EQUIPMENT

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60 Hz sine wave.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

(2) VOLTAGE APPLIED METHOD

When the withstanding voltage is applied, capacitor's lead or terminal should be firmly connected to the out-put of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the out-put of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

*ZERO CROSS is the point where voltage sine wave pass 0V. - See the right figure -

0V voltage sine wave

4. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip : 400 °C max.

Soldering iron wattage : 50W max.

Soldering time : 3.5s max.

7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100 $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%.

Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

10. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. CAPACITANCE CHANGE OF CAPACITORS

Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

· Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

3. PERFORMANCE CHECK BY EQUIPMENT

Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Generally speaking, CLASS 2 ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance. So, the capacitance value may change depending on the operating condition in a equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in a capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.

\land ΝΟΤΕ

1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.

2. You are requested not to use our product deviating from this specification.

1. Application

This specification is applied to Safety Standard Certified Lead Type Disc Ceramic Capacitors Type KX used for General Electric equipment.

Type KX is Safety Standard Certified capacitors of Class X1,Y1.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

• •				
Approval	standard	and	certified	number

	Standard number	*Certified number AC Rated volt. V(r.m.s.)
UL	UL60384-14	E37921
CSA	CSA E60384-14	1343810
VDE	IEC60384-14, EN60384-1	4 40002831
BSI	EN62368-1, IEC60384-14, EN60384-14	KM 37901
SEMKO		1905545 X1:440
DEMKO		D-07250 Y1:250
FIMKO	IEC60384-14, EN60384-14	FI 40129
NEMKO	EN00304-14	P19223458
ESTI		21.0060
IMQ	EN60384-14	V4069
CQC	GB/T6346.14	CQC04001011643
ктс	K60384-14	HU03008-4003, HU03008-4004
-40	per configuration <u>1X</u> <u>KX</u> Temperature Type Ca characteristic name	<u>680 J A4 B C05F</u> bacitance Capacitance Lead Packing Individual tolerance code style code specification
DE	1 denotes X1,Y1 class . erature characteristic	
• Temp	Code	Temperature characteristic
	1X	SL
	Please confirm detailed spe	ification on [Specification and test methods].
● Type r Thi	name s denotes safety certified typ	e name Type KX.

Capacitance

The first two digits denote significant figures ; the last digit denotes the multiplier of 10 in pF. ex.) In case of 680.

 $68 \times 10^{0} = 68 \text{pF}$

• Capacitance tolerance Please refer to [Part number list].

• Lead code

Code	Lead style					
A*	Vertical crimp long type					
B*	Vertical arimp abort type	Lead Length : 5mm				
J*	Vertical crimp short type	Lead Length : 3.5mm				
N*	N* Vertical crimp taping type					
* Please refer to [Part number list]						

Packing style code

COUE	
Code	Packing type
В	Bulk type
А	Ammo pack taping type

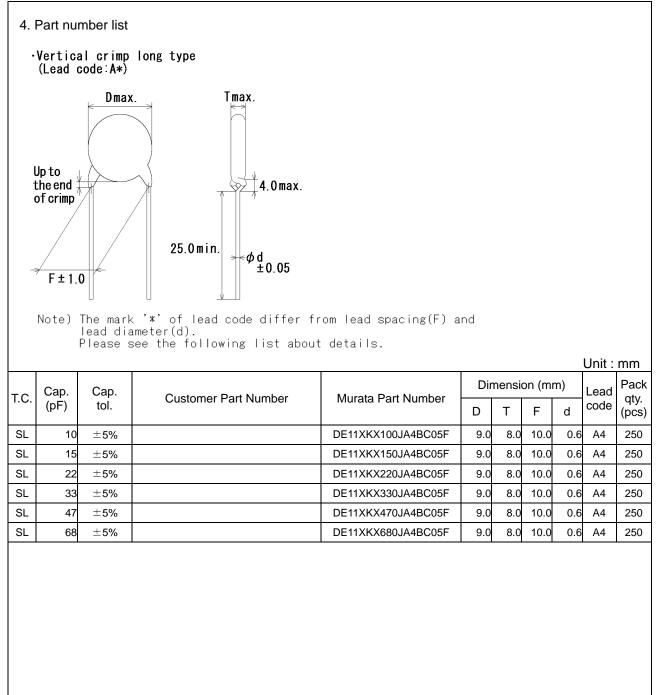
• Individual specification

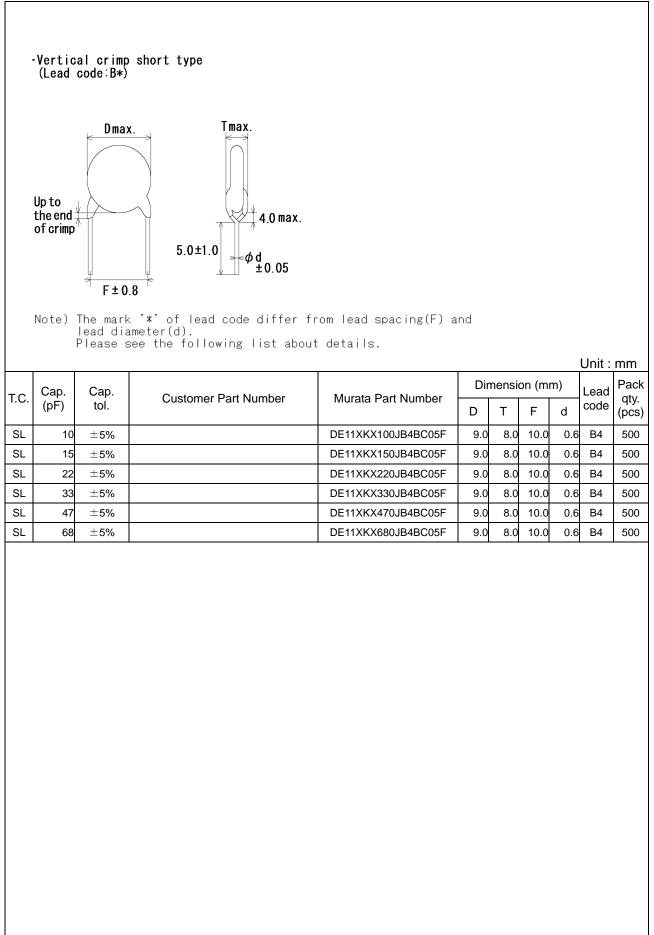
In case part number cannot be identified without 'individual specification', it is added at the end of part number.

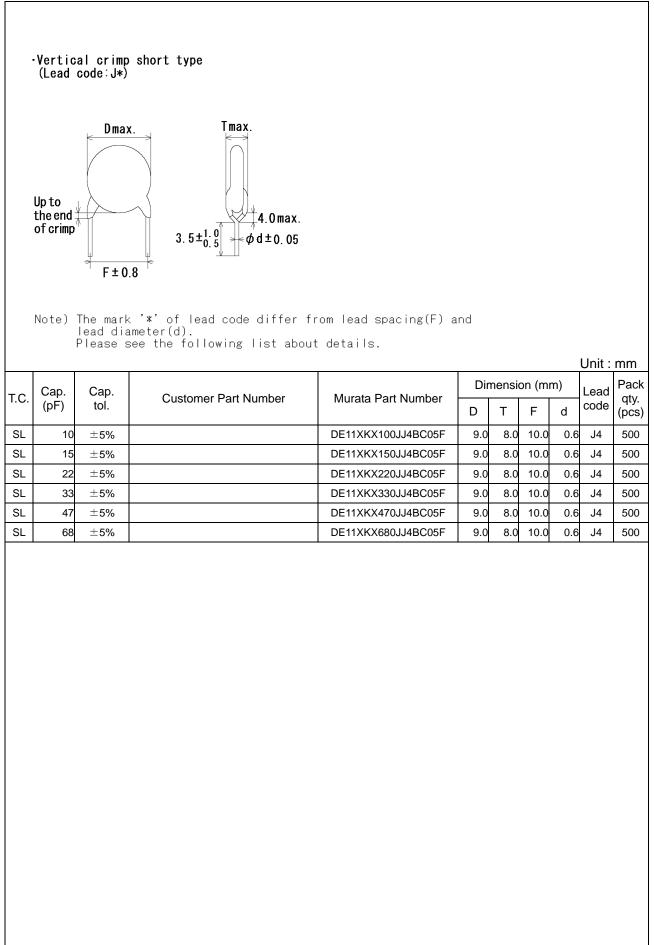
na or part namber.	
Code	Specification
C05F	 Halogen free Br ≤ 900ppm, Cl ≤ 900ppm Br + Cl ≤ 1500ppm CP wire

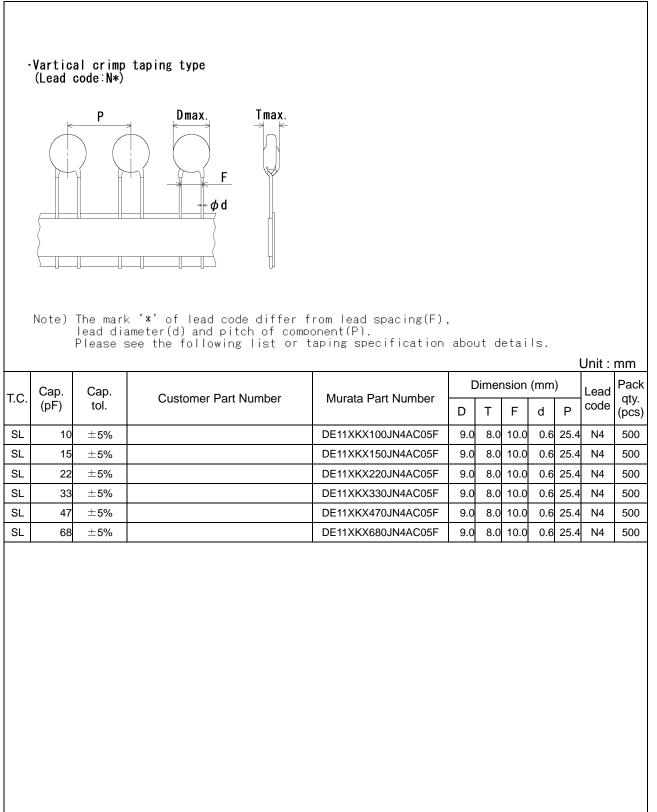
Note) Murata part numbers might be changed depending on lead code or any other changes. Therefore, please specify only the type name(KX) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

3. Marking <right side=""></right>	<reverse side=""></reverse>
Type name	: KX Rated voltage mark : X1 440~
Nominal capacitance	: Actual value Y1 250~
Capacitance tolerance	: Code CQC Approval mark : 📀
Company name code	: 🝽 15 (Made in Tailand) KTC Approval mark : 👔
Manufacturing year	: Letter code (The last digit of A.D. year.)
Manufacturing month	: Code
3	(Feb./Mar. $\rightarrow 2$ Aug./Sep. $\rightarrow 8$
	Apr./May \rightarrow 4 Oct./Nov. \rightarrow O
	$[Jun./Jul. \rightarrow 6 Dec./Jan. \rightarrow D$
UL Approval mark	
CSA Approval mark	
VDE Approval mark	:
BSI Approval mark	: BSI
SEMKO Approval mark	: S (Example)
DEMKO Approval mark	: O <right side=""> <reverse side=""></reverse></right>
FIMKO Approval mark	
NEMKO Approval mark	(\mathbb{N}) / BSI (\mathbb{D}^{E}) / X1 440~
ESTI Approval mark	
IMQ Approval mark	
Class code	
Halogen free mark	
Rated voltage mark	: 250~







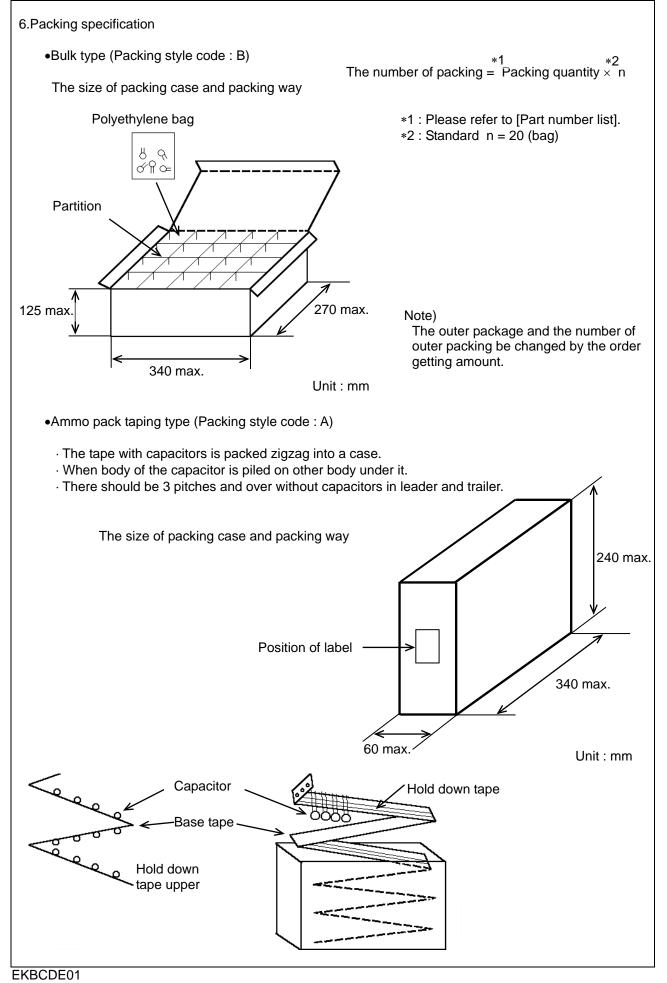


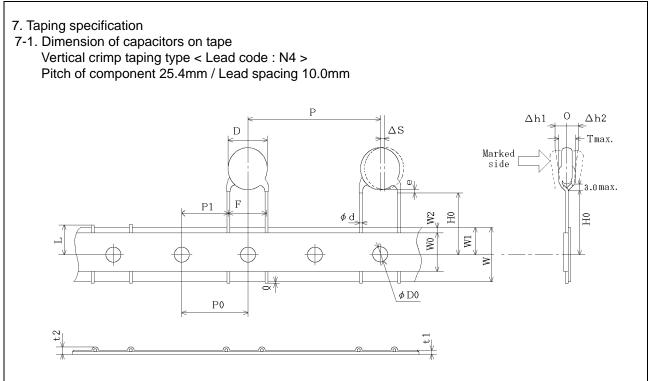
5. S	pecification and	test methods								
No.			Specification			Test method				
1	Appearance and dimensions		No marked defect on appearance form and dimensions.			The capacitor should be inspected by naked eyes for visible evidence of defect. Dimensions should be measured with slide calipers.				
2	Marking			Part number list	i].					
2 3	Marking Dielectric	Between lead	To be easily le No failure.	gible.			capacitor si			y naked eyes. d when
C	strength	wires				AC4		.)<50/60Hz		d between th
		Body	No failure.				the termin		capacitor s	hould be
		insulation					ected toget			V
							ly wrapped			X
						the b	ody of the o	capacitor	Metal 🖉	A About
							e distance o t 3 to 6mm		foil	3 to 6
							each termi		0000	Metal
							, the capac			
						conta diam	ainer filled v	vith metal	balls of abo	out 1mm
								V (r.m.s.)<	50/60Hz>	is applied for
						60 s	between th			
1	Insulation Register		40.000 Mo min			60 s between the capacitor lead wires and metal balls. The insulation resistance should be measured w		noourod with		
4	Insulation Resista	1100 (I.N.)	10000MΩ min		The insulation res DC500±50V within					
						DC500 \pm 50V within 60 \pm 5 s of charging. The voltage should be applied to the capacitor through a resistor of 1M Ω .				
	Ossasita			d to be used						
5	Capacitance		Within specifie	u tolerance.		The capacitance should be measured at 20° C v 1 ± 0.1 MHz and AC5V(r.m.s.) max			a at 20°C with	
6	Q		400+20C* ² min.(30pF under) 1000min. (30pF min.)			The (ຊ should be	e measure		with 1±0.1MH
7	Temperature chara	mperature characteristic		+350 to -1000 ppm/°C		and AC5V(r.m.s.) max The capacitance measurement should be made at				
			(Temp. range :	+20 to +85°C)		each	step speci	fied in Tab	le.	
				Step		1	2	3	4	5
				Temp.(°C)	2	0±2	-25±2	20±2	85±2	20±2
8	Active flammability	y	The cheese-cl on fire.	oth should not be		The capacitors should be individually wrapped in least one but more than two complete layers of cheese-cloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 s. The UAc should be maintained for 2min after the last discharge. $\underbrace{s_1 \\ r_r \\ s_2 \\ u_{AC} \\ t_3 \\ t_4 \\ t_5 \\ u_{AC} \\ t_3 \\ t_4 \\ t_5 \\ u_{AC} \\ u_{AC} \\ t_5 \\ u_{AC} \\ u_$			e layers of the subjected en successive hould be charge. R ct tr sciloscope % 10kV choke kV	
"C"	expresses nominal	capacitance value	e(pF)				Ux	5KV		ime

10 Vibration resistance Appearance Q No marked defect. The capacitor is held by its body in supended from each of the termination is supended from each of the termination. The body of the capacitor is held by its body in supended from each of the termination. The body of the capacitor is held by its body in supended from each of the termination. The body of the capacitor is held by its body in supended from each of the termination. The body of the capacitor is held by its body in the vertices one bead. One bend immediately followed by a second b in the opposet direction. Q 10 Vibration resistance Appearance Q No marked defect. Capacitance Within the specified tolerance. Q The capacitor should be firmly soldered to the supporting lead wire and vibration which is 10 toler ampletion frequency range, 15mm i toler ampletion frequency range, 15mm i tone ampletion frequency range, 15mm i tol				Reference only	
terminations Capacitor should not be broken. gradually to each lead write in the radial direction. Bending Bending Second provide the second provide the provide the provide the provide the provide the termination. With the termination in the module analysis of the termination. 10 Vibration Appearance No marked defect. The capacitor is held yould be firmly soldered to the second provide the p			-		
Bending With the termination in its control. With the termination is the body in such a manner the axis of the termination. Soldering effect 10 Vibration Appearance No marked defect. 10 Vibration Appearance No marked defect. 10 Vibration Appearance No marked defect. 11 Solderability of leads Lead wire should be soldered in the axial of the toring solder of the composite direction. 11 Solderability of leads Lead wire should be soldered in the capacitor should be firmly soldered to the axial direction over 3/4 of the circument in the composite direction. 11 Solderability of leads Lead wire should be soldered in the circumferential direction. 12 Solderability of leads Lead wire should be soldered in the circumferential direction. 12 Solderability of leads Lead wire should be soldered in the circumferential direction. 13 Soldering effect Appearance No marked defect. 13 Soldering effect Appearance No marked defect. 13 Soldering effect Appearance No marked defect. 11 Delectric Solder imagesomatics	9		Iensile		gradually to each lead wire in the radial direction of
Image: stand of the s			Bending		With the termination in its normal position, the capacitor is held by its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the
10 Mbration resistance Appearance Q No marked defect. The capacitor should be firmly soldered to the supporting lead wire and whoration which is 10 544 amplied wire and whoration which is 10 544 amplied for a total of 6 h; 2 h each in a mutually perpendicular directions. 11 Solderability of leads Lead wire should be soldered With wirrormy coated on the axid direction over 344 of the circumferential direction. The lead wire solub capacitor should be soldered wirror a total of 6 h; 2 h each in a mutually perpendicular directions. 12 Soldering effect (Non-preheat) Appearance change No marked defect. Solder terme wirror a solder 1: 245±5°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±7°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±7°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±7°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±7°C 10± The deplot per to about 1.5 to 2.0mm from the root of lead wires. 13 Soldering effect (On-preheat) Appearance change No marked defect. Solder temperature: 35±0.5° the deplot per to about 1.5 to 2.0mm from the root of lead wires. 13 Soldering effect (On-preheat) Appearance trength No marked defect. First the capacitor should be stored at 120+0.7° to about 1.5 to 2.0mm from the root of lead wires. 13 Soldering effect (On-preheat) Appearance trength No marked defect. First the capacitor should be stored at 120+0.7° to about to the stored at 120+0.7° to about to the stored at 120+0.7° tore condition for 24±2.1° to about to the stored at 12					The body of the capacitor is then inclined, within a period of 2 to 3 s, through an angle of approximately 90° in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.
resistance Capacitance Within the specified tolerance. Q supporting lead wire and vibration memory range.1.5mm i total amplitude, and bout 1min in the rate of vibration frequency range.1.5mm i total amplitude, and bout 1min in the rate of vibration change from 10Hz to 55Hz and back 10Hz is applied for a total of 6 h. 2 h each in a multually perpendicularizet of 5 h. 2 h each in a multually perpendicularizet of 5 h. 2 h each in a multually perpendicularizet of a capacitor should be dipped than older for 2±0.5 k. In both cases the de dipping is up to about 1.5 to 2.0mm from the ra- lead wires. 11 Soldering effect (Non-preheat) Appearance No marked defect. 12 Soldering effect (Non-preheat) Appearance No marked defect. 13 Soldering effect (Non-preheat) Appearance No marked defect. 13 Soldering effect (On-preheat) Appearance No marked defect. 13 Soldering effect (On-preheat) Appearance No marked defect. 13 Soldering effect (On-preheat) Appearance No marked defect. First the capacitor should be stored a 85±2°C for 1 h, then placed a "room condition. 13 Soldering effect (On-preheat) Appearance No marked defect. First the capacitor should be stored a 85±2°C for 1 h, then placed a "room condition. 13 Soldering effect (On-preheat) Per item 3 First the capacitor should be stored a 85±2°C for 1 h, t	10	Vibration	Appearance	No marked defect.	The capacitor should be firmly soldered to the
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(Non-preheat) Capacitance change Within ±10% Immersion time : 3.5±0.5 s (In case of 260±5°C : 10± The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. 1.R. 1000MΩ min. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. 1.8. 1000MΩ min. Per item 3 1.3 Soldering effect (On-preheat) Appearance change No marked defect. 1.8. 1000MΩ min. First the capacitor should be stored a *'room condition. 1.13 Soldering effect (On-preheat) Appearance change No marked defect. 1.18. 1000MΩ min. First the capacitor should be stored at 120+0/- for 60+0/-5 s. 1.18. 1000MΩ min. First the capacitor should be stored at 120+0/- for 60+0/-5 s. 1.18. 1000MΩ min. First the capacitor should be stored at 120+0/- for 60+0/-5 s. 1.18. 1000MΩ min. First the capacitor should be stored at 120+0/- for 60+0/-5 s. 1.19. Dielectric strength Per item 3 1.18. 1000MΩ min. First the capacitor should be stored at 120+0/- for 60+0/-5 C. 1.19. Pre-treatment : Capacitor should be stored at 120+0/- for 60+0/-5 C. Solder of 260+0/-5 C. 1.19. Pre-treatment : Capacitor should be stored at 120+0/- for			s	With uniformly coated on the axial direction over 3/4 of the circumferential direction.	molten solder for 2±0.5 s. In both cases the depth of dipping is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder : 245±5°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±5°C H63 Eutectic Solder
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Dielectric strength Per item 3 1.5 to 2.0mm from the root of lead wires. 1.5 to 2.0mm from the root of lead wires. I.5 to 2.0mm from the root of lead wires. Image: the strength Image: the strength Image: the strength Image: the strength Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a "from condition for 24±2 h before initial measurements. Image: the strength Appearance change No marked defect. Image: the strength First the capacitor should be stored at 120+0/- for 60+0/-5 s. Then, as in figure, the lead wires should be immersed solder of 260+0/-5°C up to 1.5 to 2.0 from the root of terminal for 7.5+0/-1 s. Image: the strength Per item 3 Image: the strength Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a "froom condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a "froom condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 2 h at "froom condition.		(Non-preheat)	change		(In case of 260±5°C : 10±1 s)
strength strength 13 Soldering effect (On-preheat) Appearance 14 1000MΩ min. 15 1000MΩ min. 16 Dielectric strength 17 Per item 3 18 Pre-treatment : 19 Capacitor is ould be stored at 120+0/- for 60+0/-5 s. 19 Then, as in figure, the lead wires should be immersed solder of 260+0/-5°C up to 1.5 to 2.0 from the root of terminal for 7.5+0/-1 s. 19 Dielectric strength Per item 3 10 Per item 3 10 Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a "from condition for 24±2 h before initial measurements. 10 Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a "from condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored at 2+10-0-for or on condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored fr			-		
13 Soldering effect (On-preheat) Appearance No marked defect. First the capacitor should be stored at 120+0/- for 60+0/-5 s. 13 Soldering effect (On-preheat) Appearance No marked defect. 13 Soldering effect (On-preheat) Appearance No marked defect. 13 Soldering effect (On-preheat) Appearance No marked defect. 13 Dielectric strength Per item 3 First the capacitor should be stored at 120+0/- for 60+0/-5 s. 19 I.R. 1000MΩ min. Then, as in figure, the lead wires should be immersed solder of 260+0/-5°C up to 1.5 to 2.0 from the root of terminal for 7.5+0/-1 s. 19 Per item 3 Per item 3 10 Per item 3 Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a *1*0° con condition for 24±2 h before initial measurements. 10 Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed a *1*0° con condition for 24±2 h before initial measurements. *1 "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					Insulating 1.5 1.5 to 2.0mm Molten solder
(On-preheat) Capacitance change Within ±10% for 60+0/-5 s. I.R. 1000MΩ min. Then, as in figure, the lead wires should be immersed solder of 260+0/-5°C up to 1.5 to 2.0 from the root of terminal for 7.5+0/-1 s. Dielectric strength Per item 3 Pre-treatment : Capacitor should be stored a 85±2°C for 1 h, then placed a *1 room condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 2.4±2 h before initial measurements. *1"room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					85±2°C for 1 h, then placed at * ¹ room condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 1 to 2 h at * ¹ room condition.
change I.R. 1000MΩ min. Dielectric Per item 3 strength Image: Strength Thermal Image:	13				
I.R. 1000MΩ min. Dielectric Per item 3 from the root of terminal for 7.5+0/-1 s. Thermal insulating insulating istrength Pre-treatment : Capacitor should be stored a 85±2°C for 1 h, then placed a *1room condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 2.6 ± 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa		(On-preneat)		Within ±10%	
Pre-treatment : Capacitor should be stored a 85±2°C for 1 h, then placed a *1 room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa				1000MΩ min.	immersed solder of 260+0/-5°C up to 1.5 to 2.0mm
Thermal insulating 1.5 to 20mm Pre-treatment : Capacitor should be stored a 85±2°C for 1 h, then placed a *1room condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 2 h at *1room condition. *1 "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa				Per item 3	from the root of terminal for 7.5+0/-1 s.
* ¹ "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa			strength		insulating 1.5
*1 "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					85±2°C for 1 h, then placed at * ¹ room condition for 24±2 h before initial measurements. Post-treatment : Capacitor should be stored for 1 to
	* ¹ "roo * ² "C"	om condition" Temper expresses nominal of	ature: 15 to 35°0 capacitance valu	C, Relative humidity: 45 to 75%, Atm e(pF)	

			Reference only			
No.	Item	<u></u>	Specification	Test method		
14	Flame test		Cycle Time 1 to 4 30 s max. 5 60 s max.		CycleTime1 to 430 s max.	The capacitor should be subjected to applied flame for 15 s. and then removed for 15 s until 5 cycle.
15	Passive flammabilit	у	The burning time should not be exceeded the time 30 s. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position which best promotes burning. Time of exposure to flame is for 30 s. Length of flame : 12±1mm Gas burner : Length 35mm min. Inside Dia. 0.5±0.1mm Outside Dia. 0.9mm max. Gas : Butane gas Purity 95% min.		
16	Humidity (Under steady state)	Appearance Capacitance change Q I.R. Dielectric	No marked defect. Within ±5% 275+5/2C* ² min.(30pF under) 350min. (30pF min.) 3 000MΩ min. Per item 3	Set the capacitor for 500±12 h at 40±2°C in 90 to 95% relative humidity. Post-treatment : Capacitor should be stored for 1 to 2 h at *1room condition.		
17	Humidity loading	strength Appearance Capacitance change Q I.R. Dielectric strength	No marked defect. Within \pm 5% 275+5/2C* ² min.(30pF under) 350min. (30pF min.) 3 000M Ω min. Per item 3	Apply the rated voltage for 500±12 h at 40±2°C in 90 to 95% relative humidity. Post-treatment : Capacitor should be stored for 1 to 2 h at *1room condition.		
* ¹ "ro * ² "C"	om condition" Tempe ' expresses nominal	rature: 15 to 35°(capacitance valu	2, Relative humidity: 45 to 75%, Atm e(pF)	ospheric pressure: 86 to 106kPa		

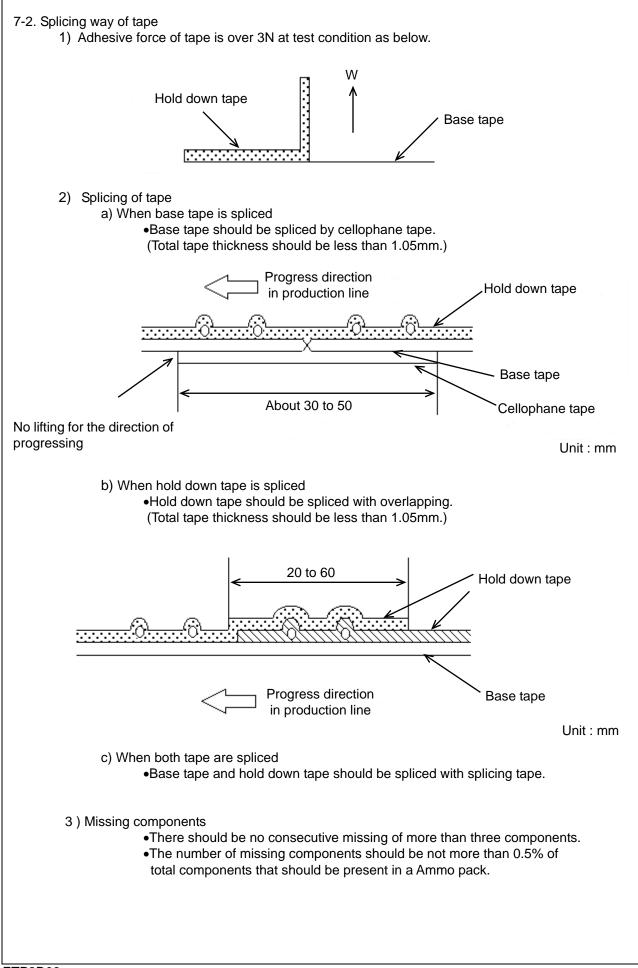
			Reference only							
No.	Item		Specification	Ļ			Test m	ethod		
18	Life	Appearance Capacitance change	No marked defect. Within ±20%	E	ach ir		e I capacitor s or three time			
		I.R.	3000MΩ min.				life test.	.s. men		1013
		Dielectric strength	Per item 3		10 9 5	8 <u>(%)</u>) = 1.7 µ s=1.6 Ilue (T2) = 50 µ	
							r2	t		
				f	or a pe	pacitors pacitors	are placed i 1 000 h. oven is maint		•	
				T te	⁻ hroug o a AC	hout the 425V(r.	, and relative e test, the ca m.s.)<50/60 ency, except	pacitors a Hz> alter	are subjec nating vol	ted age
						-	ncreased to			
	Tourse i i	A					2 h at *1rc	oom conc	lition.	
19	Temperature and immersion cycle	Appearance Capacitance change	No marked defect. Within ±5%	С	ycles,	then co	should be su onsecutively			
		Q	275+5/2C*2min.(30pF under)	<	Temp	erature	cycle>			_
		I.R.	350min. (30pF min.)			Step	Temperatu		Time	
		Dielectric	3000MΩ min. Per item 3	-		1 2	-40+0/ Room te		30 min 3 min	
		strength			·	3	+125+3		30 min	
		_			[4	Room te		3 min]
								C١	cle time :	5 cycle
				<	Imme	rsion cy	′cle>		1.	
					Step	-	perature(°C)	Time	Immer wate Clea	ər
					1	+6	65+5/-0	15 min	wat	ər
					2		0±3	15 min	Sa wat	
						1		Су	/cle time :	
				F	Pre-trea	atment		or 1 h, the	be stored a en placed or 24±2 h.	
							t: Capacitor 24 h at *	room coi		or 4 to
* ¹ "roo * ² "C"	om condition" Temper ' expresses nominal c	ature: 15 to 35°C apacitance value	C, Relative humidity: 45 to 75%, Atm e(pF)	osp	oheric	pressur	e: 86 to 106	кРа		
L										





Unit : mm

Code	Dimensions	Remarks
Р	25.4±2.0	
P0	12.7±0.3	
F	10.0±1.0	
P1	7.7±1.5	
D	Please refer to [P	'art number list].
ΔS	0±2.0	They include deviation by lead bend .
W	18.0±0.5	
W1	9.0±0.5	Deviation of tape width direction
H0	18.0± ^{2.0} ₀	
Q	+0.5~-1.0	
φD0	4.0±0.1	
φd	0.60±0.05	
t1	0.6±0.3	
t2	1.5 max.	They include hold down tape thickness.
∆h1		
∆h2		
L	11.0± ⁰ _{1.0}	
W0	11.5 min.	
W2	1.5±1.5	
е	Up to the end of c	rimp
Т	Please refer to [P	'art number list].
	P P0 F P1 D ΔS W W1 H0 Q φD0 φD0 φD0 φd t1 t2 Δh1 Δh2 L W0 W2 e	P 25.4±2.0 P0 12.7±0.3 F 10.0±1.0 P1 7.7±1.5 D Please refer to [P ΔS 0±2.0 W 18.0±0.5 W1 9.0±0.5 H0 18.0± $_0^{2.0}$ Q +0.5~-1.0 ϕ D0 4.0±0.1 ϕ d 0.60±0.05 t1 0.6±0.3 t2 1.5 max. Δ h1 2.0 max. Δ h2 11.0± $_{1.0}^{0}$ W0 11.5 min. W2 1.5±1.5 e Up to the end of c



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DE11XKX220JN4AC05	F DE11XKX330JN4AC05	F DE11XKX100JB4BC05	F DE11XKX470JA4BC05F
DE11XKX220JA4BC05F	DE11XKX100JN4AC05F	DE11XKX680JN4AC05F	DE11XKX330JA4BC05F
DE11XKX680JA4BC05F	DE11XKX150JB4BC05F	DE11XKX100JA4BC05F	DE11XKX150JA4BC05F
DE11XKX220JB4BC05F	DE11XKX150JN4AC05F	DE11XKX330JB4BC05F	DE11XKX470JB4BC05F
DE11XKX680JB4BC05F	DE11XKX470JN4AC05F	DE11XKX330JJ4BC05F	DE11XKX220JJ4BC05F
DE11XKX470JJ4BC05F	DE11XKX150JJ4BC05F	DE11XKX680JJ4BC05F	DE11XKX100JJ4BC05F