# **MOSFET** – N-Channel, POWERTRENCH

# 150 V, 167 A, 5.9 m $\Omega$

# FDH055N15A

#### Description

This N-Channel MOSFET is produced using ON Semiconductor's advanced POWERTRENCH<sup>®</sup> process that has been tai-lored to minimize the on-state resistance while maintaining superior switching performance.

#### Features

- $R_{DS(on)} = 4.8 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 120 \text{ A}$
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low RDS(on)
- High Power and Current Handling Capability
- This Device is Pb-Free and is RoHS Compliant

#### Applications

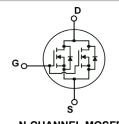
- Synchronous Rectification for ATX / Sever / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter



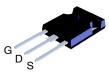
## **ON Semiconductor®**

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V <sub>DS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
150 V	5.9 m $\Omega$ @ 10 V	167 A

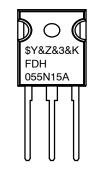


N-CHANNEL MOSFET



TO-247-3LD CASE 340CK

#### MARKING DIAGRAM



= ON Semiconductor Logo
= Assembly Plant Code
= Numeric Date Code
= Lot Code
= Specific Device Code

#### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = $25^{\circ}$ C unless otherwise noted)

Symbol	Parameter		FDH055N15A	Unit	
V <sub>DSS</sub>	Drain to Source Voltage		150	V	
V <sub>GSS</sub>	Gate to Source Voltage	– DC	±20	V	
		– AC (f > 1 Hz)	±30		
I <sub>D</sub> Drain Current	– Continuous (T <sub>C</sub> = 25°C, Silicon Limited)	167 (Note 1)	A		
	– Continuous (T <sub>C</sub> = 100°C, Silicon Limited)	118			
	– Continuous (T <sub>C</sub> = 25°C, Package Limited)	156			
I <sub>DM</sub>	Drain Current	- Pulsed (Note 2)	668	А	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 3)		835	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 4)		6.0	V/ns	
P <sub>D</sub> Power Dissipation	Power Dissipation	(T <sub>C</sub> = 25°C)	429	W	
		– Derate above 25°C	2.86	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		–55 to + 175	°C	
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Second		300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Calculated continuous current based on maximum allowable junction temperature, Package limitation current is 156 A.

2. Repetitive Rating: Pulse width limited by maximum anowable function temperature. 3. Starting  $T_J = 25^{\circ}$ C, L = 3 mH,  $I_{AS} = 23.6$  A 4.  $I_{SD} \le 120$  A, di/dt  $\le 200$  A/s,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}$ C.

#### **THERMAL CHARACTERISTICS**

Symbol	Parameter	FDH055N15A	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.35	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40	

#### PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Package Method	Reel Size	Tape Width	Quantity	
FDH055N15A	FDH055N15A	TO-247-3LD	Tube	N/A	N/A	30 Units	

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARA	ACTERISTICS		-	-	-	-
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = 250 \ \mu A, \ V_{GS} = 0 \ V$	150	-	-	V
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 µA, Referenced to 25°C	-	0.1	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ = 120 V, $V_{GS}$ = 0 V	-	-	1	μΑ
		$V_{DS}$ = 120 V, $T_{C}$ = 150°C	-	-	500	
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS}$ = $\pm 20$ V, $V_{DS}$ = 0 V	-	-	±100	nA
ON CHARAG	CTERISTICS					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, \ I_D = 250 \ \mu A$	2.0	-	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 120 A	-	4.8	5.9	mΩ
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 120 A	-	219	-	S
DYNAMIC C	HARACTERISTICS					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V},$	-	7100	9445	pF
C <sub>oss</sub>	Output Capacitance	f = 1 MHz	-	664	885	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	23	35	pF
C <sub>oss(er)</sub>	Energy Related Output Capacitance	$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}$	-	1159	-	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	$V_{DS} = 75 \text{ V}, \text{ I}_{D} = 120 \text{ A}, \\ V_{GS} = 10 \text{ V} \\ \text{(Note 5)}$	-	92	-	nC
Q <sub>gs</sub>	Gate to Source Gate Charge		-	31	-	nC
Q <sub>gs2</sub>	Gate Charge Threshold to Plateau		-	15	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge		-	16	-	nC
ESR	Equivalent Series Resistance(G-S)	f = 1 MHz	-	1.2	-	Ω
SWITCHING	CHARACTERISTICS	·				-
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 75 V, I <sub>D</sub> = 120 A,	-	35	80	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 10 V, R <sub>G</sub> = 4.7 Ω (Note 5)	-	67	144	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	71	152	ns
t <sub>f</sub>	Turn-Off Fall Time		-	21	52	ns
DRAIN-SOU	RCE DIODE CHARACTERISTICS	·				
ا <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	_	167 (Note 1)	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	668	А
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 120 A	-	-	1.25	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{SD} = 120 \text{ A}, V_{DS} = 75 \text{ V},$	-	105	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt = 100 A/μs	-	342	-	nC
Q <sub>rr</sub>	Reverse Recovery Charge	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 30 A, V <sub>DS</sub> = 75 V, dI <sub>F</sub> /dt = 100 A/μs	-	348	-	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Essentially Independent of Operating Temperature Typical Characteristics.

#### **TYPICAL CHARACTERISTICS**

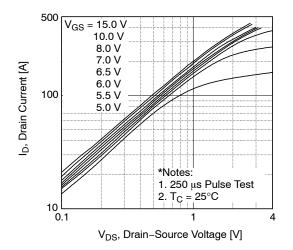


Figure 1. On-Region Characteristics

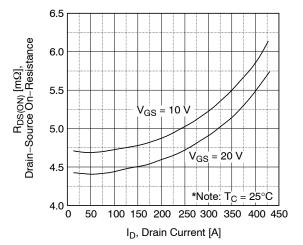


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

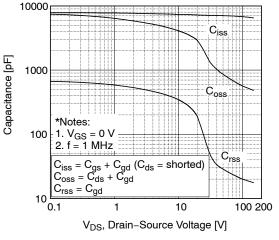


Figure 5. Capacitance Characteristics

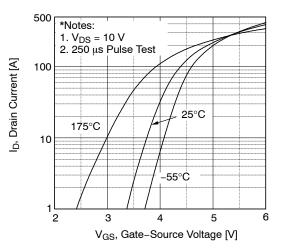


Figure 2. Transfer Characteristics

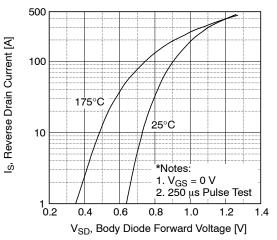


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

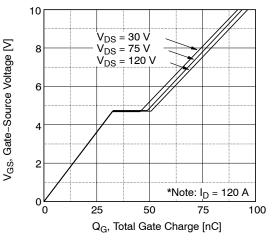
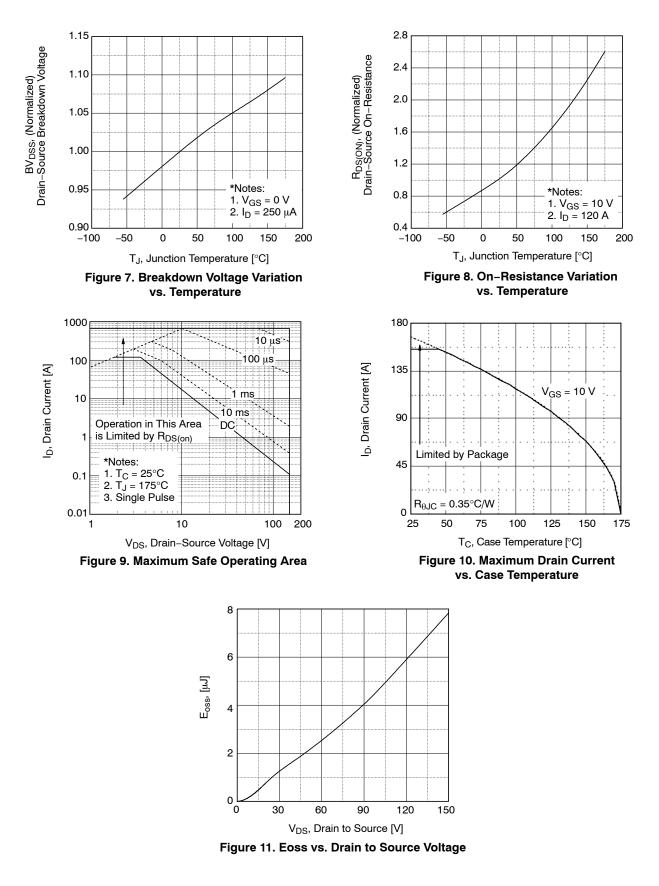


Figure 6. Gate Charge Characteristics

#### TYPICAL CHARACTERISTICS (continued)



#### TYPICAL CHARACTERISTICS (continued)

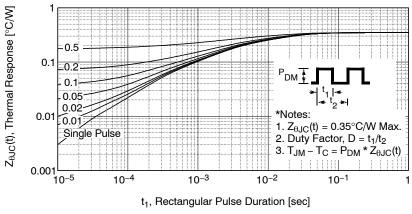
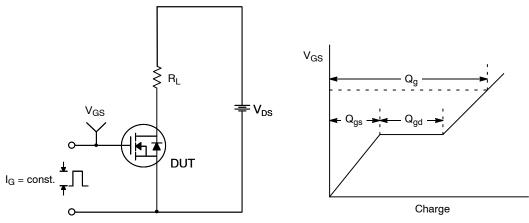


Figure 12. Transient Thermal Response Curve





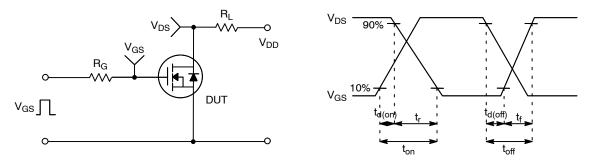


Figure 14. Resistive Switching Test Circuit & Waveforms

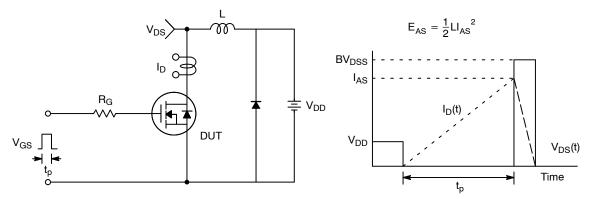


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

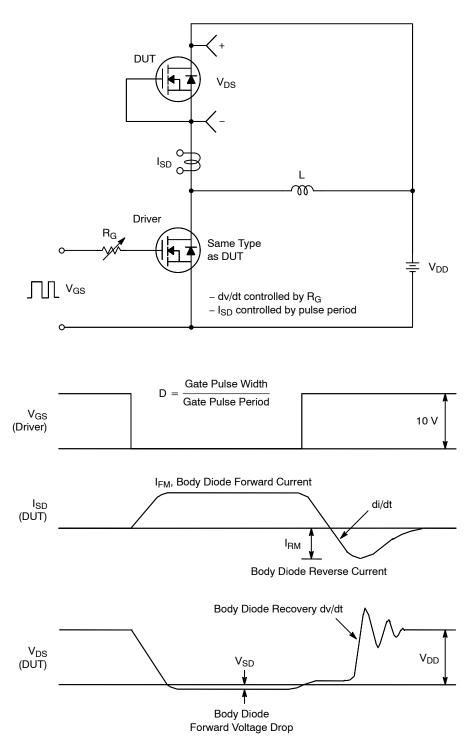


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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