



#### 0.2A SBR<sup>®</sup> SUPER BARRIER RECTIFIER

#### Product Summary (@ TA = +25°C)

V <sub>RRM</sub> (V)	I <sub>0</sub> (A)	V <sub>F(MAX)</sub> (V)	Ι <sub>R(MAX)</sub> (μΑ)
30	0.2	0.61	2

# Applications

- SMPS
- DC-DC Converter
- Freewheeling Diodes
- Reverse Polarity Protection

### **Features and Benefits**

- Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SOD-523
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin Annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.002 grams (Approximate)

SOD-523



Top View

# **Ordering Information**

Part Number	Case	Packaging
SBR0230T5-7 (Note 4)	SOD-523	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

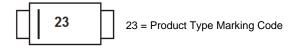
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

5. Dispensed in every other cavity of the tape.

# **Marking Information**







# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Rectified Output Current (See Figure 1)	lo	0.2	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	5	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 6)	$R_{ heta}JA$	400	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-65 to +150	°C

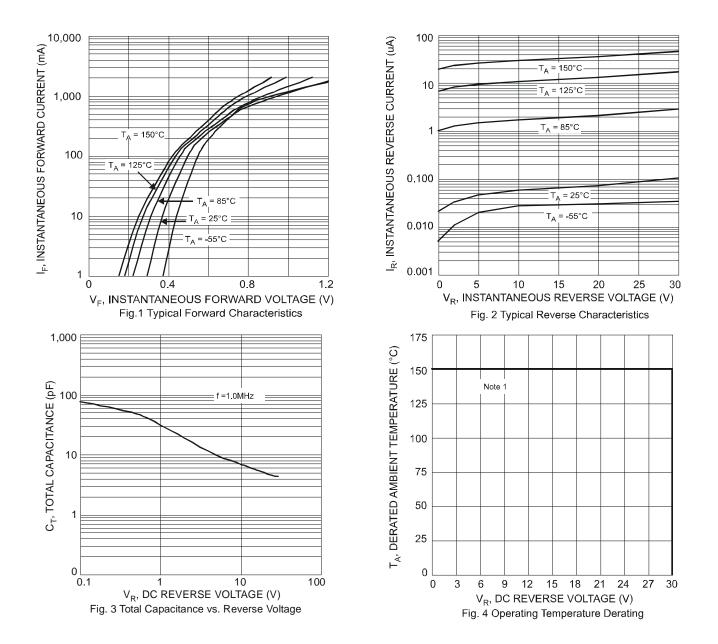
# Electrical Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V <sub>(BR)R</sub>	30	-	-	V	I <sub>R</sub> = 400μA
Forward Voltage Drop	V <sub>F</sub>	-	0.50 0.46 0.57 0.55	0.54 0.49 0.61 0.58	V	$I_{F} = 0.1A, T_{J} = +25^{\circ}C$ $I_{F} = 0.1A, T_{J} = +85^{\circ}C$ $I_{F} = 0.2A, T_{J} = +25^{\circ}C$ $I_{F} = 0.2A, T_{J} = +85^{\circ}C$
Leakage Current (Note 7)	I <sub>R</sub>	-	0.2 -	2 0.1	μA mA	V <sub>R</sub> = 30V, T <sub>J</sub> = +25°C V <sub>R</sub> = 30V, T <sub>J</sub> = +125°C
Reverse Recovery Time	t <sub>rr</sub>	-	5	-	ns	$I_F = 10mA$ through $I_R = 10mA$ to $I_R = 1mA$ , $R_L = 100\Omega$

Notes: 6. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf. 7. Short duration pulse test used to minimize self-heating effect.



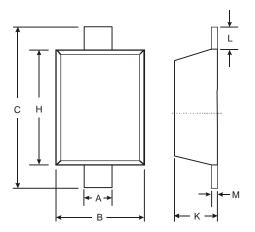
# SBR0230T5





# **Package Outline Dimensions**

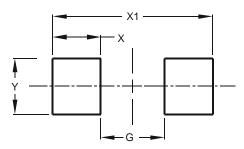
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOD523				
Dim	Min	Max		
Α	0.25	0.35		
В	0.70	0.90		
С	1.50	1.70		
Η	1.10	1.30		
K	0.55	0.65		
L	0.10	0.30		
М	0.10	0.12		
All Dimensions in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	0.80
Х	0.60
X1	2.00
Y	0.70



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