Taiwan Semiconductor

# 6A, 600V - 1000V Standard Bridge Rectifier

### **FEATURES**

- Glass passivated chip junction
- Ideal for automated placement
- UL Recognized File # E-326854
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

### **APPLICATIONS**

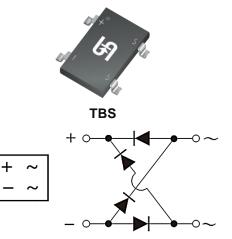
- Switching mode power supply (SMPS)
- Adapters
- Lighting application

### **MECHANICAL DATA**

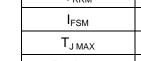
- Case: TBS
- Molding compound meets UL 94V-0 flammability rating
- Matte tin plated leads, solderable per J-STD-002 •
- Meet JESD 201 class 1 whisker test
- Polarity: As marked
- Weight: 0.220g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE UN		
I <sub>F</sub>	6	А	
V <sub>RRM</sub>	600 - 1000 V		
I <sub>FSM</sub>	150 A		
T <sub>J MAX</sub>	150 °C		
Package	TBS		
Configuration	Quad		





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER		SYMBOL	TBS606	<b>TBS608</b>	<b>TBS610</b>	UNIT
Marking code on the device			TBS606	TBS608	TBS610	
Repetitive peak reverse voltage		V <sub>RRM</sub>	600	800	1000	V
Reverse voltage, total rms value		V <sub>R(RMS)</sub>	420	560	700	V
Forward current		I <sub>F</sub>	6		А	
Surge peak forward current single half	t = 8.3ms			150		А
sine-wave superimposed on rated load	t = 1.0ms	IFSM	400			А
Rating for fusing (t<8.3ms)		l <sup>2</sup> t	93.37		A <sup>2</sup> s	
Junction temperature		TJ	-55 to +150		°C	
Storage temperature		T <sub>STG</sub>	-55 to +150		°C	







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THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>θJL</sub>	12	°C/W	
Junction-to-ambient thermal resistance	R <sub>θJA</sub>	47	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	13	°C/W	

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage per diode <sup>(1)</sup>	$I_F = 3A, T_J = 25^{\circ}C$	V <sub>F</sub>	0.90	-	V
	$I_F = 6A, T_J = 25^{\circ}C$		0.96	1.00	V
	$I_F = 3A, T_J = 125^{\circ}C$		0.79	-	V
	$I_F = 6A, T_J = 125^{\circ}C$		0.86	0.96	V
Poweree eurrent @ reted \/ per diade <sup>(2)</sup>	$T_J = 25^{\circ}C$	I <sub>R</sub>	-	2	μA
Reverse current @ rated V <sub>R</sub> per diode <sup>(2)</sup>	T <sub>J</sub> = 125°C		-	200	μA
Junction capacitance per diode	1MHz, V <sub>R</sub> = 4.0V	CJ	51	-	pF

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

## ORDERING INFORMATION

	r	
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
TBS6x	TBS	1,800 / Tape & Reel

Notes:

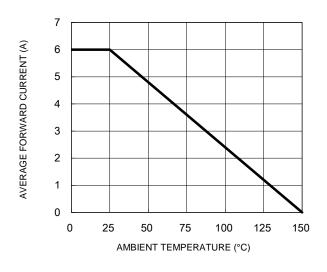
1. "x" defines voltage from 600V(TBS606) to 1000V(TBS610)



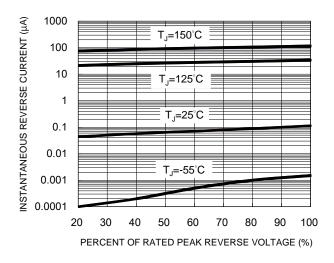
### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve



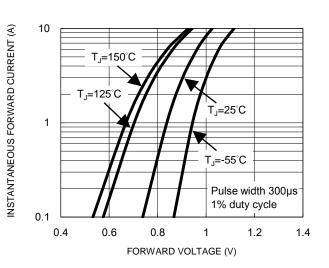
**Fig.3 Typical Reverse Characteristics** 



CAPACITANCE (pF)

Fig.2 Typical Junction Capacitance

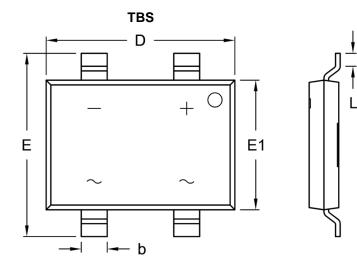




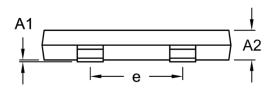


# TBS606 – TBS610 Taiwan Semiconductor

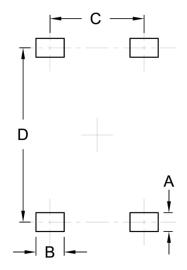
### **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit (mm)		Unit (	(inch)	
	Min.	Max.	Min.	Max.	
A1	0.00	0.15	0.000	0.006	
A2	1.40	1.80	0.055	0.071	
b	1.30	1.50	0.051	0.059	
D	10.00	10.40	0.394	0.409	
E	9.70	10.10	0.382	0.398	
E1	6.80	7.20	0.268	0.283	
е	4.90	5.10	0.193	0.201	
L	0.50	1.10	0.020	0.043	



### SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.00	0.039
В	1.50	0.059
С	5.00	0.197
D	9.25	0.364

### **MARKING DIAGRAM**



- = Date Code YW
- F = Factory Code



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