

Trench gate field-stop IGBT, M series 650 V, 10 A low-loss in TO-220 package

Datasheet - production data

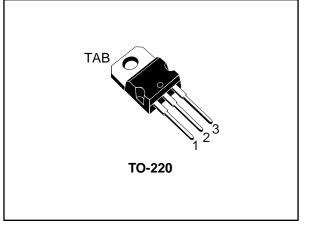
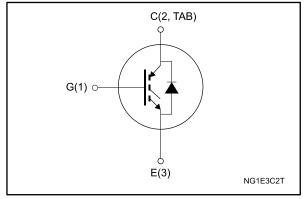


Figure 1: Internal schematic diagram



Features

- 6 µs of short-circuit withstand time
- V_{CE(sat)} = 1.55 V (typ.) @ I_C = 10 A
- Tight parameter distribution
- Safer paralleling
- Positive V_{CE(sat)} temperature coefficient
- Low thermal resistance
- Soft and very fast recovery antiparallel diode
- Maximum junction temperature: T_J = 175 °C

Applications

- Motor control
- UPS
- PFC
- General purpose inverter

Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the M series IGBTs, which represent an optimal balance between inverter system performance and efficiency where low-loss and short-circuit functionality are essential. Furthermore, the positive $V_{CE(sat)}$ temperature coefficient and tight parameter distribution result in safer paralleling operation.

Order code	Marking	Package	Packing
STGP10M65DF2	G10M65DF2	TO-220	Tube

This is information on a product in full production.

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1 Electrical ratings

 Table 2: Absolute maximum ratings

Symbol	Parameter Value			
VCES	Collector-emitter voltage (V _{GE} = 0 V)	650	V	
lc	Continuous collector current at $T_c = 25$ °C	20	А	
IC	Continuous collector current at T _c = 100 °C	10	A	
ICP ⁽¹⁾	Pulsed collector current	40	А	
V_{GE}	Gate-emitter voltage	±20	V	
I_	Continuous forward current at T _C = 25 °C	20	А	
IF	Continuous forward current at T _C = 100 °C	10	A	
IFP ⁽¹⁾	Pulsed forward current	40	А	
Ртот	Total dissipation at $T_c = 25 \ ^{\circ}C$	115	W	
Tstg	Storage temperature range	- 55 to 150	°C	
TJ	Operating junction temperature range	- 55 to 175	C	

Notes:

 $^{(1)}\mbox{Pulse}$ width limited by maximum junction temperature.

Table 3: Thermal data

Symbol	Parameter V		Unit
RthJC	Thermal resistance junction-case IGBT	1.3	
RthJC	Thermal resistance junction-case diode 2.08		°C/W
R _{thJA}	Thermal resistance junction-ambient	abient 62.5	



2 Electrical characteristics

 $T_C = 25$ °C unless otherwise specified

Table 4: Static characteristics						
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage	V_{GE} = 0 V, I _C = 250 µA	650			V
		V_{GE} = 15 V, I_{C} = 10 A		1.55	2.0	
V _{CE(sat)} Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 10 A, T _J = 125 °C		1.9		V	
	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 10 \text{ A},$ T _J = 175 °C		2.1			
		I _F = 10 A		1.5	2.25	
VF	Forward on-voltage	I _F = 10 A, T _J = 125 °C		1.3		V
	I _F = 10 A, T _J = 175 °C		1.2			
V _{GE(th)}	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250 \ \mu A$	5	6	7	V
Ices	Collector cut-off current	$V_{GE} = 0 V, V_{CE} = 650 V$			25	μA
IGES	Gate-emitter leakage current	$V_{CE} = 0 V, V_{GE} = \pm 20 V$			±250	μA

Table 4: Static characteristics

Table 5: Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Cies	Input capacitance		-	840	-	
Coes	Output capacitance	V _{CE} = 25 V, f = 1 MHz, V _{GE} = 0 V	-	63	-	pF
Cres	Reverse transfer capacitance		-	16	-	F.
Qg	Total gate charge	Vcc = 520 V, Ic = 10 A,	-	28	-	
Q _{ge}	Gate-emitter charge	V _{GE} = 0 to 15 V (see Figure 30: " Gate charge test circuit")	-	6	-	nC
Q _{gc}	Gate-collector charge		-	12	-	



Electrical characteristics

	Table 6: IGBT switching characteristics (inductive load)					
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time			19	-	ns
tr	Current rise time			7.4	-	ns
(di/dt) _{on}	Turn-on current slope	V _{CE} = 400 V, I _C = 10 A,		1086	-	A/µs
t _{d(off)}	Turn-off-delay time	$V_{GE} = 400 \text{ V}, \text{ IC} = 10 \text{ A},$ $V_{GE} = 15 \text{ V}, \text{ R}_{G} = 22 \Omega$		91	-	ns
t _f	Current fall time	(see Figure 29: "Test circuit		92	-	ns
Eon ⁽¹⁾	Turn-on switching energy	for inductive load switching")		0.12	-	mJ
E _{off} ⁽²⁾	Turn-off switching energy			0.27	-	mJ
Ets	Total switching energy			0.39	-	mJ
t _{d(on)}	Turn-on delay time	Vce = 400 V, Ic = 10 A,		18	-	ns
tr	Current rise time			9	-	ns
(di/dt) _{on}	Turn-on current slope			890	-	A/µs
t _{d(off)}	Turn-off-delay time	V _{GE} = 15 V, R _G = 22 Ω, T ₋ = 175 °C		90	-	ns
t _f	Current fall time	(see Figure 29: " Test circuit		170	-	ns
Eon ⁽¹⁾	Turn-on switching energy	for inductive load switching")		0.26	-	mJ
E _{off} ⁽²⁾	Turn-off switching energy			0.4	-	mJ
E _{ts}	Total switching energy			0.66	-	mJ
		$\label{eq:VGE} \begin{array}{l} V_{CC} \leq 400 \text{ V}, \text{ V}_{GE} = 13 \text{ V}, \\ T_{Jstart} = 150 \ ^{\circ}\text{C} \end{array}$	10		-	μs
t _{sc}	Short-circuit withstand time	$V_{CC} \le 400 \text{ V}, \text{ V}_{GE} = 15 \text{ V},$ $T_{Jstart} = 150 ^{\circ}\text{C}$	6		-	μs

Notes:

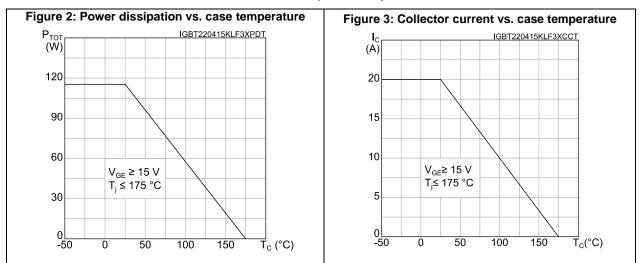
⁽¹⁾Including the reverse recovery of the diode. ⁽²⁾Including the tail of the collector current.

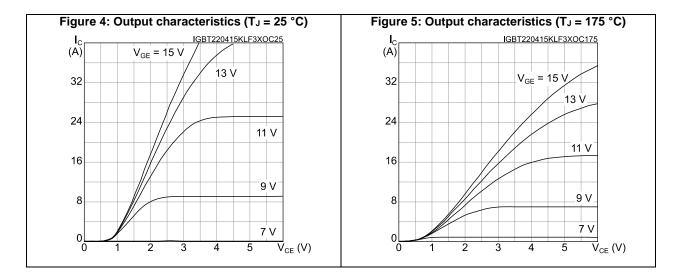
Symbol	Parameter Test conditions		Min.	Тур.	Max.	Unit
trr	Reverse recovery time		-	96	-	ns
Q _{rr}	Reverse recovery charge	$I_F = 10 \text{ A}, V_R = 400 \text{ V},$	-	373	-	nC
Irrm	Reverse recovery current	V _{GE} = 15 V, di/dt = 1000 A/µs	-	13	-	А
dIrr/dt	Peak rate of fall of reverse recovery current during t _b	(see Figure 29: "Test circuit for inductive load switching")	-	661	-	A/µs
Err	Reverse recovery energy		-	52	-	μJ
t _{rr}	Reverse recovery time		-	201	-	ns
Qrr	Reverse recovery charge	I _F = 10 A, V _R = 400 V, V _{GE} = 15 V.	-	1352	-	nC
Irrm	Reverse recovery current	$di/dt = 1000 \text{ A}/\mu\text{s},$ $T_J = 175 ^{\circ}\text{C}$ (see Figure 29: "Test circuit for inductive load switching")	-	19	-	А
dlrr/dt	Peak rate of fall of reverse recovery current during t _b		-	405	-	A/µs
Err	Reverse recovery energy	ior inductive load switching)	-	150	-	μJ

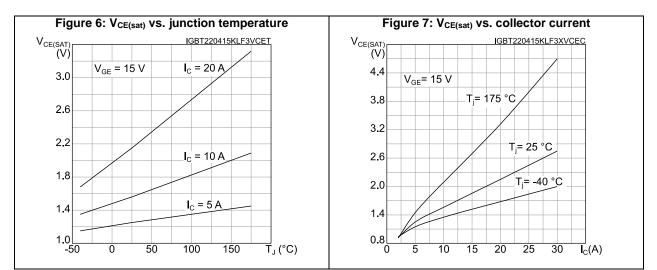
Table 7: Diode switching	characteristics	(inductive load)	•
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2.1 Electrical characteristics (curves)



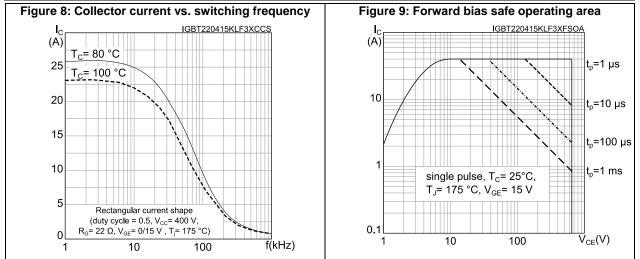


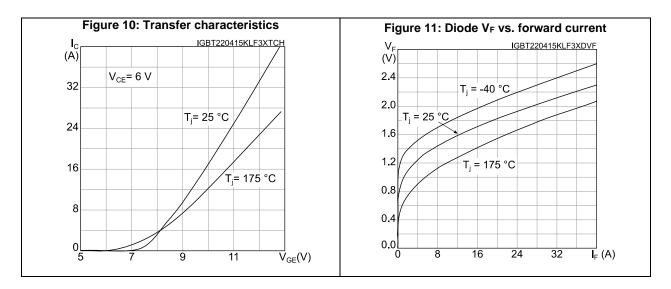


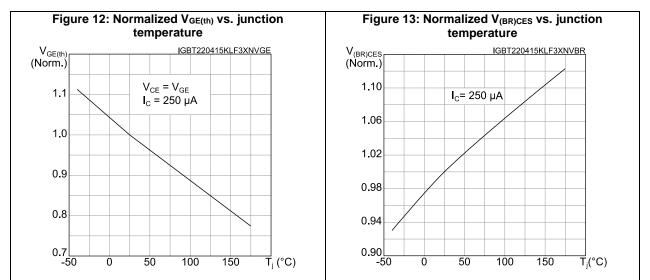


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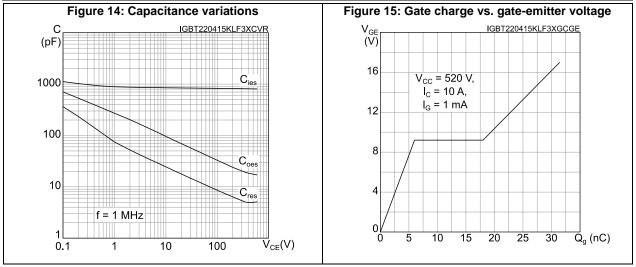
Electrical characteristics

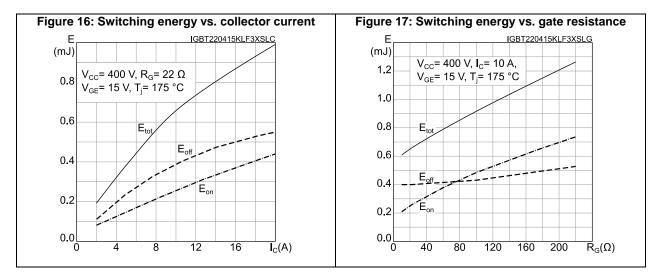


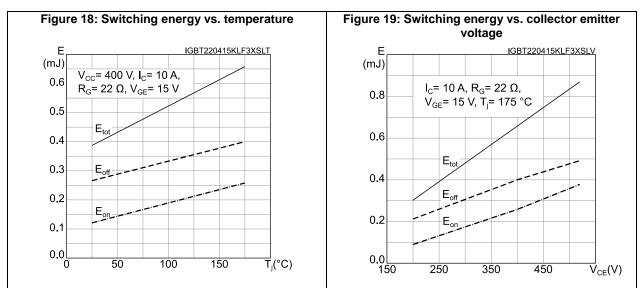








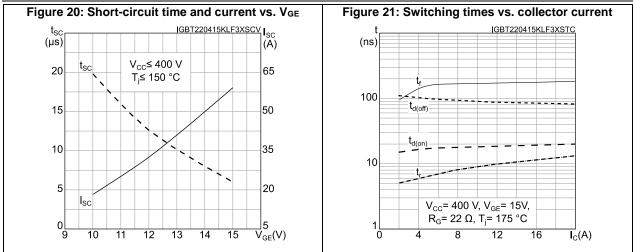


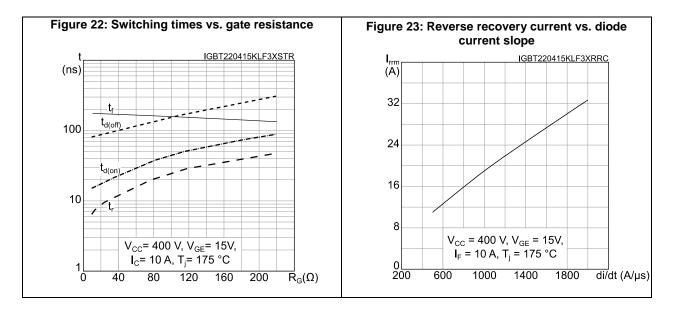


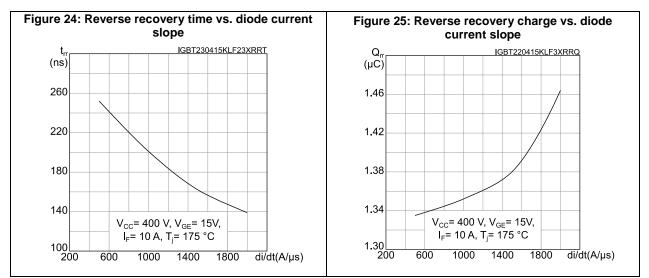


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Electrical characteristics





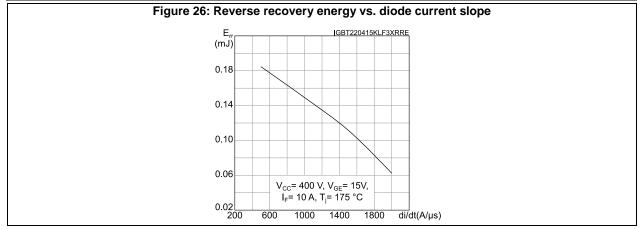


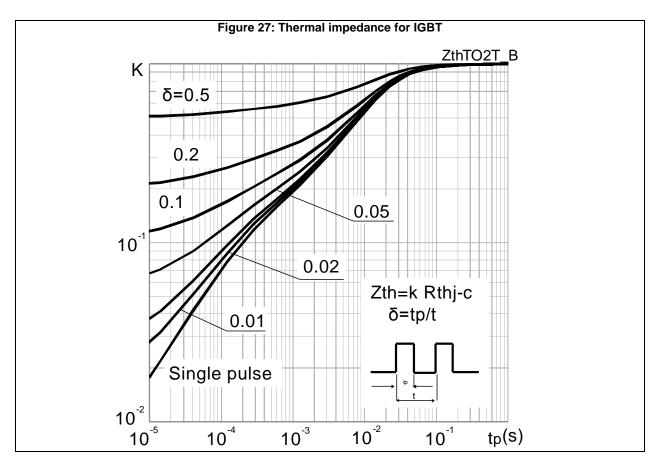
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Electrical characteristics

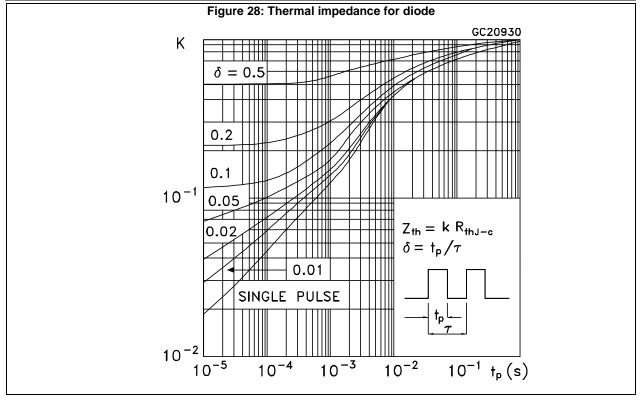
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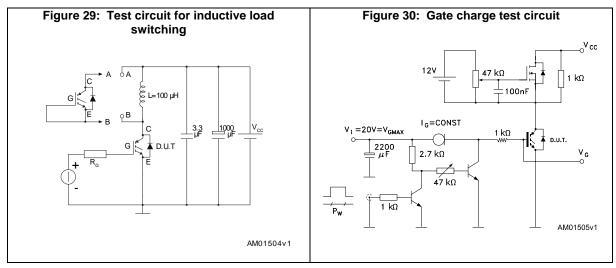


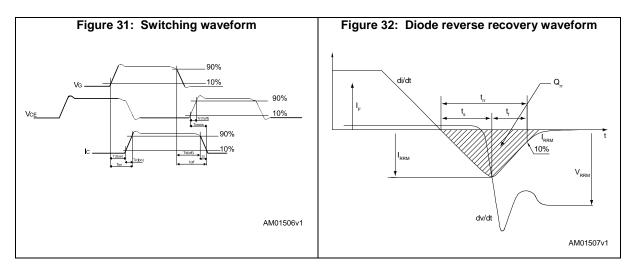
Electrical characteristics





3 Test circuits





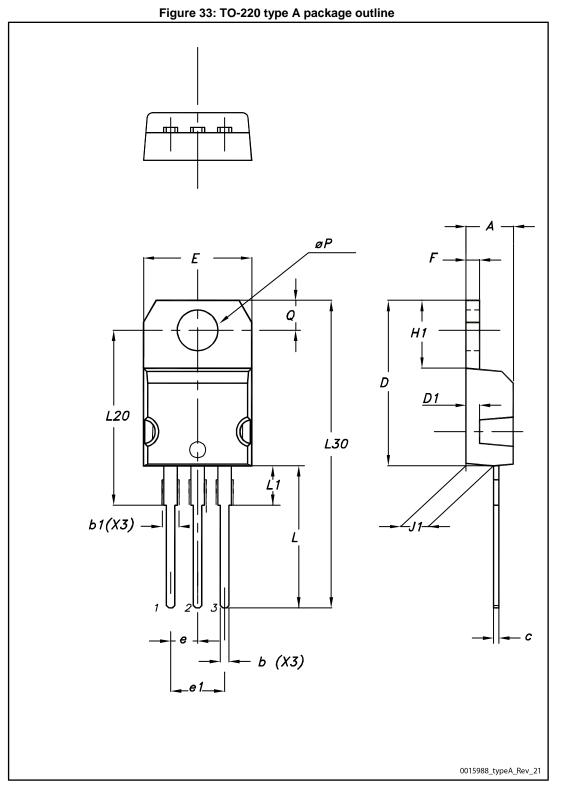


4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.









Package information

		Fackage information			
Table 8: TO-220 type A mechanical data					
	mm				
Min.	Тур.	Max.			
4.40		4.60			
0.61		0.88			
1.14		1.55			
0.48		0.70			
15.25		15.75			
	1.27				
10.00		10.40			
2.40		2.70			
4.95		5.15			
1.23		1.32			
6.20		6.60			
2.40		2.72			
13.00		14.00			
3.50		3.93			
	16.40				
	28.90				
3.75		3.85			
2.65		2.95			
	Min. 4.40 0.61 1.14 0.48 15.25 10.00 2.40 4.95 1.23 6.20 2.40 13.00 3.50	Min. Typ. 4.40			



5 Revision history

Table 9: Document revision history

Date	Revision	Changes	
10-Feb-2015	1	First release.	
23-Apr-2015	2	Minor text edits throughout document Document status promoted to 'Production data' In Section 2 Electrical characteristics: - updated Table 4: Static characteristics - updated Table 5: Dynamic characteristics - updated Table 6: IGBT switching characteristics (inductive load) - updated Table 7: Diode switching characteristics (inductive load) Added Section 2.1 Electrical characteristics (curves)	
31-Jul-2015	3	Updated Table 7: Diode switching characteristics (inductive load)	
19-Oct-2015	4	Updated Table 5: "Dynamic characteristics" and Table 6: "IGBT switching characteristics (inductive load)". Updated Figure 8: "Collector current vs. switching frequency".	
07-Apr-2017	5	Modified title, features and applications on cover page Modified Table 4: "Static characteristics", Table 6: "IGBT switching characteristics (inductive load)" and Table 7: "Diode switching characteristics (inductive load)" Minor text changes.	



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