

2A, 50V - 600V Super Fast Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low profile package
- Super fast recovery time for high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Lighting application
- Snubber
- Freewheeling application

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.090g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
١ _F	2	А		
V _{RRM}	50 - 600	V		
I _{FSM}	50	А		
T _{JMAX}	150	°C		
Package	DO-214A	A (SMB)		
Configuration	Single die			





DO-214AA (SMB)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)										
PARAMETER	SYMBOL	ES2A	ES2B	ES2C	ES2D	ES2F	ES2G	ES2H	ES2J	UNIT
Marking code on the device		ES2A	ES2B	ES2C	ES2D	ES2F	ES2G	ES2H	ES2J	
Repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	105	140	210	280	350	420	V
Forward current	I _F				2	2				А
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}				5	0				A
Junction temperature	TJ				- 55 to	+150				°C
Storage temperature	T _{STG}				- 55 to	+150				°C



THERMAL PERFORMANCE					
PARAMETER	SYMBOL	ТҮР	UNIT		
Junction-to-lead thermal resistance	R _{ejl}	20	°C/W		
Junction-to-ambient thermal resistance	R _{eJA}	75	°C/W		

ELECTRICAL SPECIFICATIONS (TA = 25°C unless otherwise noted)						
PARAMETER	-	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
— • • • • • • • • • • • • • • • • • • •	ES2A ES2B ES2C ES2D		-	0.95	V	
	ES2F ES2G	I _F = 2A, T _J = 25°C	V _F	-	1.30	V
	ES2H ES2J		-	1.70	V	
Reverse current @ rated V _R ⁽²⁾				-	10	μA
	-	T _J = 125°C	I _R	-	350	μA
	ES2A ES2B ES2C ES2D			25	-	pF
Junction capacitance $IMHz, V_R = 4.0V$ ES2F ES2G ES2H ES2J	CJ	20	-	pF		
Reverse recovery time		$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t _{rr}	-	35	ns

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING			
ES2x	DO-214AA (SMB)	3,000 / Tape & Reel			

Notes:

1. "x" defines voltage from 50V(ES2A) to 600V(ES2J)



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

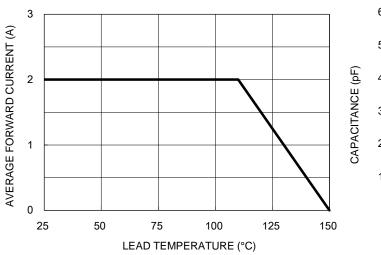


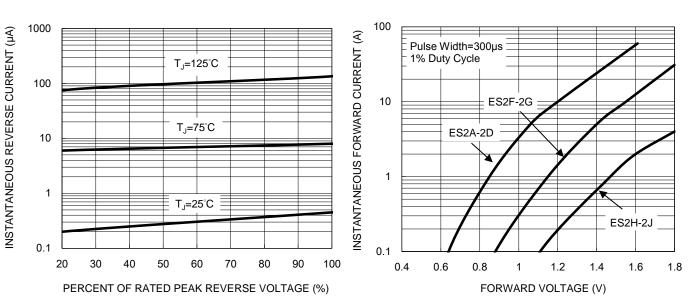
Fig.1 Forward Current Derating Curve

60 50 40 ES2A-D 30 20 ES2F-J +++++ 10 f=1.0MHz Vsig=50mVp-p 0 0.1 10 100 1 **REVERSE VOLTAGE (V)**

Fig.2 Typical Junction Capacitance

Fig.3 Typical Reverse Characteristics

Fig.4 Typical Forward Characteristics





CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

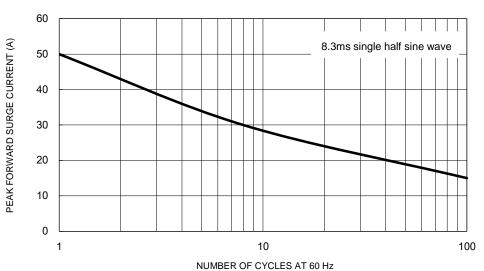
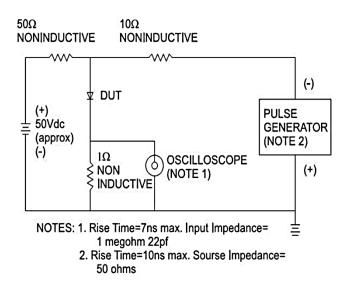
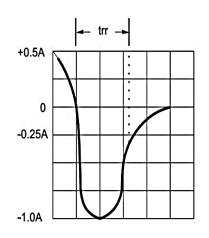


Fig.5 Maximum Non-Repetitive Forward Surge Current

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

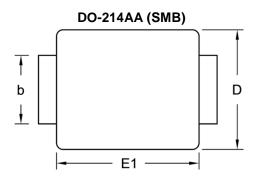


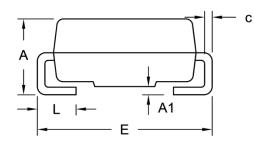




ES2A – ES2J Taiwan Semiconductor

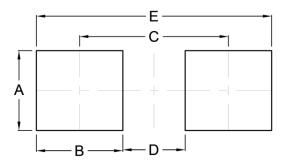
PACKAGE OUTLINE DIMENSIONS





DIM.	Unit	(mm)	Unit	(inch)
	Min.	Max.	Min.	Max.
A	1.95	2.65	0.077	0.104
A1	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
с	0.15	0.31	0.006	0.012
D	3.30	3.95	0.130	0.156
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
L	0.75	1.60	0.030	0.063

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
В	2.50	0.098
С	4.30	0.169
D	1.80	0.071
E	6.80	0.268

MARKING DIAGRAM



P/N	= Marking Code
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G = Green Compound

YW = Date Code

F = Factory Code



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