# VSMY14940



**Vishay Semiconductors** 

### High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



### DESCRIPTION

As part of the SurfLight<sup>IM</sup> portfolio, the VSMY14940 is an infrared, 940 nm, side looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted PCB based package (with lens) for surface mounting (SMD).

### **APPLICATIONS**

- Emitter for remote control (38 kHz)
- · Learning remote control
- Photointerrupters
- · Optical switch

### **FEATURES**

- Package type: surface-mount
- · Package form: side view
- Dimensions (L x W x H in mm): 3.0 x 2.51 x 1.2
- Peak wavelength: λ<sub>p</sub> = 940 nm
- · High reliability
- · High radiant power
- · Very high radiant intensity
- Angle of half intensity:  $\varphi = \pm 9^{\circ}$
- · Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, according to J-STD-020
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PRODUCT SUMMARY				
COMPONENT	l <sub>e</sub> (mW/sr)	φ <b>(deg)</b>	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)
VSMY14940	90	± 9	940	5

Note

· Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY14940	Tape and reel	MOQ: 1500 pcs, 1500 pcs/reel	Side view	

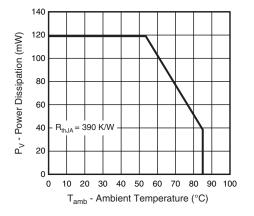
Note

MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	70	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	0.7	A	
Power dissipation		Pv	119	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-55 to +100	°C	
Soldering temperature	According to Fig. 10, J-STD-020	T <sub>sd</sub>	260	°C	
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	390	K/W	

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Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

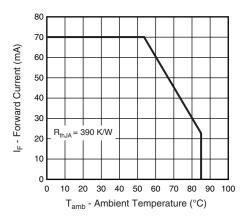


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 20 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	1.1	1.3	1.5	V
	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>	-	1.5	-	V
	I <sub>F</sub> = 0.7 A, t <sub>p</sub> = 100 μs	V <sub>F</sub>	-	3.0	-	V
Temperature coefficient of $V_F$	I <sub>F</sub> = 20 mA	TK <sub>VF</sub>	-	-0.9	-	mV/K
Reverse current		I <sub>R</sub>	Not designed for reverse operation			nA
Junction capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0 mW/cm^{2}$	CJ	-	30	-	pF
Radiant intensity	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	l <sub>e</sub>	15	25	-	mW/sr
	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	-	90	-	mW/sr
	I <sub>F</sub> = 0.7 A, t <sub>p</sub> = 100 μs	l <sub>e</sub>	-	560	-	mW/sr
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	-	0.5	-	μA
Radiant power	I <sub>F</sub> = 70 mA, t <sub>p</sub> = 20 ms	фе	-	40	-	mW
Temperature coefficient of radiant power	I <sub>F</sub> = 70 mA	ΤΚφ <sub>e</sub>	-	-0.21	-	%/K
Angle of half intensity		φ	-	± 9	-	deg
Peak wavelength	I <sub>F</sub> = 70 mA	λ <sub>p</sub>	920	940	960	nm
Spectral bandwidth	I <sub>F</sub> = 70 mA	Δλ	-	55	-	nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 70 mA	ΤΚλ <sub>p</sub>	-	0.28	-	nm/K
Rise time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>r</sub>	-	5	-	ns
Fall time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>f</sub>	-	6	-	ns



### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

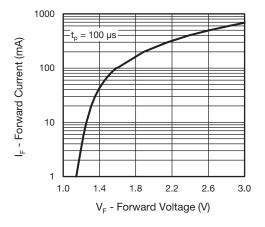


Fig. 3 - Forward Current vs. Forward Voltage

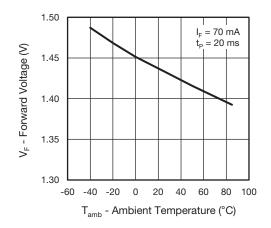


Fig. 4 - Forward Voltage vs. Ambient Temperature

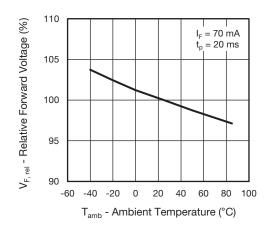


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

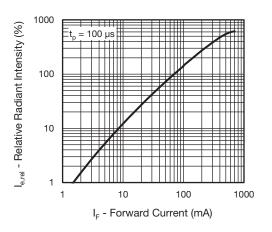


Fig. 6 - Relative Radiant Intensity vs. Forward Current

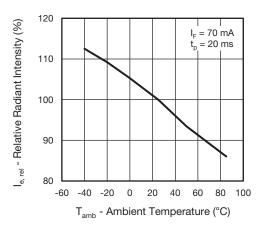


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

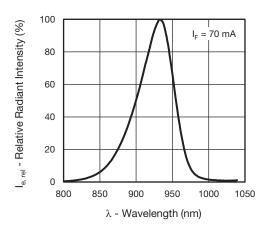


Fig. 8 - Relative Radiant Intensity vs. Wavelength

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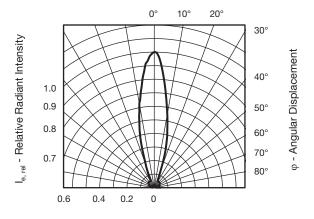


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

### SOLDER PROFILE

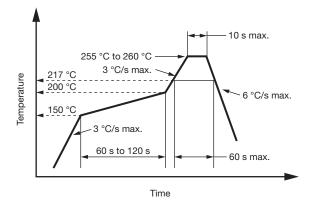


Fig. 10 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

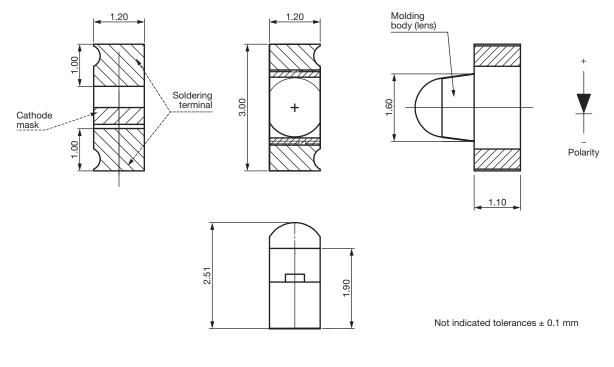
Moisture sensitivity level 3, according to J-STD-020.

### DRYING

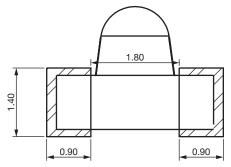
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



PACKAGE DIMENSIONS in millimeters: VSMY14940

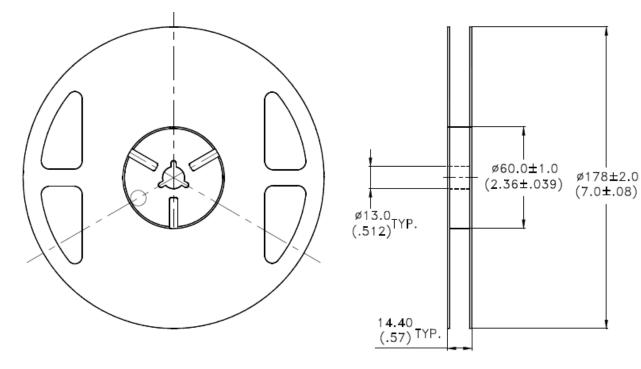


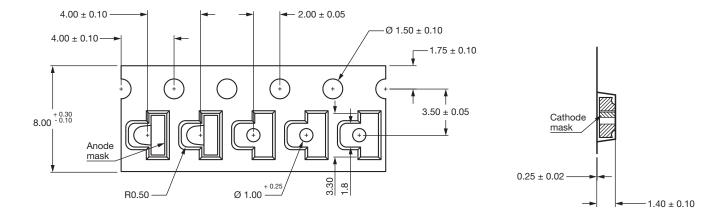
**Recommended Solder Pad** 





### **TAPING AND REEL DIMENSIONS** in millimeters: VSMY14940







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