Electronic Components CHARGED.*

Floating Electrode Design with Flexible Termination System (FF-CAP), X7R Dielectric, 6.3 – 250 VDC (Commercial & Automotive Grade)

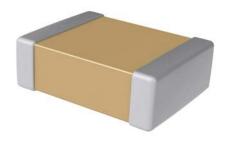
Overview

KEMET's Floating Electrode with Flexible Termination capacitor (FF-CAP) combines two existing KEMET technologies - Floating Electrode and Flexible Termination. The floating electrode component utilizes a cascading internal electrode design configured to form multiple capacitors in series within a single monolithic structure. This unique configuration results in enhanced voltage and ESD performance over standard capacitor designs while allowing for a fail-open condition if mechanically damaged (cracked). The flexible termination component utilizes a conductive silver epoxy between the base metal and nickel barrier layers of KEMET's standard termination system in order to establish pliability while maintaining terminal strength, solderability and electrical performance. Both technologies address the primary failure mode of MLCCsflex cracks, which are typically the result of excessive tensile and shear stresses produced during board flexure and thermal cycling.

Although neither technology can eliminate the potential for mechanical damage that may propagate during extreme environmental and/or handling conditions, the combination of these two technologies provide the ultimate level of protection against a low IR or short circuit condition.

The FF-CAP complements KEMET's Open Mode, Floating Electrode (FE-CAP), Flexible Termination (FT-CAP) and KEMET Power Solutions (KPS) product lines by providing an ultimate fail-safe design optimized for low to mid range capacitance values. These devices exhibit a predictable change in capacitance with respect to time and voltage and boast a minimal change in capacitance with reference to ambient temperature. Capacitance change is limited to ±15% from -55°C to +125°C.

In addition to Commercial Grade, Automotive Grade devices are available which meet the demanding Automotive Electronics Council's AEC-Q200 qualification requirements.



Ordering Information

С	0805	Υ	104	K	5	R	A	С	TU
Ceramic	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish ¹	Packaging/ Grade (C-Spec)
	0603 0805 1206 1210 1812	Y = Floating Electrode with Flexible Termination	Two significant digits and number of zeros	J = ±5% K = ±10% M = ±20%	9 = 6.3 8 = 10 4 = 16 3 = 25 5 = 50 1 = 100 2 = 200 A = 250	R = X7R	A = N/A	C = 100% Matte Sn L = SnPb (5% Pb minimum)	See "Packaging C-Spec Ordering Options Table"

¹ Additional termination finish options may be available. Contact KEMET for details.

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¹ SnPb termination finish option is not available on automotive grade product.



Packaging C-Spec Ordering Options Table

Packaging Type	Packaging/Grade Ordering Code (C-Spec)					
Commerc	cial Grade ¹					
Bulk Bag	Not Required (Blank)					
7" Reel/Unmarked	TU					
13" Reel/Unmarked	7411 (EIA 0603 and smaller case sizes) 7210 (EIA 0805 and larger case sizes)					
7" Reel/Marked	TM					
13" Reel/Marked	7040 (EIA 0603) 7215 (EIA 0805 and larger case sizes)					
7" Reel/Unmarked/2 mm pitch²	7081					
13" Reel/Unmarked/2 mm pitch ²	7082					
Automot	ive Grade ³					
7" Reel	AUTO					
13" Reel/Unmarked	AUTO7411 (EIA 0603 and smaller case sizes) AUTO7210 (EIA 0805 and larger case sizes)					
7" Reel/Unmarked/2 mm pitch ²	3190					
13" Reel/Unmarked/2 mm pitch ²	3191					

¹ Default packaging is "Bulk Bag". An ordering code C-Spec is not required for "Bulk Bag" packaging.

Benefits

- -55°C to +125°C operating temperature range
- Superior flex performance (up to 5 mm)
- · Floating Electrode/fail open design
- · Low to mid capacitance flex mitigation
- Lead (Pb)-free, RoHS and REACH compliant
- EIA 0603, 0805, 1206, 1210, and 1812 case sizes
- DC voltage ratings of 6.3 V, 10 V, 16 V, 25 V, 50 V, 100 V, 200 V, and 250 V

- Capacitance offerings ranging from 180 pF to 0.22 μF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- Commercial and Automotive (AEC-Q200) grades available
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability
- SnPb termination finish option available upon request (5% Pb minimum)

Applications

Typical applications include circuits with a direct battery or power source connection, critical and safety relevant circuits without (integrated) current limitation and any application that is subject to high levels of board flexure or temperature cycling. Examples include raw power input side filtering (power plane/bus), high current applications (automobile battery line) and circuits that cannot be fused to open. Markets include consumer, medical, industrial (power supply), automotive, aerospace and telecom.

¹ The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked.

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

³ Reeling tape options (Paper or Plastic) are dependent on capacitor case size (L" x W") and thickness dimension. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information".

³ For additional Information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information".

³ All Automotive packaging C-Specs listed exclude the option to laser mark components. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking".



Automotive C-Spec Information

KEMET automotive grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. These products are supported by a Product Change Notification (PCN) and Production Part Approval Process warrant (PPAP).

Automotive products offered through our distribution channel have been assigned an inclusive ordering code C-Spec, "AUTO." This C-Spec was developed in order to better serve small and medium-sized companies that prefer an automotive grade component without the requirement to submit a customer Source Controlled Drawing (SCD) or specification for review by a KEMET engineering specialist. This C-Spec is therefore not intended for use by KEMET OEM automotive customers and are not granted the same "privileges" as other automotive C-Specs. Customer PCN approval and PPAP request levels are limited (see details below.)

Product Change Notification (PCN)

The KEMET product change notification system is used to communicate primarily the following types of changes:

- Product/process changes that affect product form, fit, function, and/or reliability
- · Changes in manufacturing site
- Product obsolescence

KEMET Automotive	Customer Notifica	tion Due To:	Days Prior To		
C-Spec	Process/Product change	Obsolescence*	Implementation		
KEMET assigned ¹	Yes (with approval and sign off)	Yes	180 days minimum		
AUT0	Yes (without approval)	Yes	90 days minimum		

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

Production Part Approval Process (PPAP)

The purpose of the Production Part Approval Process is:

- To ensure that supplier can meet the manufacturability and quality requirements for the purchased parts.
- To provide the evidence that all customer engineering design records and specification requirements are properly understood and fulfilled by the manufacturing organization.
- To demonstrate that the established manufacturing process has the potential to produce the part.

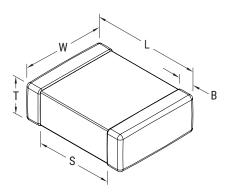
KEMET Automotive		PPAP (Product	Part Approval	Process) Leve	
C-Spec	1	2	3	4	5
KEMET assigned ¹	•	•	•	•	•
AUT0	0		0		

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

- Part number specific PPAP available
- Product family PPAP only



Dimensions - Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0603	1608	1.60 (0.063) ±0.17 (0.007)	0.80 (0.032) ±0.15 (0.006)		0.45 (0.018) ±0.15 (0.006)	0.58 (0.023)	
0805	2012	2.00 (0.079) ±0.30 (0.012)	1.25 (0.049) ±0.30 (0.012)		0.50 (0.02) ±0.25 (0.010)	0.75 (0.030)	Solder Wave or Solder Reflow
1206	3216	3.30 (0.130) ±0.40 (0.016)	1.60 (0.063) ±0.35 (0.013)	See Table 2 for Thickness	0.60 (0.024) ±0.25 (0.010)		
1210	3225	3.30 (0.130) ±0.40 (0.016)	2.60 (0.102) ±0.30 (0.012)		0.60 (0.024) ±0.25 (0.010)	N/A	Solder Reflow
1812	4532	4.50 (0.178) ±0.40 (0.016)	3.20 (0.126) ±0.30 (0.012)		0.70 (0.028) ±0.35 (0.014)		Only

Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance & Reliability.

Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC-Q200, please visit their website at www.aecouncil.com.

Environmental Compliance

Lead (Pb)-free, RoHS, and REACH compliant without exemptions (excluding SnPb termination finish option).



Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC)	±15%
¹ Aging Rate (Maximum % Capacitance Loss/Decade Hour)	3.0%
² Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5±1 seconds and charge/discharge not exceeding 50mA)
³ Dissipation Factor (DF) Maximum Limit at 25°C	5%(6.3V & 10V), 3.5%(16V & 25V) and 2.5%(50V to 250V)
⁴ Insulation Resistance (IR) Minimum Limit at 25°C	See Insulation Resistance Limit Table (Rated voltage applied for 120±5 seconds at 25°C)

¹ Regarding Aging Rate: Capacitance measurements (including tolerance) are indexed to a referee time of 1,000 hours.

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON".

Post Environmental Limits

	High Temperature Life, Biased Humidity, Moisture Resistance											
Dielectric Rated DC Capacitance Dissipation Factor Capacitance Insula (Maximum %) Shift Resist												
	> 25		3.0		100: (1:::1							
X7R	16/25	All	5.0	±20%	10% of Initial							
	< 16		7.5		Limit							

Insulation Resistance Limit Table (X7R Dielectric)

EIA Case Size	1,000 Megohm Microfarads or 100 GΩ	500 Megohm Microfarads or 10 GΩ
0201	N/A	ALL
0402	< 0.012 μF	≥ 0.012 µF
0603	< 0.047 μF	≥ 0.047 µF
0805	< 0.15 μF	≥ 0.15 µF
1206	< 0.47 μF	≥ 0.47 µF
1210	< 0.39 μF	≥ 0.39 µF
1808	ALL	N/A
1812	< 2.2 μF	≥ 2.2 µF
1825	ALL	N/A
2220	< 10 μF	≥ 10 µF
2225	ALL	N/A

² DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

 $^{^3}$ Capacitance and dissipation factor (DF) measured under the following conditions: 1kHz \pm 50Hz and 1.0 \pm 0.2 Vrms if capacitance \leq 10 μF

¹²⁰Hz ± 10Hz and 0.5 ± 0.1 Vrms if capacitance >10μF

⁴ To obtain IR limit, divide M Ω - μ F value by the capacitance and compare to G Ω limit. Select the lower of the two limits.



Table 1A - Capacitance Range/Selection Waterfall (0603 - 0805 Case Sizes)

			se Siz Series				С	0603	3Y						C08	05Y			
	Capacitance	Vo	Itage Co	de	9	8	4	3	5	1	2	9	8	4	3	5	1	2	Α
Capacitance	Code	Rat			6.3	9	91	25	20	100	200	6.3	10	16	25	50	100	200	250
			pacitan oleranc			Product Availability and Chip Thickness Codes See Table 2 for Chip Thickness Dimensions													
180 pF	181	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
220 pF	221	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
270 pF	271	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
330 pF	331	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
390 pF	391	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
470 pF	471	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
560 pF	561	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
680 pF	681	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
820 pF	821	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
1,000 pF	102	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
1,200 pF	122	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
1,500 pF	152	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
1,800 pF	182	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
2,200 pF	222	J	К	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
2,700 pF	272	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
3,300 pF	332	J	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
3,900 pF	392	Ĵ	K	М	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
4,700 pF	472	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ	CJ	DR	DR	DR	DR	DR	DR	DR	DR
5,600 pF	562	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ		DR	DR	DR	DR	DR	DR	DR	DR
6,800 pF	682	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ		DR	DR	DR	DR	DR	DR	DR	DR
8,200 pF	822	J	K	M	CJ	CJ	CJ	CJ	CJ	CJ		DR	DR	DR	DR	DR	DR	DR	DR
10,000 pF	103	J	K	M	CJ	CJ	CJ	CJ	CJ	00		DR	DR	DR	DR	DR	DR	DR	DR
12,000 pF	123	J	K	M	CJ	CJ	CJ	CJ	CJ			DR	DR	DR	DR	DR	DR	DR	DR
15,000 pF	153	J	K	M	CJ	CJ	CJ	CJ	CJ			DR	DR	DR	DR	DR	DD	DIC	DIX
18,000 pF	183	J	K	M	CJ	CJ	CJ	CJ	CJ			DR	DR	DR	DR	DR	DD		
22,000 pF	223	J	K	M	CJ	CJ	CJ	CJ	CJ			DR	DR	DR	DR	DR	DD		
27,000 pF	273	J	K	M	63	0.5	0.5	0.5	0.5			DR	DR	DR	DR	DR	טט		
33,000 pF	333	J	K	M								DR	DR	DR	DR	DR			
39,000 pF	393	J	K	M								DR	DR	DR	DR	DR			
	473		K	M								DR	DR	DR	DR	DR			
47,000 pF	563	J	K	M								DD DD	DD DD	DD	DD	DD			
56,000 pF		_												DD סט	DD	DD סט			
68,000 pF	683	J	K	М								DD	DD						
82,000 pF	823	J	K	М								DG	DG	DG	DG	DG			
0.10 μF	104	J	IX.	М								DG	DG	DG	DG	DG			
		ка	ted Volta (VDC)	age	6.3	10	16	25	20	100	200	6.3	10	16	25	20	100	200	250
Capacitance	Capacitance Code	Vo	ltage Co	de	9	8	4	3	5	1	2	9	8	4	3	5	1	2	A
		Case	Size/S	eries			(C0603	Y						C08	05Y			



Table 1B - Capacitance Range/Selection Waterfall (1206 - 1812 Case Sizes)

			se Si Serie					C12	06Y							C12	10Y					С	1812	Y	
Capacitance	Cap	Vol	tage C	ode	9	8	4	3	5	1	2	Α	9	8	4	3	5	1	2	A	3	5	1	2	A
Cupacitanoc	Code	Rate	ed Volt (VDC)	•	6.3	1	16	25	20	100	200	250	6.3	10	16	25	20	100	200	250	25	20	100	200	250
		Cap	pacita oleran	nce		Product Availability and Chip Thickness Codes See Table 2 for Chip Thickness Dimensions																			
1,000 pF	102	J	К	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ	10 2 1		7	, KIICO	<u> </u>	-11010							
1,200 pF	122	J	K	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ													
1,500 pF	152	J	K	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ									İ				
1,800 pF	182	J	K	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ									İ				
2,200 pF	222	J	K	М	EQ	EO	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	İ				
2,700 pF	272	J	К	М	ΕÔ	EO	EO	EO	EO	EO	EO	EO	FN	FN	FN	FN	FN	FN	FN	FN					
3,300 pF	332	Ĵ	K	M	EO	EO	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	İ				
3,900 pF	392	J	K	М	EQ	EO	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	İ				
4,700 pF	472	J	K	М	ΕÔ	EO	EQ	EQ	EO	EQ	EO	EQ	FN	FN	FN	FN	FN	FN	FN	FN	İ				
5,600 pF	562	J	K	М	ΕÔ	EO	EQ	EQ	EO	EO	EO	EO	FN	FN	FN	FN	FN	FN	FN	FN	İ				
6,800 pF	682	J	K	M	EO	EO	EO	EQ	EO	EO	EQ	EO	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
8,200 pF	822	Ĵ	K	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
10,000 pF	103	Ĵ	K	М	EO	EO	EO	EQ	EO	EO	EO	EQ	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
12,000 pF	123	J	K	М	EQ	EQ	EQ	EQ	EO	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
15,000 pF	153	J	K	М	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
18,000 pF	183	J	K	M	EQ	EQ	EQ	EQ	EQ	EQ	EQ	EQ	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
22,000 pF	223	Ĵ	K	М	EO	EO	EQ	EQ	EO	EO	EO	EO	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
27,000 pF	273	J	K	М	EO	EO	EO	EO	EO	EO	EO	EO	FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
33,000 pF	333	J	K	М	EQ	EQ	EQ	EQ	EQ	EQ			FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
39,000 pF	393	Ĵ	K	М	EO	EO	EO	EO	EO	ER			FN	FN	FN	FN	FN	FN	FN	FN	GB	GB	GB	GB	GB
47.000 pF	473	J	K	M	EO	EO	EO	EO	EO	ER			FN	FN	FN	FN	FN	FN	FQ	FO	GB	GB	GB	GB	GB
56,000 pF	563	J	K	M	EO	EO	EO	EO	EO	EO			FN	FN	FN	FN	FN	FN	F0	FO	GB	GB	GB	GB	GB
68,000 pF	683	J	K	M	EQ	EQ	EQ	EQ	EQ	-4			FN	FN	FN	FN	FN	FN	١, ٩	. 4	GB	GB	GB	GB	GB
82,000 pF	823	Ĵ	K	M	EO	EO	EQ	EO	EQ				FN	FN	FN	FN	FN	FQ			GB	GB	GB	GB	GB
0.10 uF	104	J	K	M	EO	EO	EO	EQ	EQ				FN	FN	FN	FN	FN	FX			GB	GB	GB	GB	GB
0.12 μF	124	J	K	M	ER	ER	ER	ER	ER				FN	FN	FN	FN	FN				GB	GB	GB	GB	GB
0.15 μF	154	J	K	M	l								FQ	FQ	FQ	FQ	FQ				GB	GB	GB	GB	GB
0.18 μF	184	J	K	M									F0	FQ	FO	F0	F0				GB	GB	GB	GB	GB
0.22 μF	224	Ĵ	K	M	l								F0	F0	FO	F0	F0				GB	GB	GB	GB	GB
F-		Rate	ed Volt	tage	6.3	2	91	25	20	100	200	250	6.3	2	91	25	20	100	200	250	25	20	9	200	250
Oanaaltan :	0 01		(VDC)			_	-								-								1		
Capacitance	Cap Code	Voltage Code Case Size/			9	8	4	3	5	1	2	Α	9	8	4	3	5	1	2	A	3	5	<u>'</u>	2	Α
			Series	,				C12	06Y							C12	10Y						C1812	Υ	



Table 2A - Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity ¹	Plastic (Quantity		
Code	Size ¹	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel		
CJ	0603	0.80 ± 0.15	4,000	15,000	0	0		
DR	0805	0.78 ± 0.20	0	0	4,000	10,000		
DD	0805	0.90 ± 0.10	0	0	4,000	10,000		
DG	0805	1.25 ± 0.15	0	0	2,500	10,000		
EQ	1206	0.78 ± 0.20	4,000	10,000	4,000	10,000		
ER	1206	0.90 ± 0.20	0	0	4,000	10,000		
FN	1210	0.78 ± 0.20	0	0	4,000	10,000		
FQ	1210	0.90 ± 0.20	0	0	4,000	10,000		
FX	1210	0.95 ± 0.20	0	0	4,000	10,000		
GB	1812	1.00 ± 0.10	0	0	1,000	4,000		
Thickness	Case	Thickness ±	7" Reel	13" Reel	7" Reel	13" Reel		
Code	Size ¹	Range (mm)	Paper Q	uantity¹	Plastic Quantity			

Package quantity based on finished chip thickness specifications.

Table 2B - Bulk Packaging Quantities

Dookoa	ing Type	Loose Pa	Loose Packaging						
Packay	ing Type	Bulk Bag (default)							
Packagir	ng C-Spec ¹	N/	'A²						
Case	Size	Packaging Quantities (pieces/unit packaging)						
EIA (in)	Metric (mm)	Minimum	Maximum						
0402	1005								
0603	1608								
0805	2012		50,000						
1206	3216								
1210	3225	1							
1808	4520	'							
1812	4532								
1825	4564		20,000						
2220	5650								
2225	5664								

¹ The "Packaging C-Spec" is a 4 to 8 digit code which identifies the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See "Ordering Information" section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products.

¹ If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

² A packaging C-Spec (see note 1 above) is not required for "Bulk Bag" packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging.



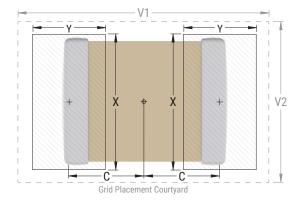
Table 3 - Chip Capacitor Land Pattern Design Recommendations per IPC-7351

EIA Size Code	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)				ı	Density Level B: Median (Nominal) Land Protrusion (mm)				Density Level C: Minimum (Least) Land Protrusion (mm)					
Code	oode	С	Y	X	V1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2
0603	1608	0.85	1.25	1.10	4.00	2.10	0.75	1.05	1.00	3.10	1.50	0.65	0.85	0.90	2.40	1.20
0805	2012	0.99	1.44	1.66	4.47	2.71	0.89	1.24	1.56	3.57	2.11	0.79	1.04	1.46	2.42	1.81
1206	3216	1.59	1.62	2.06	5.85	3.06	1.49	1.42	1.96	4.95	2.46	1.39	1.22	1.86	4.25	2.16
1210	3225	1.59	1.62	3.01	5.90	4.01	1.49	1.42	2.91	4.95	3.41	1.39	1.22	2.81	4.25	3.11
1812	4532	2.10	1.80	3.60	7.00	4.60	2.00	1.60	3.50	6.10	4.00	1.90	1.40	3.40	5.40	3.70

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

Image below based on Density Level B for an EIA 1210 case size.





Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/ J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Termination Finish			
rionic i catare	SnPb	100% Matte Sn		
Preheat/Soak				
Temperature Minimum (T _{Smin})	100°C	150°C		
Temperature Maximum (T _{Smax})	150°C	200°C		
Time (t_s) from T_{Smin} to T_{Smax}	60 - 120 seconds	60 - 120 seconds		
Ramp-Up Rate (T _L to T _P)	3°C/second maximum	3°C/second maximum		
Liquidous Temperature (T_L)	183°C	217°C		
Time Above Liquidous (t _L)	60 - 150 seconds	60 - 150 seconds		
Peak Temperature (T _P)	235°C	260°C		
Time Within 5°C of Maximum Peak Temperature (t _p)	20 seconds maximum	30 seconds maximum		
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.

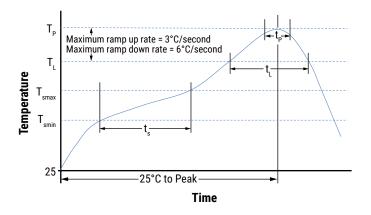




Table 4 - Performance & Reliability: Test Methods and Conditions

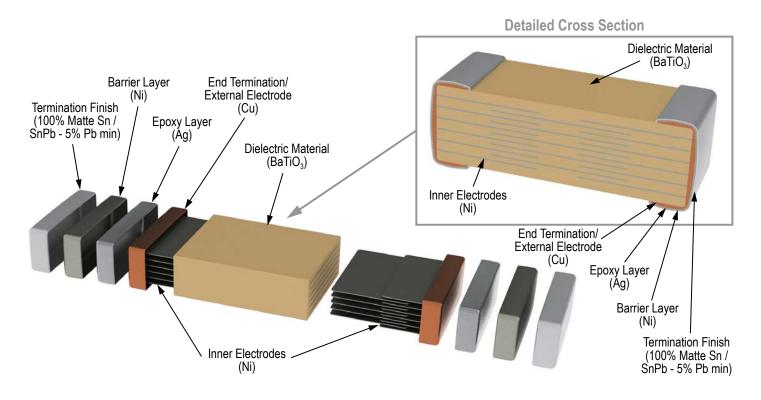
Stress	Reference	Test or Inspection Method		
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.		
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for COG. Flexible termination system – 3.0 mm (minimum).		
		Magnification 50 X. Conditions:		
Caldarability	J-STD-002	a) Method B, 4 hours at 155°C, dry heat at 235°C		
Solderability	J-51D-002	b) Method B at 215°C category 3		
		c) Method D, category 3 at 260°C		
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 4 hours after test conclusion.		
Biased Humidity	MIL-STD-202 Method	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion.		
,	103	Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion.		
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Measurement at 24 hours +/- 4 hours after test conclusion.		
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Number of cycles required - 300, maximum transfer time - 20 seconds, dwell time - 15 minutes. Air - Air.		
High Temperature Life	MIL-STD-202 Method 108 /EIA-198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.		
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.		
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz		
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.		
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.		

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature—reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction





Capacitor Marking (Optional):

These surface mount multilayer ceramic capacitors are normally supplied unmarked. If required, they can be marked as an extra cost option. Marking is available on most KEMET devices, but must be requested using the correct ordering code identifier(s). If this option is requested, two sides of the ceramic body will be laser marked with a "K" to identify KEMET, followed by two characters (per EIA–198 - see table below) to identify the capacitance value. EIA 0603 case size devices are limited to the "K" character only.

Laser marking option is <u>not</u> available on:

- · COG, ultra stable X8R and Y5V dielectric devices.
- EIA 0402 case size devices.
- EIA 0603 case size devices with flexible termination option.
- KPS commercial and automotive grade stacked devices
- X7R dielectric products in capacitance values outlined below.

EIA Case Size	Metric Size Code	Capacitance
0603	1608	≤ 170 pF
0805	2012	≤ 150 pF
1206	3216	≤ 910 pF
1210	3225	≤ 2,000 pF
1808	4520	≤ 3,900 pF
1812	4532	≤ 6,700 pF
1825	4564	≤ 0.018 µF
2220	5650	≤ 0.027 µF
2225	5664	≤ 0.033 µF

Marking appears in legible contrast. Illustrated below is an example of an MLCC with laser marking of "KA8", which designates a KEMET device with rated capacitance of 100 μ F. Orientation of marking is vendor optional.





Capacitor Marking (Optional) cont'd

Conscitance (nE) For Various Alpha (Numeral Identifiers												
	Capacitance (pF) For Various Alpha/Numeral Identifiers Numeral											
Alpha		•			1	T T	1		_			
Character	9	0	1	2	3	4	5	6	7	8		
	Capacitance (pF)											
Α	0.10	1.0	10	100	1,000	10,000	100,000	1,000,000	10,000,000	100,000,000		
В	0.11	1.1	11	110	1,100	11,000	110,000	1,100,000	11,000,000	110,000,000		
С	0.12	1.2	12	120	1,200	12,000	120,000	1,200,000	12,000,000	120,000,000		
D	0.13	1.3	13	130	1,300	13,000	130,000	1,300,000	13,000,000	130,000,000		
E	0.15	1.5	15	150	1,500	15,000	150,000	1,500,000	15,000,000	150,000,000		
F	0.16	1.6	16	160	1,600	16,000	160,000	1,600,000	16,000,000	160,000,000		
G	0.18	1.8	18	180	1,800	18,000	180,000	1,800,000	18,000,000	180,000,000		
Н	0.20	2.0	20	200	2,000	20,000	200,000	2,000,000	20,000,000	200,000,000		
J	0.22	2.2	22	220	2,200	22,000	220,000	2,200,000	22,000,000	220,000,000		
К	0.24	2.4	24	240	2,400	24,000	240,000	2,400,000	24,000,000	240,000,000		
L	0.27	2.7	27	270	2,700	27,000	270,000	2,700,000	27,000,000	270,000,000		
М	0.30	3.0	30	300	3,000	30,000	300,000	3,000,000	30,000,000	300,000,000		
N	0.33	3.3	33	330	3,300	33,000	330,000	3,300,000	33,000,000	330,000,000		
Р	0.36	3.6	36	360	3,600	36,000	360,000	3,600,000	36,000,000	360,000,000		
Q	0.39	3.9	39	390	3,900	39,000	390,000	3,900,000	39,000,000	390,000,000		
R	0.43	4.3	43	430	4,300	43,000	430,000	4,300,000	43,000,000	430,000,000		
S	0.47	4.7	47	470	4,700	47,000	470,000	4,700,000	47,000,000	470,000,000		
T	0.51	5.1	51	510	5,100	51,000	510,000	5,100,000	51,000,000	510,000,000		
U	0.56	5.6	56	560	5,600	56,000	560,000	5,600,000	56,000,000	560,000,000		
V	0.62	6.2	62	620	6,200	62,000	620,000	6,200,000	62,000,000	620,000,000		
W	0.68	6.8	68	680	6,800	68,000	680,000	6,800,000	68,000,000	680,000,000		
Х	0.75	7.5	75	750	7,500	75,000	750,000	7,500,000	75,000,000	750,000,000		
Υ	0.82	8.2	82	820	8,200	82,000	820,000	8,200,000	82,000,000	820,000,000		
Z	0.91	9.1	91	910	9,100	91,000	910,000	9,100,000	91,000,000	910,000,000		
а	0.25	2.5	25	250	2,500	25,000	250,000	2,500,000	25,000,000	250,000,000		
b	0.35	3.5	35	350	3,500	35,000	350,000	3,500,000	35,000,000	350,000,000		
d	0.40	4.0	40	400	4,000	40,000	400,000	4,000,000	40,000,000	400,000,000		
е	0.45	4.5	45	450	4,500	45,000	450,000	4,500,000	45,000,000	450,000,000		
f	0.50	5.0	50	500	5,000	50,000	500,000	5,000,000	50,000,000	500,000,000		
m	0.60	6.0	60	600	6,000	60,000	600,000	6,000,000	60,000,000	600,000,000		
n	0.70	7.0	70	700	7,000	70,000	700,000	7,000,000	70,000,000	700,000,000		
t	0.80	8.0	80	800	8,000	80,000	800,000	8,000,000	80,000,000	800,000,000		
у	0.90	9.0	90	900	9,000	90,000	900,000	9,000,000	90,000,000	900,000,000		



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

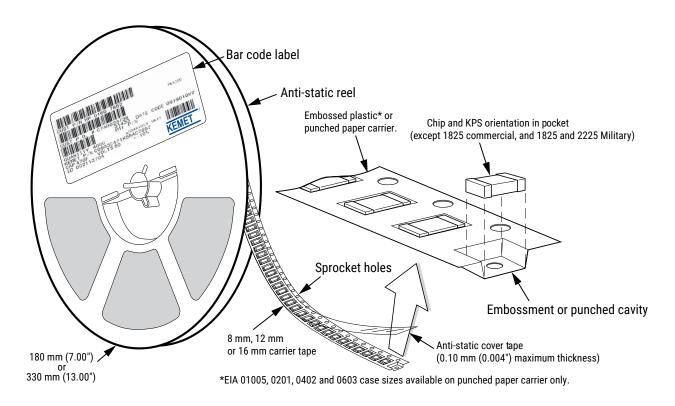


Table 5 - Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

	Tape	Embosse	ed Plastic	Punched Paper		
EIA Case Size	Size	7" Reel	13" Reel	7" Reel	13" Reel	
	(W)*	Pitch	(P ₁)*	Pitch	(P ₁)*	
01005 - 0402	8			2	2	
0603	8			2/4	2/4 /	
0805	8	4	4	4	4	
1206 - 1210	8	4	4	4	4	
1805 - 1808	12	4	4			
≥ 1812	12	8	8			
KPS 1210	12	8	8			
KPS 1812 and 2220	16	12	12			
Array 0612	8	4	4			

^{*}Refer to Figures 1 and 2 for W and P_1 carrier tape reference locations.

New 2 mm Pitch Reel Options*

	Packaging Ordering Code (C-Spec)	Packaging Type/Options
1	C-3190	Automotive grade 7" reel unmarked
١	C-3191	Automotive grade 13" reel unmarked
	C-7081	Commercial grade 7" reel unmarked
	C-7082	Commercial grade 13" reel unmarked

^{* 2} mm pitch reel only available for 0603 EIA case size. 2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- · Lower placement costs.
- Double the parts on each reel results in fewer reel changes and increased efficiency.
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste.

^{*}Refer to Tables 6 and 7 for tolerance specifications.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

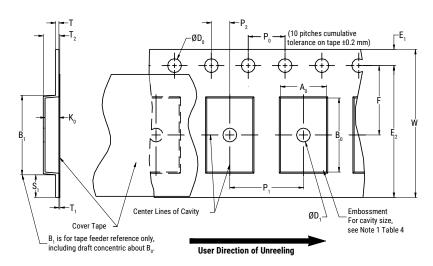


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)										
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum		
8 mm		1.0 (0.039)				25.0 (0.984)					
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)		
16 mm		(0.059)				(1.181)					
	Variable Dimensions — Millimeters (Inches)										
Tape Size	Pitch	B ₁ Maximum Note 4	E_2 Minimum	F	P ₁	T_2 Maximum	W Maximum	A_0, B_0	& K ₀		
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)				
12 mm	Single (4 mm) and double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5			
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)				

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of the embossment location and the hole location shall be applied independently of each other.
- 2. The tape with or without components shall pass around R without damage (see Figure 6.)
- 3. If $S_1 < 1.0$ mm, there may not be enough area for a cover tape to be properly applied (see EIA Standard 481, paragraph 4.3, section b.)
- 4. B, dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by A_{n} , B_{n} and K_{n} shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3.)
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4.)
 - (e) for KPS product, A_0 and B_0 are measured on a plane 0.3 mm above the bottom of the pocket.
 - (f) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 - Punched (Paper) Carrier Tape Dimensions

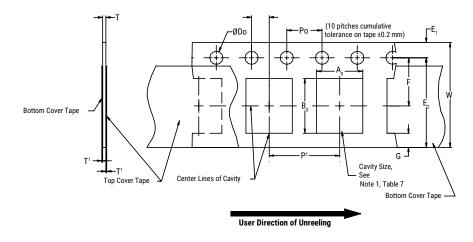


Table 7 - Punched (Paper) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)											
Tape Size	D _o	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2					
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) maximum	0.75 (0.030)	25 (0.984)					
	Variable Dimensions — Millimeters (Inches)											
Tape Size	Pitch	E2 Minimum	F	P ₁	T Maximum	W Maximum	$A_0^{}B_0^{}$					
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1					
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	Note I					

- 1. The cavity defined by A_{o} , B_{o} and T shall surround the component with sufficient clearance that:
 - a) the component does not protrude beyond either surface of the carrier tape.
 - b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - c) rotation of the component is limited to 20° maximum (see Figure 3.)
 - d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4.)
 - e) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.
- 2. The tape with or without components shall pass around R without damage (see Figure 6.)



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624*.

Figure 3 - Maximum Component Rotation

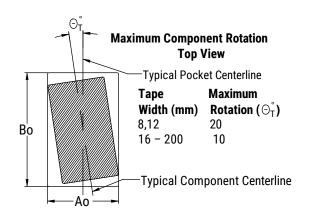


Figure 4 - Maximum Lateral Movement

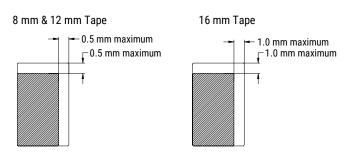


Figure 5 - Bending Radius

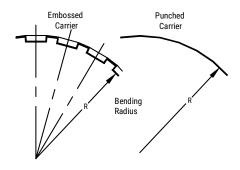
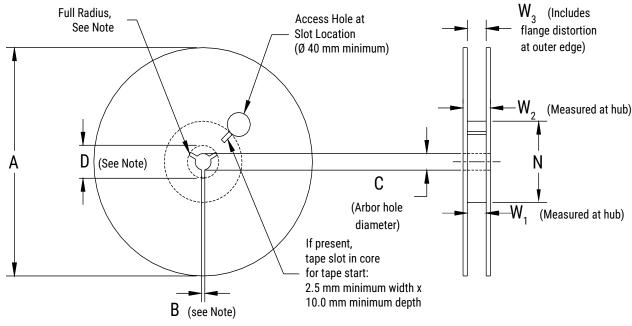




Figure 6 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 - Reel Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)										
Tape Size	A	B Minimum	С	D Minimum							
8 mm	178 ±0.20										
12 mm	(7.008 ±0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)							
16 mm	330 ±0.20 (13.000 ±0.008)	, ,	,	,							
	Variable Dimensions — Millimeters (Inches)										
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃							
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)								
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference							
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)								



Figure 7 - Tape Leader & Trailer Dimensions

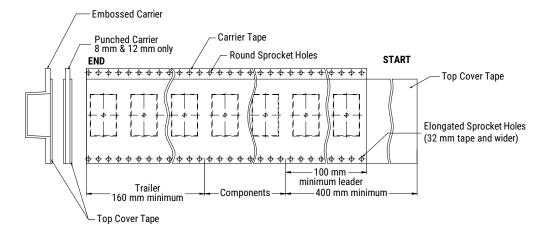
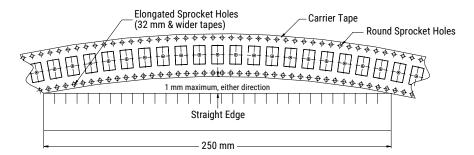


Figure 8 - Maximum Camber





KEMET Electronics Corporation Sales Offices

For a complete list of our global sales offices, please visit www.kemet.com/sales.

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Although KEMET designs and manufactures its products to the most stringent quality and safety standards, given the current state of the art, isolated component failures may still occur. Accordingly, customer applications which require a high degree of reliability or safety should employ suitable designs or other safeguards (such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal injury or property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that other measures may not be required.

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