Vishay General Semiconductor

# **Trench MOS Barrier Schottky Rectifier** for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.41$  V at  $I_F = 5$  A



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**DESIGN SUPPORT TOOLS** 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	10 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	100 A			
$V_F$ at $I_F = 10$ A	0.52 V			
T <sub>OP</sub> max. (AC mode)	150 °C			
T <sub>J</sub> max. (DC forward current)	200 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Single			

# **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- RoHS Meets MSL level 1, per J-STD-020, LF maximum COMPLIANT peak of 245 °C
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

## **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VBT1045BP	UNIT	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	45	V	
Maximum DC forward bypassing current (fig. 1)	I <sub>F(DC)</sub> <sup>(1)</sup>	10	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	100	А	
Operating junction temperature range (AC mode)	T <sub>OP</sub>	-40 to +150	°C	
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$	T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C	

#### Notes

<sup>(1)</sup> With heatsink

(2) Meets the requirements of IEC 61215 ed.2 bypass diode thermal test

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 5 A	T 05 %0		0.50	-	v	
	I <sub>F</sub> = 10 A	T <sub>A</sub> = 25 °C	V (1)	0.57	0.68		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C	V <sub>F</sub> <sup>(1)</sup>	0.41	-		
	I <sub>F</sub> = 10 A			0.52	0.64		
Reverse current	V - 45 V	T <sub>A</sub> = 25 °C	L (2)	-	500	μA	
	V <sub>R</sub> = 45 V	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	5	15	mA	

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

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<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VBT1045BP	UNIT	
Typical thermal resistance	$R_{ ext{ heta}JC}$	3.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT1045BP-E3/4W	1.37	4W	50/tube	Tube	
TO-263AB	VBT1045BP-E3/8W	1.37	8W	800/reel	Tape and reel	

# RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

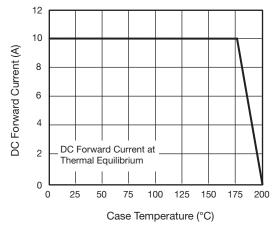
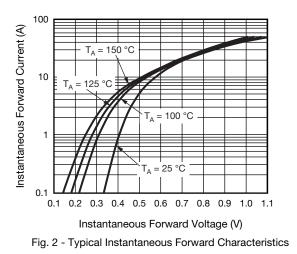


Fig. 1 - Maximum Forward Current Derating Curve



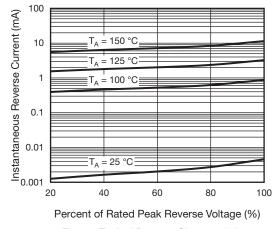


Fig. 3 - Typical Reverse Characteristics

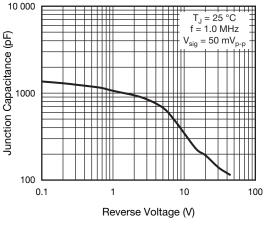
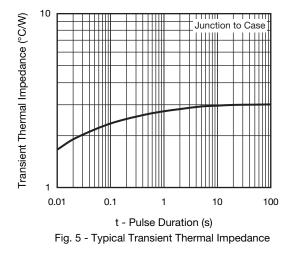


Fig. 4 - Typical Junction Capacitance



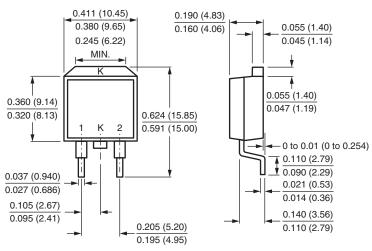




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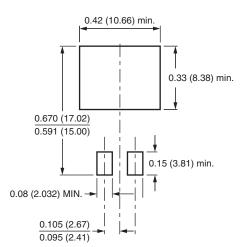
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



# D<sup>2</sup>PAK (TO-263AB)

### **Mounting Pad Layout**





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