

STD75N3LLH6, STP75N3LLH6 STU75N3LLH6, STU75N3LLH6-S

N-channel 30 V, 0.0042 Ω, 75 A, DPAK, TO-220, IPAK, Short IPAK STripFETTM VI DeepGATETM Power MOSFET

Features

Order codes	V _{DSS}	R _{DS(on)} max	I _D
STD75N3LLH6		< 0.0055 Ω	
STP75N3LLH6	30 V		75 A
STU75N3LLH6	30 v	< 0.0059 Ω	75 A
STU75N3LLH6-S			

- R_{DS(on)} * Q_g industry benchmark
- Extremely low on-resistance R_{DS(on)}
- High avalanche ruggedness
- Low gate drive power losses



Switching applications

Description

This N-Channel Power MOSFET product utilizes the 6th generation of design rules of ST's proprietary STripFETTM technology, with a new gate structure. The resulting Power MOSFET exhibits the lowest $R_{DS(on)}$ in all packages.

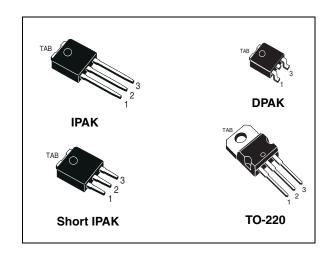


Figure 1. Internal schematic diagram

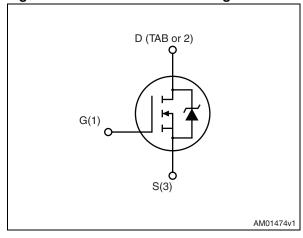


Table 1. Device summary

Order codes	Marking	Package	Packaging
STD75N3LLH6		DPAK	Tape and reel
STP75N3LLH6	75N3LLH6	TO-220	
STU75N3LLH6	/SNOLLHO	IPAK	Tube
STU75N3LLH6-S		Short IPAK	

July 2011 Doc ID 15978 Rev 4 1/21

Contents

1	Electrical ratings	. 3
2	Electrical characteristics	
3	Test circuits	
4	Package mechanical data	10
5	Packaging mechanical data	18
6	Revision history	20

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage (V _{GS} = 0)	30	V
V _{GS}	Gate-source voltage	±20	V
I _D ⁽¹⁾	Drain current (continuous) at T _C = 25 °C	75	Α
I _D	Drain current (continuous) at T _C = 70 °C	56	Α
I _D	Drain current (continuous) at T _C = 100 °C	47	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	300	Α
P _{TOT}	Total dissipation at T _C = 25 °C	60	W
	Derating factor	0.4	W/°C
T _j T _{stg}	Operating junction temperature storage temperature	-55 to 175	°C

^{1.} The value is rated according to Rthj-case

Table 3. Thermal data

Symbol	Parameter	DPAK	TO-220	IPAK	Short IPAK	Unit
R _{thj-case}	Thermal resistance junction-case (drain) (steady state)	2.5				°C/W
R _{thj-amb}	Thermal resistance junction-amb max	100	62.5	62.5 100		°C/W
R _{thj-pcb} ⁽¹⁾	Thermal resistance junction-pcb max	35	35		°C/W	
T _J	Maximum lead temperature for soldering purpose	275	75 300 275		°C	

^{1.} When mounted on FR-4 board of 1in², 2oz Cu. t < 10 sec

^{2.} Pulse width limited by safe operating area

2 Electrical characteristics

 $(T_{CASE}=25^{\circ}C \text{ unless otherwise specified})$

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage (V _{GS} = 0)	I _D = 250 μA	30			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 30 V V _{DS} =30 V T _C = 125 °C			1 10	μ Α μ Α
I _{GSS}	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ± 20 V			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.7	2.5	V
		$V_{GS} = 10 \text{ V}, I_D = 37.5 \text{ A}$ SMD version		0.0042	0.0055	Ω
D	R _{DS(on)} Static drain-source on resistance	$V_{GS} = 10 \text{ V}, I_D = 37.5 \text{ A}$		0.0046	0.0059	Ω
nDS(on) re		V_{GS} = 4.5 V, I_D = 37.5 A SMD version		0.0065	0.008	Ω
		$V_{GS} = 4.5 \text{ V}, I_D = 37.5 \text{ A}$		0.0069	0.0084	Ω

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GS} = 0$	1350 230 140	1690 290 176	2030 350 210	pF pF pF
Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	$V_{DD} = 15 \text{ V}, I_{D} = 75 \text{ A},$ $V_{GS} = 4.5 \text{ V}$ (see <i>Figure 14</i>)		17 8 6	23.8 11.2 8.4	nC nC nC
Q _{gs1}	Pre V _{th} gate-to-source charge Post V _{th} gate-to-source charge	V _{DD} =15 V, I _D = 75 A V _{GS} =5 V (Figure 19)		3.9 4.1	5.5 5.7	nC nC
R _G	Gate input resistance	f=1 MHz gate bias Bias=0 test signal level=20 mV open drain	1.25	1.7	2	Ω

Table 6. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max	Unit
t _{d(on)} t _r t _{d(off)} t _f	Turn-on delay time Rise time Turn-off delay time Fall time	$V_{DD} = 15 \text{ V}, I_D = 37.5 \text{ A}$ $R_G = 4.7 \Omega V_{GS} = 5 \text{ V}$ (see <i>Figure 13</i>)	-	9.5 30 37 12	-	ns ns ns ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current Source-drain current (pulsed)		-		75 300	A A
V _{SD} (2)	Forward on voltage	I _{SD} = 37.5 A, V _{GS} = 0	-		1.1	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I_{SD} = 75 A, di/dt = 100 A/µs, V_{DD} = 24 V (see <i>Figure 15</i>)	-	24 16.8 1.4		ns nC A

^{1.} Pulse width limited by safe operating area.

^{2.} Pulsed: Pulse duration = 300 μ s, duty cycle 1.5%

2.1 Electrical characteristics (curves)

10

VDS(V)

Figure 2. Safe operating area

Figure 3. Thermal impedance

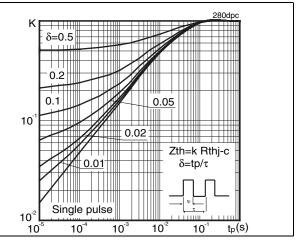


Figure 4. Output characteristics

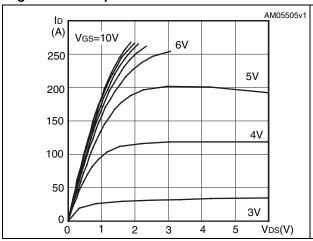


Figure 5. Transfer characteristics

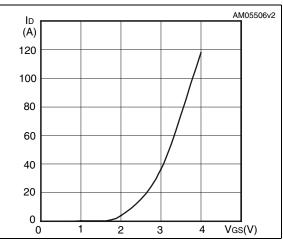


Figure 6. Normalized BV_{DSS} vs temperature

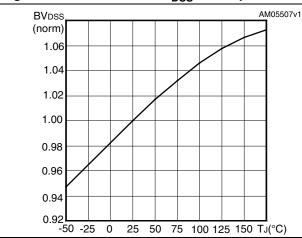
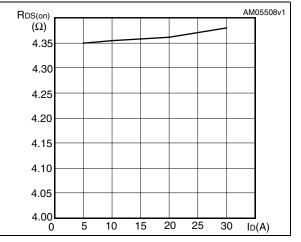


Figure 7. Static drain-source on resistance



Doc ID 15978 Rev 4

Downloaded from Arrow.com.

AM05509v1 AM05510v1 Vgs (pF) (V) VDD=15V 12 2500 ID=75A 10 2000 8 Ciss 1500 6 1000 4 500 2 Coss Crss 0 40 V_{DS}(V) 10 20 30 50 Qg(nC) 10 20

Figure 8. Gate charge vs gate-source voltage Figure 9. Capacitance variations

Figure 10. Normalized gate threshold voltage Figure 11. Normalized on resistance vs vs temperature temperature

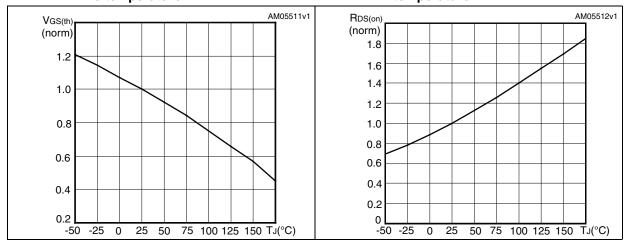
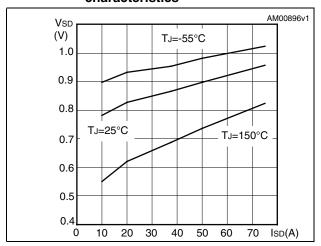


Figure 12. Source-drain diode forward characteristics



577

3 Test circuits

Figure 13. Switching times test circuit for resistive load

Figure 14. Gate charge test circuit

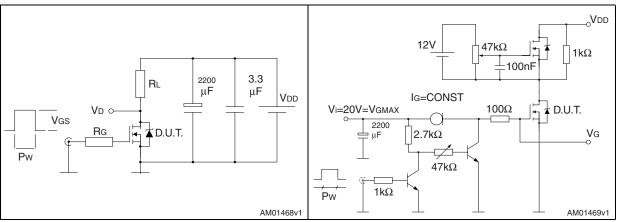


Figure 15. Test circuit for inductive load switching and diode recovery times

Figure 16. Unclamped inductive load test circuit

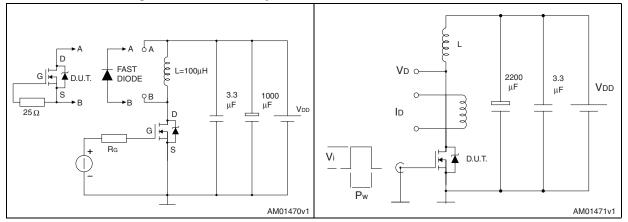


Figure 17. Unclamped inductive waveform

Figure 18. Switching time waveform

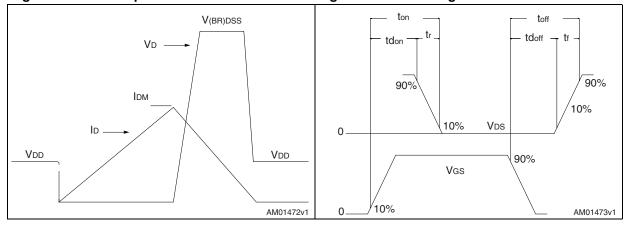
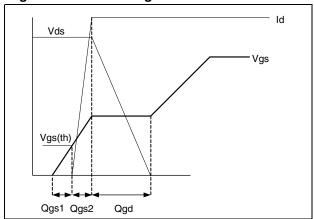


Figure 19. Gate charge waveform



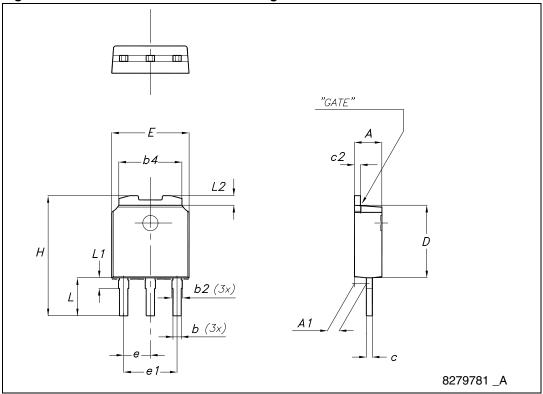
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 8. Short IPAK mechanical dimensions

Dim	mm				
Dim.	Min	Тур	Max		
А	2.20		2.40		
A1	0.90		1.10		
b	0.64		0.90		
b2			0.95		
b4	5.20		5.40		
С	0.45		0.60		
c2	0.48		0.60		
D	6.00		6.20		
E	6.40		6.60		
е		2.25			
e1	4.40		4.60		
Н	9.80		10.40		
L	3.00		3.40		
L1	0.80		1.20		
L2		0.80	1.00		

Figure 20. Short IPAK mechanical drawing



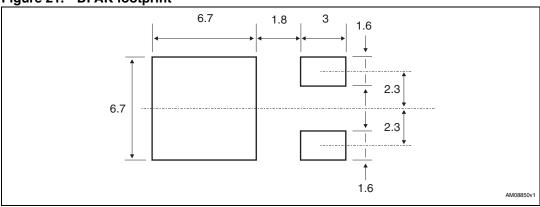
577

Doc ID 15978 Rev 4

Table 9. DPAK (TO-252) mechanical data

Dim	mm		
Dim.	Min.	Тур.	Max.
Α	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
С	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
е		2.28	
e1	4.40		4.60
Н	9.35		10.10
L	1		1.50
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0°		8°

Figure 21. DPAK footprint^(a)



a. All dimension are in millimeters

THERMAL PAD

E1

D1

R

GAUGE PLANE

1

O068772_H

Figure 22. DPAK (TO-252) drawing

Table 10. IPAK (TO-251) mechanical data

	1 7 11 (10 201) 11 10 11 11 10 11				
DIM.	mm.				
Diw.	min.	typ	max.		
Α	2.20		2.40		
A1	0.90		1.10		
b	0.64		0.90		
b2			0.95		
b4	5.20		5.40		
B5		0.3			
С	0.45		0.60		
c2	0.48		0.60		
D	6.00		6.20		
E	6.40		6.60		
е		2.28			
e1	4.40		4.60		
Н		16.10			
L	9.00		9.40		
L1	0.80		1.20		
L2		0.80	1.00		
V1		10 °			

Figure 23. IPAK (TO-251) drawing

Table 11. TO-220 type A mechanical data

Dim.	mm				
	Min.	Тур.	Max.		
Α	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.70		
С	0.48		0.70		
D	15.25		15.75		
D1		1.27			
E	10		10.40		
е	2.40		2.70		
e1	4.95		5.15		
F	1.23		1.32		
H1	6.20		6.60		
J1	2.40		2.72		
L	13		14		
L1	3.50		3.93		
L20		16.40			
L30		28.90			
ØP	3.75		3.85		
Q	2.65		2.95		

Figure 24. TO-220 type A drawing

5 Packaging mechanical data

Table 12. DPAK (TO-252) tape and reel mechanical data

Таре				Reel		
Dim.	n	nm	Dim.	mm		
	Min.	Max.		Min.	Max.	
A0	6.8	7	А		330	
В0	10.4	10.6	В	1.5		
B1		12.1	С	12.8	13.2	
D	1.5	1.6	D	20.2		
D1	1.5		G	16.4	18.4	
Е	1.65	1.85	N	50		
F	7.4	7.6	Т		22.4	
K0	2.55	2.75				
P0	3.9	4.1		Base qty.	2500	
P1	7.9	8.1		Bulk qty.	2500	
P2	1.9	2.1				
R	40					
Т	0.25	0.35				
W	15.7	16.3				

Figure 25. Tape for DPAK (TO-252)

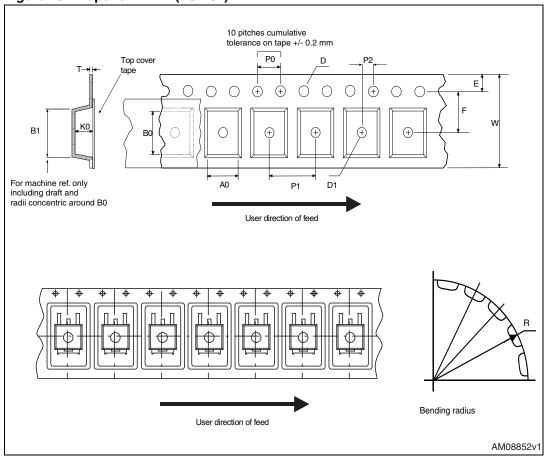
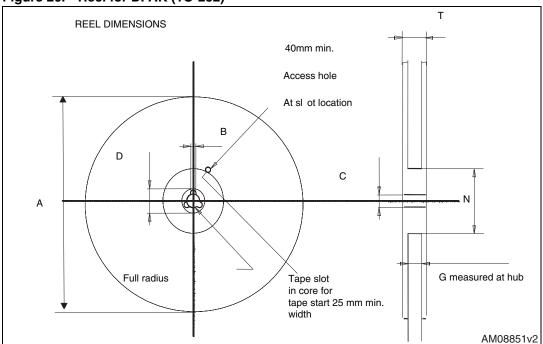


Figure 26. Reel for DPAK (TO-252)



577

Doc ID 15978 Rev 4

6 Revision history

Table 13. Document revision history

Date	Revision	Changes
01-Jul-2009	1	First issue.
02-Oct-2009	2	Added device in Short IPAK. Document status promoted from preliminary data to datasheet.
19-Apr-2011	3	 Added max values in <i>Table 5: Dynamic</i>. Added new package and mechanical data. Inserted new I_D value @ 70 °C (see <i>Table 2: Absolute maximum ratings</i>)
04-Jul-2011	4	Updated: mechanical data

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2011 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

577

Doc ID 15978 Rev 4