



74LVC1G08

SINGLE 2 INPUT POSITIVE AND GATE

Description

The 74LVC1G08 is a single 2-input positive AND gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

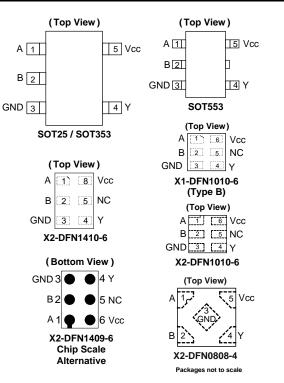
The gate performs the positive Boolean function:

$$Y = A \bullet B$$
 or $Y = \overline{\overline{A} + \overline{B}}$

Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- ± 24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept Up to 5.5V
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
 - Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Direct Interface with TTL Levels
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as.
 - PCs, Networking, Notebooks, Netbooks, PDAs
 - Tablet Computers, E-readers
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players , Cameras, Video Recorders

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information (Note 4)

		XXX -7	
Logic Device	Function	Package	Packing
74 : Logic Prefix	08 : 2-Input	W5 : SOT25	-7 : 7" Tape & Reel
LVC : 1.65 to 5.5 V	AND Gate	SE : SOT353	
Logic Family		Z : SOT553	
1G : One Gate		FS3 : X2-DFN0808-4	
		FW5 : X1-DFN1010-6	(Туре В)
		FW4 :X2-DFN1010-6	
		FX4 : X2- DFN1409-6	
		FZ4 : X2- DFN1410-6	

Dort Number	Package	Package	Package	7" Tape	and Reel
Part Number	Code (Notes 5 & 6) Size		Quantity	Part Number Suffix	
74LVC1G08W5-7	W5	SOT25	3.0mm x 2.8mm x 1.2mm 0.95mm lead pitch	3,000/Tape & Reel	-7
74LVC1G08SE-7	SE	SOT353	2.0mm x 2.0mm x 1.1mm 0.65mm lead pitch	3,000/Tape & Reel	-7
74LVC1G08Z-7	74LVC1G08Z-7 Z SOT553 1.6mm x 1.6 mm x 0.62mm 0.5mm lead pitch		1.6mm x 1.6 mm x 0.62mm 0.5mm lead pitch	4,000/Tape & Reel	-7
74LVC1G08FS3-7	VC1G08ES3-7 ES3 X2-DEN0808-4		0.8mm x 0.8mm x 0.35mm 0.5mm pad pitch (diamond)	5,000/Tape & Reel	-7
74LVC1G08FW5-7	5-7 FW5 X1-DFN1010-6 (Type B)		1.0mm x 1.0mm x 0.5mm 0.35mm pad pitch	5,000/Tape & Reel	-7
74LVC1G08FW4-7	74I VC1G08FW4-7 FW4 X2-DEN1010-6		1.0mm x 1.0mm x 0.4mm 0.35mm pad pitch	5,000/Tape & Reel	-7
74LVC1G08FX4-7	FX4	4 X2-DFN1409-6 (Chip scale alternative) 1.4mm x 0.9mm x 0.4mm 0.5mm pad pitch		5,000/Tape & Reel	-7
74LVC1G08FZ4-7	FZ4 X2-DFN1410-6		1.4mm x 1.0mm x 0.4mm 0.5mm pad pitch	5,000/Tape & Reel	-7

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

5. Pad layout as shown on Diodes Inc. suggested pad layout which can be found on our website at http://www.diodes.com/package-outlines.html.
 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

Pin Descriptions

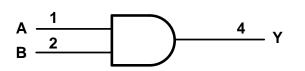
Notes:

Pin Name	Description	
А	Data Input	
В	Data Input	
GND	Ground	
Y	Data Output	
V _{CC}	Supply Voltage	
NC	No Connection	

Function Table

Inp	Inputs		
Α	В	Y	
Н	Н	Н	
L	Х	L	
Х	L	L	

Logic Diagram





Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State.	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current VI < 0	-50	mA
I _{OK}	Output Clamp Current	-50	mA
Ι _Ο	Continuous Output Current	±50	mA
I _{CC,} I _{GN}	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Absolute Maximum Ratings (Notes 7 & 8) (@T_A = +25°C, unless otherwise specified.)

7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device Notes:

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range..

Recommended Operating Conditions (Note 9) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit
N/		Operating	1.65	5.5	V
Vcc	Operating Voltage	Data Retention Only	1.5	_	V
		$V_{CC} = 1.65 V$ to 1.95 V	$0.65 \times V_{CC}$	—	
Max	V _{IH} High-Level Input Voltage	$V_{CC} = 2.3 V$ to 2.7	1.7	—	V
VIH		V _{CC} = 3 V to 3.6V	2	—	V
		$V_{CC} = 4.5V$ to 5.5V	0.7 x V _{CC}	—	
		V _{CC} = 1.65V to 1.95V	—	0.35 x V _{CC}	
		V _{CC} = 2.3V to 2.7V	_	0.7	N/
VIL	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V
		V _{CC} = 4.5V to 5.5V	_	0.3 x V _{CC}	
VI	Input Voltage			5.5	V
Vo	Output Voltage		0	Vcc	V
		V _{CC} = 1.65V	_	-4	
		$V_{CC} = 2.3V$	_	-8	
	Lligh Lovel Output Current	V _{CC} = 2.7V	_	-12	~ ^
Юн	ligh-Level Output Current	V/ - 2V/	_	-16	mA
		V _{CC} = 3V	—	-24	
		$V_{CC} = 4.5V$	—	-32	
		V _{CC} = 1.65V	_	4	
		$V_{CC} = 2.3V$	—	8	
I _{OL}	Low-Level Output Current	V _{CC} = 2.7V	_	12	mA
IOL		$V_{CC} = 3V$	—	16	IIIA
		V _{CC} = 3V	—	24	
		$V_{CC} = 4.5V$	—	32	
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	—	20	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	—	10	ns/V
		$V_{CC} = 5V \pm 0.5V$	—	5	
T _A	Operating Free-Air Temperature	—	-40	+125	°C

Note: 9. Unused inputs should be held at $V_{\mbox{CC}}\,\mbox{or Ground}.$



Symbol	Parameter	Test Conditions	Vaa	-40	0°C to +85°	С	-40°C to	+125°C	Unit
Symbol			Vcc	Min	Тур.	Max	Min	Max	Unit
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} – 0.1		—	V _{CC} – 0.1		
		I _{OH} = -4mA	1.65V	1.2		—	0.95		
		I _{OH} = -8mA	2.3V	1.9		—	1.7		
V _{OH}	High Level Output Voltage	I _{OH} = -12mA	2.7V	2.2	-	—	1.9	_	V
	e alput voltage	I _{OH} = -16mA	2)/	2.4		_	2.2	_	
		I _{OH} = -24mA	3V	2.3	_	—	2.0	_	
		I _{OH} = -32mA	4.5V	3.8	_	—	3.4	_	
		I _{OL} = 100μA	1.65V to 5.5V	—	_	0.1	—	0.1	
		I _{OL} = 4mA	1.65V	—	_	0.45	—	0.7	
		I _{OL} = 8mA	2.3V	—	_	0.3	_	0.45	
V _{OL}	Low Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.4	_	0.6	V
	ouput voltage	I _{OL} = 16mA	2)/	—	_	0.4	_	0.6	
		$I_{OL} = 24mA$	3V	_	_	0.55	_	0.8	
		$I_{OL} = 32mA$	4.5V	—	_	0.55	—	.8	
h	Input Current	V _I = 5.5V or GND	0 to 5.5V	—	± 0.1	±5	—	± 100	μA
I _{OFF}	Power Down Leakage Current	V_{I} or $V_{O} = 5.5V$	0V	_	_	±10	_	±200	μA
Icc	Supply Current	$V_I = 5.5V$ or GND, $I_O = 0$	5.5V	—	0.1	10	_	200	μA
ΔI _{CC}	Additional Supply Current	One input at V_{CC} -0.6V Other inputs at V_{CC} or GND	3V to 5.5V	—	_	500	—	5,000	μA
Ci	Input Capacitance	$V_i = V_{CC}$ — or GND	3.3V	—	5	_	_	_	pF

Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Package Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25^{\circ}C$)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур	Max	Unit
		SOT25		_	204	_	
		SOT353		_	371	_	
		SOT553		_	231	_	
0	Thermal Resistance	X2-DFN0808-4	(Note 10)	_	400	_	°C/W
θ _{JA}	Junction-to-Ambient	X1-DFN1010-6 (Type B)	(Note 10)	_	435	_	°C/VV
		X2-DFN1010-6		_	445	_	
		X2-DFN1409-6		_	470	_	
		X2-DFN1410-6		_	460	_	
		SOT25		_	52	_	
		SOT353		_	143	_	
		SOT553		_	105	_	
0	Thermal Resistance Junction-to-Case	X2-DFN0808-4		_	225	_	°C/W
θις		X1-DFN1010-6 (Type B)	(Note 10)	_	250	_	°C/vv
		X2-DFN1010-6		_	250	_	
		X2-DFN1409-6		_	275	_	
		X2-DFN1410-6		_	265	_	

Note: 10. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

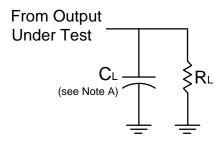
Parameter	From	То	V	TA	= -40°C to +85	5°C	T _A = -40°C	to +125°C	Unit
Input	Output	t Output ^V C	V _{cc}	Min	Тур.	Max	Min	Max	Onit
		1.8V ± 0.15V	1.0	3.4	8.0	1.0	10.5		
		-	2.5V ± 0.2V	0.5	2.2	5.5	0.5	7.0	
t _{pd}	A or B	or B Y	2.7V	0.5	2.5	5.5	0.5	7.0	ns
		-	3.3V ± 0.3V	0.5	2.1	4.5	0.5	6.0	
			5.0V ± 0.5V	0.5	1.7	4.0	0.5	5.5	

Operating Characteristics

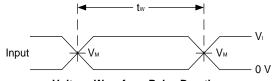
T _A = +25°C								
Parameter		Test Conditions	V _{CC} = 1.8V	$V_{CC} = 2.5V$	$V_{CC} = 3.3V$	$V_{CC} = 5V$	Unit	
	Faidilietei		Тур	Тур	Тур	Тур	Onic	
C _{pd}	Power Dissipation Capacitance	f = 10MHz	16	16	16	16	pF	



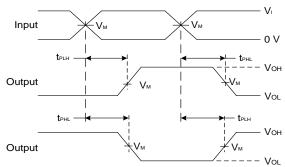
Parameter Measurement Information



N	In	puts	, v	<u> </u>	р	
Vcc	VI	t _r /t _f	VM	C∟	RL	
1.8V ± 0.15V	V _{CC}	≤2ns	V _{CC} /2	30pF	1ΚΩ	
2.5V ± 0.2V	V _{CC}	≤2ns	V _{CC} /2	30pF	500Ω	
2.7V	Vcc	≤2.5ns	1.5V	50pF	500Ω	
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω	
5.0V ± 0.5V	Vcc	≤2.5ns	V _{CC} /2	50pF	500Ω	







Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

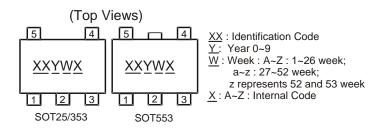
- B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.

D. tPLH and tPHL are the same as tPD.



Marking Information

(1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G08W5-7	SOT25	UV
74LVC1G08SE-7	SOT353	UV
74LVC1G08Z-7	SOT553	UV

(2) DFN Packages



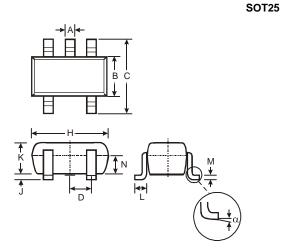
XX: Identification Code <u>Y</u>: Year 0~9

<u>Y</u>: Year 0-9 <u>W</u>: Week : A-Z : 1-26 week; a-z : 27-52 week; z represents 52 and 53 week <u>X</u>: A-Z : Internal Code

Part Number	Package	Identification Code
74LVC1G08FS3-7	X2-DFN0808-4	WV
74LVC1G08FW5-7	X1-DFN1010-6 (Type B)	V7
74LVC1G08FW4-7	X2-DFN1010-6	UV
74LVC1G08FX4-7	X2-DFN1409-6	MF
74LVC1G08FZ4-7	X2-DFN1410-6	UV



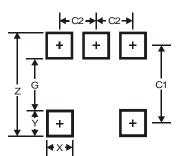
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT25			
Dim	Min	Max	Тур
Α	0.35	0.50	0.38
В	1.50	1.70	1.60
С	2.70	3.00	2.80
D	-	-	0.95
н	2.90	3.10	3.00
J	0.013	0.10	0.05
к	1.00	1.30	1.10
L	0.35	0.55	0.40
Μ	0.10	0.20	0.15
Ν	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

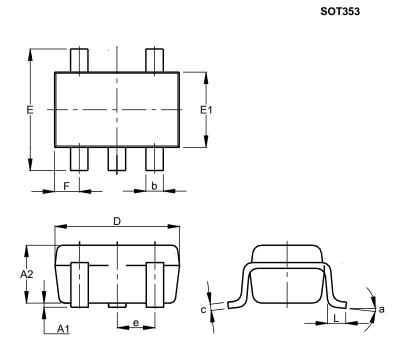


SOT25

Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



Please see http://www.diodes.com/package-outlines.html for the latest version.

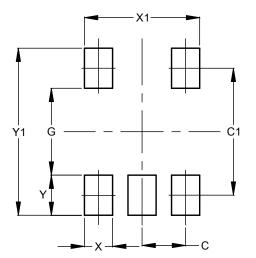


	SOT353			
Dim	Min	Min Max Typ		
A1	0.00	0.10	0.05	
A2	0.90	1.00	1.00	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
E	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	e 0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All	All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

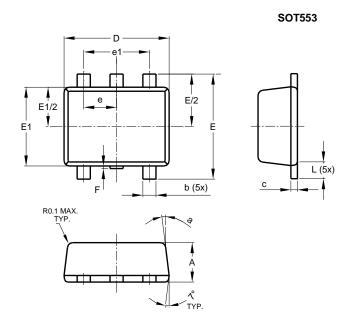
SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Ý	0.600
Y1	2.500



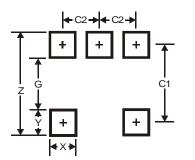
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT553			
Dim	Min	Max	Тур
Α	0.55	0.62	0.60
b	0.15	0.30	0.20
С	0.10	0.18	0.15
D	1.50	1.70	1.60
Ε	1.55	1.70	1.60
E1	1.10	1.25	1.20
е	0.50 BSC		
e1	1.	00 BS(0
F	0.00	0.10	
L	0.10	0.30	0.20
а	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



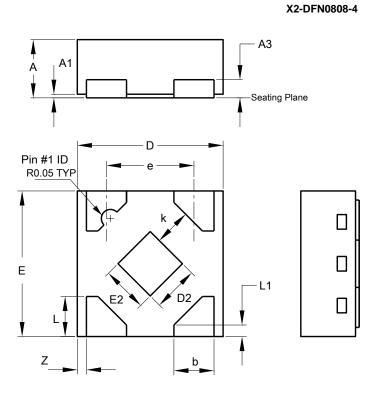
SOT553

Dimensions	Value
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5

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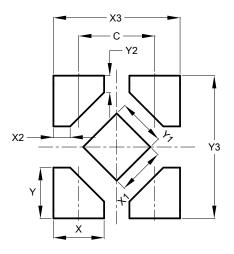


	X2-DFN0808-4		
Dim	Min	Max	Тур
Α	0.25	0.35	0.30
A1	0	0.04	0.02
A3	-	-	0.13
b	0.17	0.27	0.22
D	0.75	0.85	0.80
D2	0.15	0.35	0.25
E	0.75	0.85	0.80
E2	0.15	0.35	0.25
е	-	-	0.48
k	0.20	-	-
L	0.17	0.27	0.22
L1	0.02	0.12	0.07
z	-	-	0.05
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0808-4



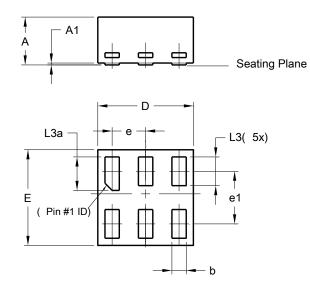
Dimensions	Value
С	0.480
Х	0.320
X1	0.300
X2	0.106
X3	0.800
Y	0.320
Y1	0.300
Y2	0.106
Y3	0.900

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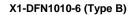
X1-DFN1010-6 (Type B)

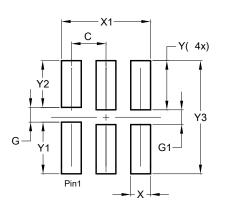


	X1-DFN1010-6 (Type B)		
Dim	Min	Max	Тур
Α	-	0.50	0.39
A1	-	0.04	-
b	0.12	0.20	0.15
D	0.95	1.050	1.00
Е	0.95	1.050	1.00
е	0.35 BSC		
e1	0.55 BSC		
L3	0.27	0.30	0.30
L3a	0.32	0.40	0.35
All	All Dimensions in mm		

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

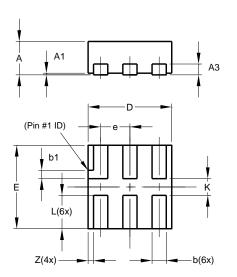




Dimensions	Value
Dimensione	(in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Y	0.500
Y1	0.525
Y2	0.475
Y3	1.150



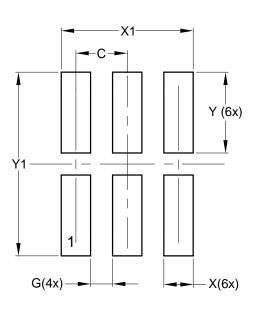
Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1010-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.14	0.20	0.17
b1	0.05	0.15	0.10
D	0.95	1.05	1.00
E	0.95	1.05	1.00
е			0.35
L	0.35	0.45	0.40
K	0.15		
Z			0.065
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1010-6

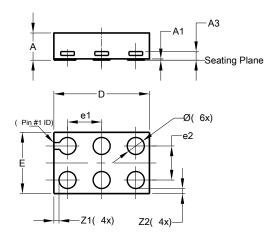
X2-DFN1010-6

Dimensions	Value (in mm)	
С	0.350	
G	0.150	
Х	0.200	
X1	0.900	
Y	0.550	
Y1	1.250	



Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1409-6 CHIP SCALE ALTERNATIVE

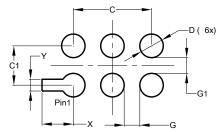


X2-DFN1409-6			
Dim	Min	Max	Тур
Α	-	0.40	0.39
A1	0	0.05	0.02
A3	-	-	0.13
Ø	0.20	0.30	0.25
D	1.35	1.45	1.40
Е	0.85	0.95	0.90
e1	-	-	0.50
e2	-	-	0.50
Z1	-	-	0.075
Z2	-	-	0.075
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

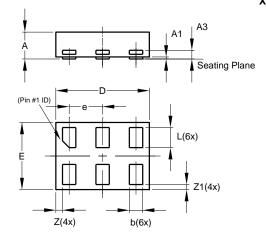
X2-DFN1409-6 CHIP SCALE ALTERNATIVE



Dimensions	Value	
С	(in mm) 1.000	
C1	0.500	
D	0.300	
G	0.200	
G1	0.200	
Х	0.400	
Y	0.150	



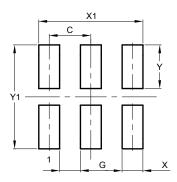
Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1410-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
ш	0.95	1.05	1.00
e			0.50
L	0.25	0.35	0.30
Z		_	0.10
Z1	0.045	0.105	0.075
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1410-6

Dimensions	Value (in mm)
C	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250

X2-DFN1410-6



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