

# Optocoupler, Phototransistor Output, AC Input, Low Input Current, 4 Pin LSOP, Long Creepage Mini-Flat Package



#### **DESCRIPTION**

The VOL628A has two GaAs infrared emitting diodes, which are optically coupled to a silicon planar phototransistor detector, and are incorporated in a 4 pin LSOP wide body package.

It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits.

#### **FEATURES**

- · Low profile package
- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- Isolation test voltage, 5000 V<sub>RMS</sub>
- Low coupling capacitance
- · High common mode transient immunity
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

#### **APPLICATIONS**

- Telecom
- · Industrial controls
- · Battery powered equipment
- · Office machines
- Programmable controllers

#### **AGENCY APPROVALS**

(All parts are certified under base model VOL628A)

- UL1577, file no. E76222
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI: EN 60065:2002, EN 60950-1:2006
- FIMKO EN60950-1
- CQC: GB8898-2011, GB4943.1-2011

ORDERING INFORMATION							
V O L 6 2 8 A - # X 0 0 1 T  PART NUMBER CTR PACKAGE OPTION TAPE AND REEL							
AOENOV OEDTIFIED / DAOVAGE	CTR (%)						
AGENCY CERTIFIED / PACKAGE	1 mA						
UL, cUL, BSI, FIMKO, CQC	50 to 600	63 to 125	100 to 200	160 to 320			
4 pin LSOP, mini-flat, long creepage	VOL628AT	VOL628A-2T	VOL628A-3T	VOL628A-4T			
UL, cUL, BSI, FIMKO, CQC, VDE (option 1)	50 to 600	63 to 125	100 to 200	160 to 320			
4 pin LSOP, mini-flat, long creepage	-	VOL628A-2X001T	VOL628A-3X001T	VOL628A-4X001T			

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V <sub>R</sub>	6	V			
Power dissipation		P <sub>diss</sub>	100	mW			
Forward current		I <sub>F</sub>	± 60	mA			
Junction temperature		Tj	125	°C			
OUTPUT							
Collector emitter voltage		V <sub>CEO</sub>	80	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		I <sub>C</sub>	50	mA			
Collector current	$t_p/T = 0.5, t_p < 10 \text{ ms}$	I <sub>C</sub>	100	mA			
Power dissipation		P <sub>diss</sub>	150	mW			
Junction temperature		Tj	125	°C			
COUPLER							
Total power dissipation		P <sub>tot</sub>	250	mW			
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C			
Ambient temperature range		T <sub>amb</sub>	-55 to +110	°C			
Soldering temperature (1)	≤ 10 s	T <sub>sld</sub>	260	°C			

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability
- (1) Refer to reflow profile for soldering conditions for surface mounted devices

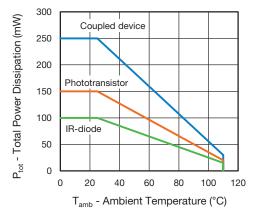


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	INPUT						
Forward voltage	$I_F = \pm 5 \text{ mA}$		$V_{F}$	ı	1.16	1.5	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	ı	ı	100	μΑ
Capacitance	$V_R = 0 V$ , $f = 1 MHz$		Co	-	45	-	рF
OUTPUT	OUTPUT						
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$		I <sub>CEO</sub>	-	10	200	nA
Collector emitter capacitance	$V_{CE} = 5 \text{ V}, f = 1 \text{ MHz}$		C <sub>CE</sub>	-	7	-	рF
COUPLER							
	$I_C = 0.2 \text{ mA}, I_F = \pm 1 \text{ mA}$	VOL628A	V <sub>CEsat</sub>	-	0.25	0.4	V
Collector emitter	$I_C = 0.32 \text{ mA}, I_F = \pm 1 \text{ mA}$	VOL628A-2T	V <sub>CEsat</sub>		0.25	0.4	V
saturation voltage	$I_C = 0.5 \text{ mA}, I_F = \pm 1 \text{ mA}$	VOL628A-3T	V <sub>CEsat</sub>	-	0.25	0.4	V
	$I_C = 0.8 \text{ mA}, I_F = \pm 1 \text{ mA}$	VOL628A-4T	V <sub>CEsat</sub>	-	0.25	0.4	V
Coupling capacitance	f = 1 MHz		C <sub>C</sub>	-	0.25	-	pF

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements



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# Vishay Semiconductors

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$I_F = \pm 1 \text{ mA}, V_{CE} = 5 \text{ V}$	VOL628A	CTR	50	-	600	%
		VOL628A-2	CTR	63	-	125	%
		VOL628A-3	CTR	100	-	200	%
		VOL628A-4	CTR	160	-	320	%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn on time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>on</sub>	-	6	-	μs
Rise time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>r</sub>	1	3.5	-	μs
Turn off time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>off</sub>	1	5.5	-	μs
Fall time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>f</sub>	-	5	-	μs

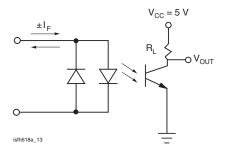


Fig. 2 - Test Circuit

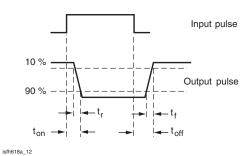


Fig. 3 - Test Circuit and Waveforms

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 110 / 21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	275				
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5000	V <sub>RMS</sub>			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	1050	V <sub>peak</sub>			
	$T_{amb} = 25  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	T <sub>amb</sub> = 100 °C, V <sub>IO</sub> = 500 V	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
	$T_{amb} = TS$ , $V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω			
Output safety power		P <sub>SO</sub>	265	mW			
Input safety current		I <sub>SI</sub>	130	mA			
Input safety temperature		T <sub>S</sub>	150	°C			
Creepage distance			≥ 8	mm			
Clearance distance			≥ 8	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Input to output test voltage, method B	$V_{IORM}$ x 1.875 = $V_{PR}$ , 100 % production test with $t_M$ = 1 s, partial discharge < 5 pC	V <sub>PR</sub>	2000	V <sub>peak</sub>			
Input to output test voltage, method A	$V_{IORM}$ x 1.6 = $V_{PR}$ , 100 % sample test with $t_M$ = 10 s, partial discharge < 5 pC	V <sub>PR</sub>	1680	V <sub>peak</sub>			

#### Note

 According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2, (see Fig. 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits

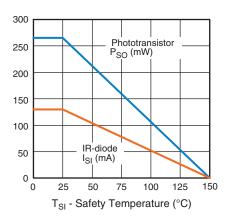


Fig. 4 - Derating Diagram

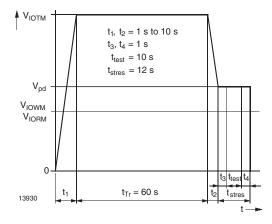


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

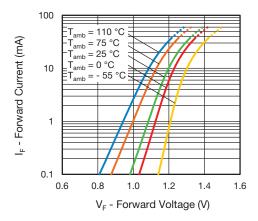


Fig. 6 - Forward Current vs. Forward Voltage

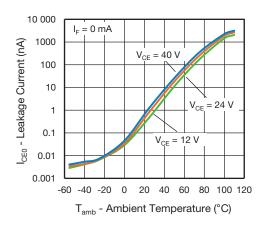


Fig. 8 - Collector Emitter Current vs. Ambient Temperature

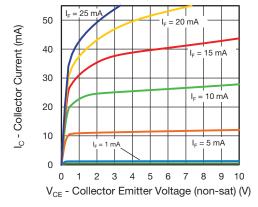


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

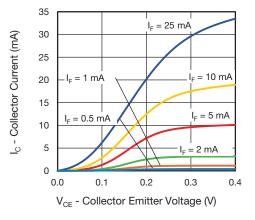


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)



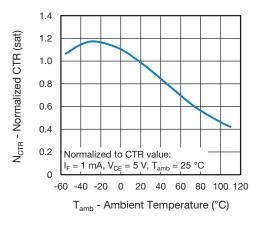


Fig. 10 - Normalized Current Transfer Ratio vs. Ambient Temperature (saturated)

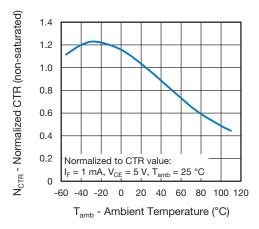


Fig. 11 - Normalized Current Transfer Ratio vs. Ambient Temperature (non-saturated)

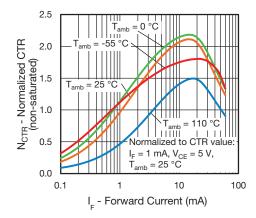


Fig. 12 - Normalized Current Transfer Ratio (non-saturated) vs. Forward Current

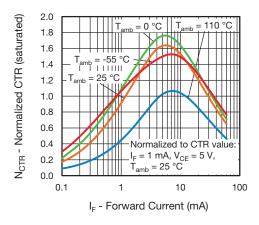


Fig. 13 - Normalized Current Transfer Ratio (saturated) vs. Forward Current

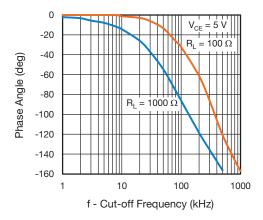


Fig. 14 - Phase Angle vs. Frequency

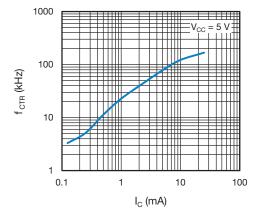


Fig. 15 - f<sub>CTR</sub> vs. Collector Current



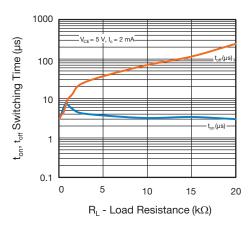


Fig. 16 - Switching Time vs. Load Resistance

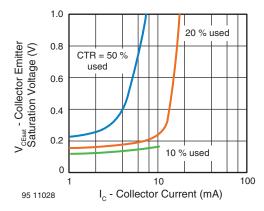


Fig. 17 - Collector Emitter Saturation Voltage vs. Collector Current

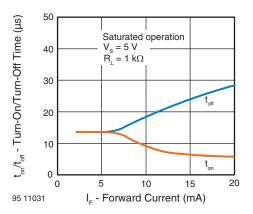


Fig. 18 - Turn-On/Turn-Off Time vs. Forward Current

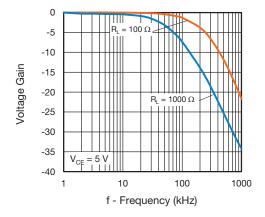
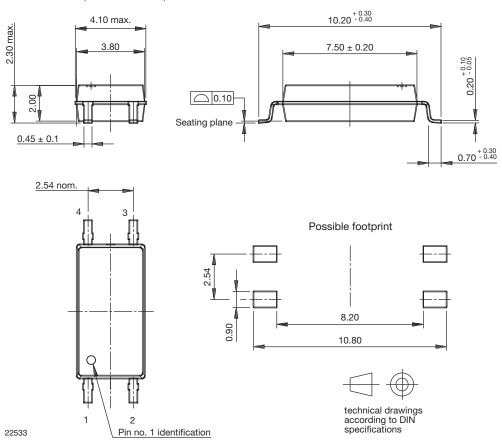


Fig. 19 - Voltage Gain vs. Cut-off Frequency

### **PACKAGE DIMENSIONS** (in millimeters)



### PACKAGE MARKING (example of VOL628A-3X001T)



#### Notes

- Only option 1 is reflected in the package marking with the characters "X1"
- Tape and reel suffix (T) is not part of the package marking

### TAPE AND REEL DIMENSIONS (in millimeters)

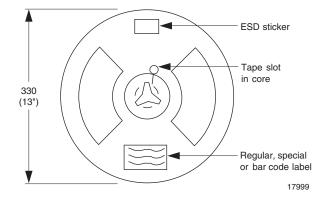


Fig. 20 - Reel Dimensions (3000 units per reel)

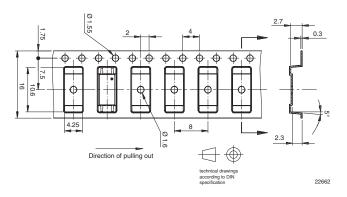


Fig. 21 - Tape Dimensions



### **SOLDER PROFILE**

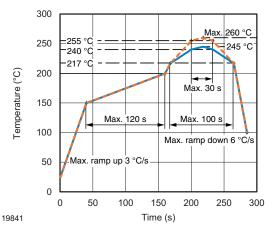


Fig. 22 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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