

STGW45HF60WD

45 A, 600 V ultra fast IGBT

Features

- Improved E_{off} at elevated temperature
- Low C_{RES} / C_{IES} ratio (no cross-conduction susceptibility)
- Ultra fast soft recovery antiparallel diode

Applications

- Welding
- High frequency converters
- Power factor correction



The "HF" family is based on a new advanced planar technology concept to yield an IGBT with more stable switching performance (E_{off}) versus temperature, as well as lower conduction losses. The "W" series is a subset of products tailored to high switching frequency operation (over 100 kHz).

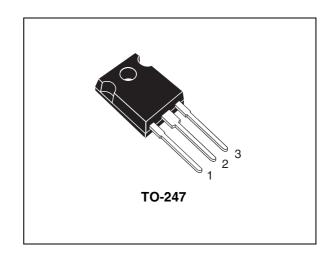


Figure 1. Internal schematic diagram

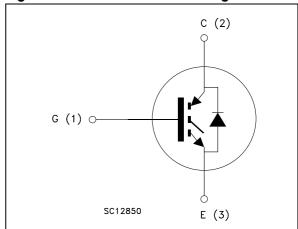


Table 1. Device summary (1)

Order code	Marking	Package	Packaging
	GW45HF60WDA		
STGW45HF60WD	GW45HF60WDB	TO-247	Tube
	GW45HF60WDC		

Collector-emitter saturation voltage is classified in group A, B and C, see Table 5: VCE(sat) classification. STMicroelectronics reserves the right to ship from any group according to production availability.

Electrical ratings STGW45HF60WD

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{GE} = 0)	600	V
I _C ⁽¹⁾	Continuous collector current at T _C = 25 °C	70	Α
I _C ⁽¹⁾	Continuous collector current at T _C = 100 °C	45	Α
I _{CP} ⁽²⁾	Pulsed collector current	150	Α
I _{CL} (3)	Turn-off latching current	80	Α
V _{GE}	Gate-emitter voltage	± 20	V
I _F	Diode RMS forward current at T _C = 25 °C	30	Α
I _{FSM}	Surge not repetitive forward current t _p = 10 ms sinusoidal	120	Α
P _{TOT}	Total dissipation at T _C = 25 °C	250	W
T _{stg}	Storage temperature	- 55 to 150	
Tj	Operating junction temperature	- 55 10 150	°C

^{1.} Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{j(max)} - T_{C}}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_{C}(T_{C}))}$$

- 2. Pulse width limited by maximum junction temperature and turn-off within RBSOA
- 3. V_{CLAMP} = 80% (V_{CES}), V_{GE} = 15 V, R_{G} = 10 Ω , T_{J} = 150 °C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
В	Thermal resistance junction-case IGBT	0.5	°C/W
R _{thj-case}	Thermal resistance junction-case diode	1.5	°C/W
R _{thj-amb}	Thermal resistance junction-ambient	50	°C/W

2 Electrical characteristics

 $(T_J = 25 \, ^{\circ}C \text{ unless otherwise specified})$

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)CES}	Collector-emitter breakdown voltage (V _{GE} = 0)	I _C = 1 mA	600			V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 30 A V _{GE} = 15V, I _C = 30 A,T _J = 125 °C		1.65	2.5	V V
V _{GE(th)}	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 1 \text{ mA}$	3.75		5.75	٧
I _{CES}	Collector cut-off current (V _{GE} = 0)	V _{CE} = 600 V V _{CE} = 600 V, T _J = 125 °C			500 5	μA mA
I _{GES}	Gate-emitter leakage current (V _{CE} = 0)	V _{GE} = ±20 V			± 100	nA

Table 5. V_{CE(sat)} classification

Symbol	Parameter	Group	Va	Unit	
Symbol	Farameter	Group	Min.	Max.	Oill
		Α	1.68	1.92	
V _{CE(sat)}	$V_{CE(sat)}$ Collector-emitter saturation voltage $V_{GE} = 15 \text{ V}, I_{C} = 30 \text{ A}$	В	1.88	2.17	٧
		С	2.13	2.50	

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{ies} C _{oes} C _{res}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{CE} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GE} = 0$	-	2900 260 55	-	pF pF pF
Q _g Q _{ge} Q _{gc}	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 400 \text{ V}, I_{C} = 30 \text{ A},$ $V_{GE} = 15 \text{ V},$ Figure 17	-	160 17 65	-	nC nC nC

Electrical characteristics STGW45HF60WD

Table 7. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A}$ $R_{G} = 6.8 \Omega, V_{GE} = 15 \text{ V},$ (Figure 16)	-	30 12 2600	-	ns ns A/µs
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A}$ $R_{G} = 6.8 \Omega, V_{GE} = 15 \text{ V},$ $T_{J} = 125 \text{ °C } (Figure 16)$	-	30 14 2200	-	ns ns A/µs
$t_r(V_{off})$ $t_d(_{off})$ t_f	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A},$ $R_{G} = 6.8 \Omega, V_{GE} = 15 \text{ V}$ (Figure 16)	-	30 145 50	-	ns ns ns
t _r (V _{off}) t _d (_{off}) t _f	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A},$ $R_{G} = 6.8 \Omega, V_{GE} = 15 \text{ V},$ $T_{J} = 125 \text{ °C}$ (Figure 16)	-	47 185 65	-	ns ns ns

Table 8. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
E _{on} ⁽¹⁾	Turn-on switching losses	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A}$		300		μJ
E_{off}	Turn-off switching losses	$R_G = 6.8 \Omega$, $V_{GE} = 15 V$,	-	330		μJ
E _{ts}	Total switching losses	(Figure 18)		630		μJ
E _{on} ⁽¹⁾	Turn-on switching losses	$V_{CC} = 400 \text{ V}, I_{C} = 30 \text{ A}$		550		μJ
E_{off}	Turn-off switching losses	$R_G = 6.8 \Omega$, $V_{GE} = 15 V$,	-	550	800	μJ
E _{ts}	Total switching losses	T _J = 125 °C (<i>Figure 18</i>)		1100		μJ

Eon is the tun-on losses when a typical diode is used in the test circuit in *Figure 18*. If the IGBT is offered
in a package with a co-pak diode, the co-pack diode is used as external diode. IGBTs & Diode are at the
same temperature (25 °C and 125 °C). Eon include diode recovery energy.

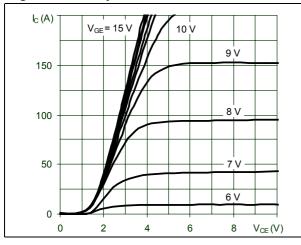
Table 9. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _F	L Forward on-voltage	I _F = 30 A I _F = 30 A, T _J = 125 °C	-	2 1.65	2.5	V V
t _{rr} Q _{rr} I _{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 30 \text{ A}, V_R = 50 \text{ V},$ $di/dt = 100 \text{ A}/\mu\text{s}$ (see Figure 19)	-	55 110 3	-	ns nC A
t _{rr} Q _{rr} I _{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 30 \text{ A}, V_R = 50 \text{ V},$ $di/dt = 100 \text{ A/}\mu\text{s}$ $T_J = 125 ^{\circ}\text{C}, \text{ (see Figure 19)}$	-	140 400 5.5	-	ns nC A

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

Figure 3. Transfer characteristics



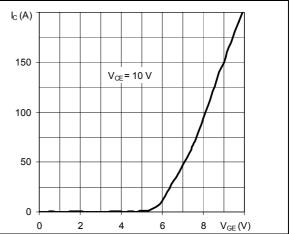
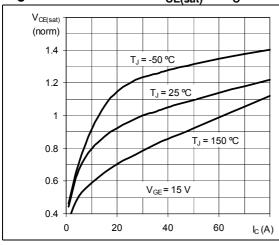


Figure 4. Normalized $V_{CE(sat)}$ vs. I_C

Figure 5. Normalized $V_{CE(sat)}$ vs. temperature



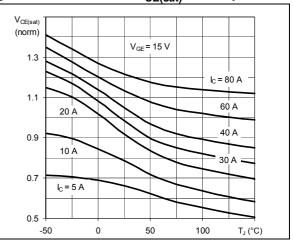
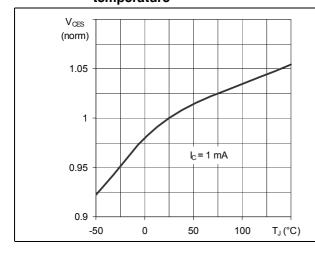
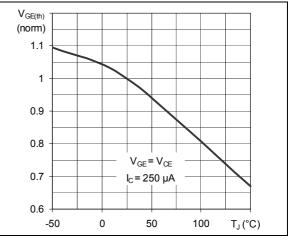


Figure 6. Normalized breakdown voltage vs. Figure 7. temperature

Normalized gate threshold voltage vs. temperature

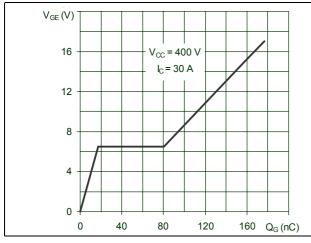




Electrical characteristics STGW45HF60WD

Figure 8. Gate charge vs. gate-emitter voltage

Figure 9. Capacitance variations



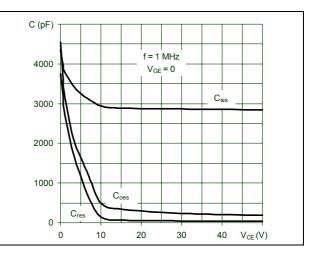
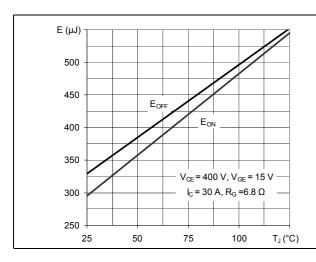


Figure 10. Switching losses vs temperature

Figure 11. Switching losses vs. gate resistance



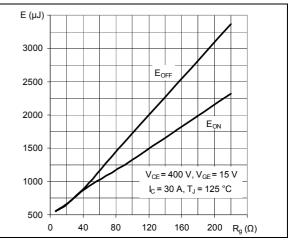
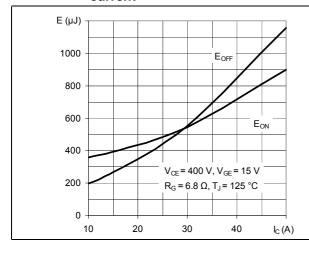


Figure 12. Switching losses vs. collector current

Figure 13. Turn-off SOA



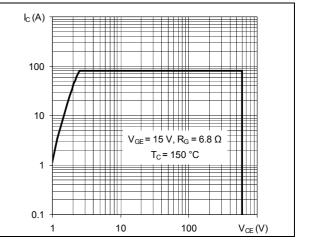
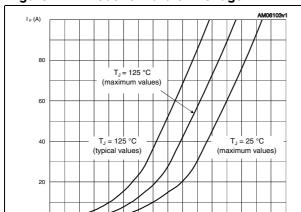


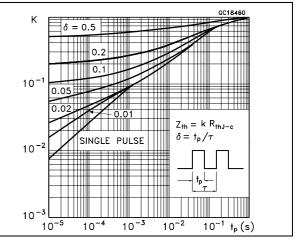
Figure 14. Diode forward on voltage



2.5

3.5 V_F (V)

Figure 15. Thermal impedance



Test circuits STGW45HF60WD

3 Test circuits

Figure 16. Test circuit for inductive load switching

Figure 17. Gate charge test circuit

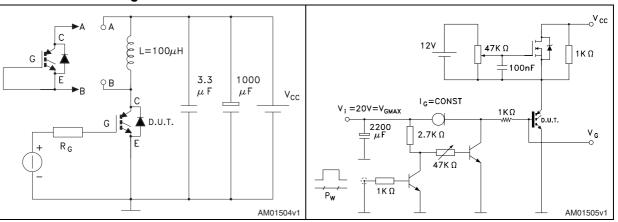
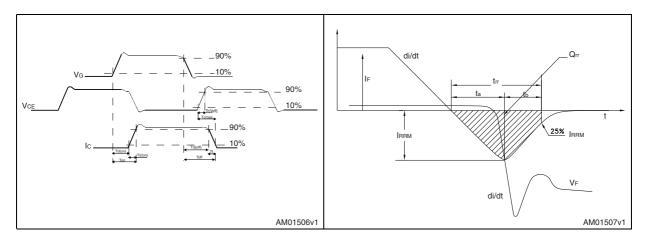


Figure 18. Switching waveform

Figure 19. Diode recovery time waveform



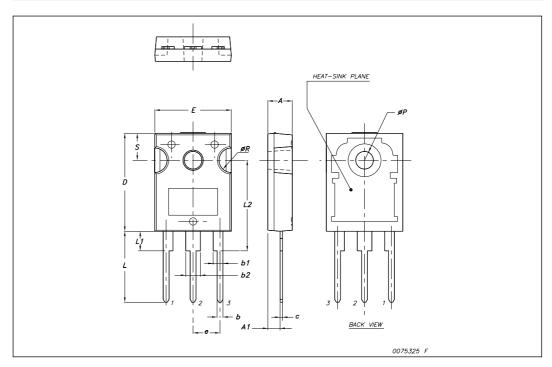
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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

TO-247 Mechanical data

Dim.		mm.	
Dilli.	Min.	Тур	Max.
Α	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
Е	15.45		15.75
е		5.45	
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
øΡ	3.55		3.65
øR	4.50		5.50
S		5.50	



STGW45HF60WD Revision history

5 Revision history

Table 10. Document revision history

Date	Revision	Changes
16-Apr-2009	1	Initial release.
04-Aug-2009	2	 Modified I_C value on Test conditions <i>Table 4</i> Modified R_G value on Test conditions <i>Table 7</i> and <i>Table 8</i>
28-Apr-2010	3	 Document status promoted from preliminary data to datasheet Inserted V_{CE(sat)} grouping A, B and C (see <i>Table 5</i>) Inserted dynamic parameters on <i>Table 5</i>, <i>Table 6</i> and <i>Table 7</i> Inserted <i>Section 2.1: Electrical characteristics (curves)</i>

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