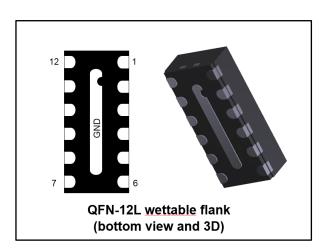
# life.augmented

## EMIF06-1005MX12Y

# Automotive 6-line IPAD™ low capacitance EMI filter and ESD protection in micro QFN package

Datasheet - production data



#### **Features**



- EMI symmetrical (I/O) low-pass filter
- High efficiency in EMI filtering: -34 dB at frequencies from 900 MHz to 1.8 GHz
- Very low PCB space consumption: 3 mm x 1.35 mm
- Very thin package: 0.8 mm max
- High efficiency in ESD suppression on input pins (IEC 61000-4-2 level 4)
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration and wafer level packaging
- Lead-free package

#### Complies with the following standards

- IEC 61000-4-2 level 4 input and output pins
  - 15 kV (air discharge)
  - 15 kV (contact discharge)
- ISO10605 input and output pins:
  - 11kV (air discharge)
  - 11kV (contact discharge)
- MIL STD 883G method 3015-7 Class 3B (all pins)

#### Applications

Where EMI filtering in ESD sensitive equipment is required:

- LCD and camera for cars
- **DSP** inputs
- V2V, V2I communication systems
- Digital tuner inputs

#### Description

This is a highly integrated 6-line device designed to suppress EMI/RFI noise in all systems exposed to electromagnetic interference.

This filter includes ESD protection circuitry, which prevents the application from damages when subjected to ESD surges up to 15 kV on the input pins.

Figure 1: Pin configuration (top view)

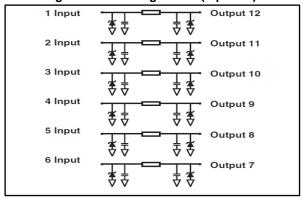
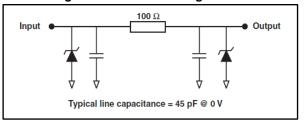


Figure 2: Basic cell configuration



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#### 1 Characteristics

Table 1: Absolute ratings (limiting values at T<sub>amb</sub> = 25 °C unless otherwise specified)

Symbol	Para	Value	Unit	
V <sub>pp</sub>	Peak pulse voltage	ESD IEC 61000-4-2 Contact discharge Air discharge ISO 10605 (330 pF - 330 Ω) Contact discharge Air discharge	15 15 11 11	kV
Tj	Operating junction temperature		150	°C
T <sub>stg</sub>	Storage temperature range		-55 to +150	°C
Top	Operating temperature range		-55 to +150	°C

Figure 3: Electrical characteristics (definitions)

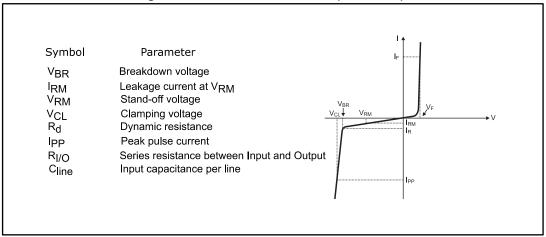
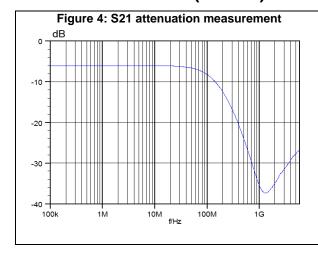


Table 2: Electrical characteristics (T<sub>amb</sub> = 25 °C)

Symbol	Test conditions		Тур.	Max.	Unit
V <sub>BR</sub>	I <sub>R</sub> = 1 mA	6	8	10	V
VF	I <sub>F</sub> = 10 mA	0.5	1.0	1.5	
I <sub>RM</sub>	V <sub>RM</sub> = 3 V per line			200	nA
R <sub>I/O</sub>	R <sub>I/O</sub> Tolerance ±10%		100	110	Ω
Cline	V <sub>LINE</sub> = 0 V dc, V <sub>OSC</sub> = 30 mV, f = 1 MHz		45	52	pF

EMIF06-1005MX12Y Characteristics

## 1.1 Characteristics (curves)



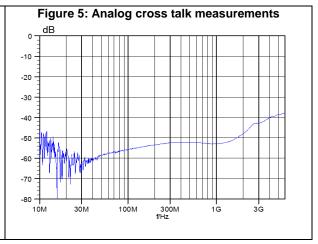
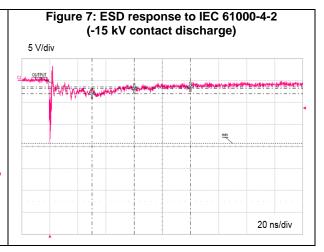


Figure 6: ESD response to IEC 61000-4-2 (+15 kV contact discharge)

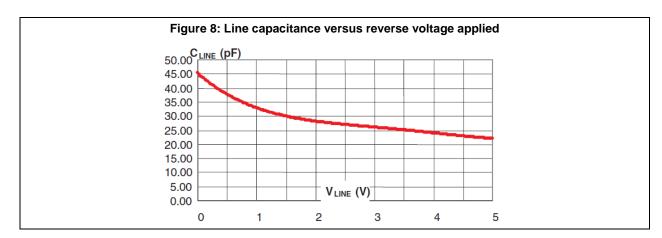
5 V/div

20 ns/div





Pulse applied on input and measured on output. Behavior is symmetrical.



Package information EMIF06-1005MX12Y

# 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

### 2.1 QFN-12L package information

FRONT SIDE VIEW

PIN #1

TOP VIEW

SIDE VIEW

PIN #1

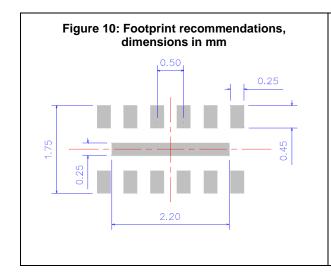
PIN #1 ID

P

Table 3: QFN-12L package mechanical data

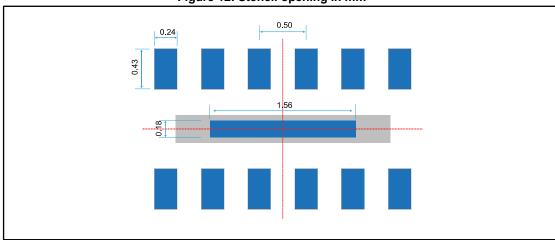
	Dimensions					
Ref.	Millimeters					
	Min.	Тур.	Max.			
А	0.70	0.75	0.80			
A1	0.00	0.02	0.05			
b	0.18	0.25	0.30			
D	2.95	3.00	3.05			
Е	1.30	1.35	1.40			
D2	2.10	2.20	2.30			
E2	0.20	0.25	0.30			
е		0.50				
K	0.20					
L	0.20	0.25	0.30			
Cd	0.1					
Cw	0.01		0.06			

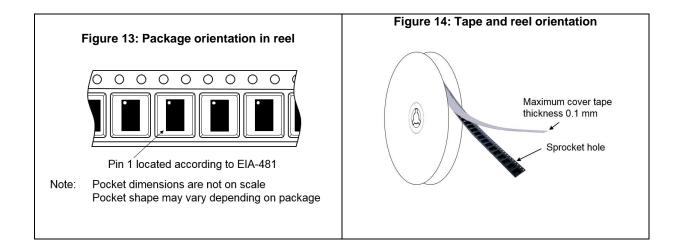
EMIF06-1005MX12Y Package information

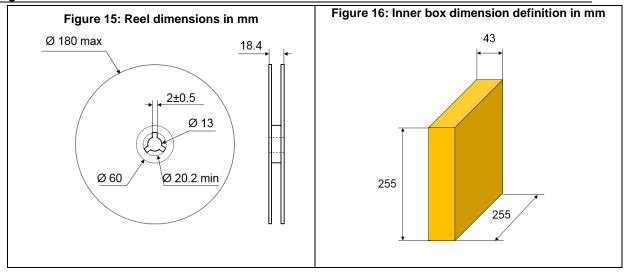


Dot: Pin 1
Identification
X = Marking (N7)
YWW = Data code
(Y = year
WW = week)

Figure 12: Stencil opening in mm







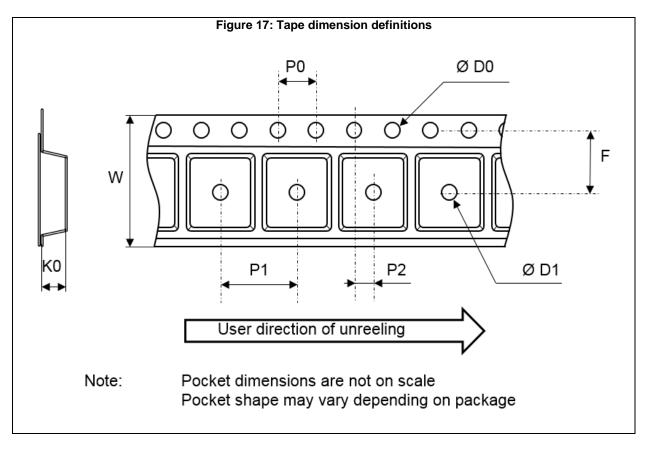
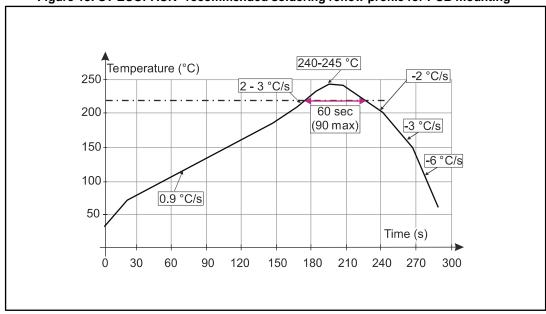


Table 4: Tape mechanical data

	Dimensions Millimeters				
Ref.					
	Min.	Тур.	Max.		
P1	3.9	4.0	4.1		
P0	3.9	4.0	4.1		
P2	1.95	2	2.05		
Ø D0	1.5				
Ø D1	1				
F	5.45	5.50	5.55		
K0	0.9	0.95	1.0		
W	11.9	12	12.3		

Figure 18: ST ECOPACK® recommended soldering reflow profile for PCB mounting



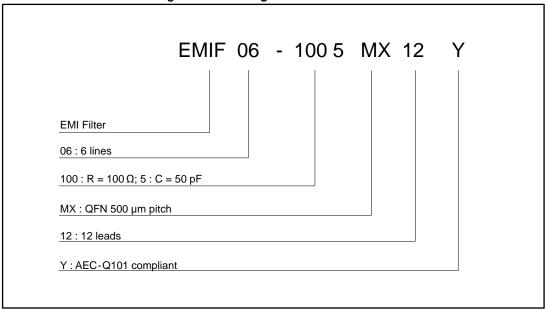


Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

Ordering information EMIF06-1005MX12Y

# 3 Ordering information

Figure 19: Ordering information scheme



**Table 5: Ordering information** 

Order code	Marking	Package	Weight	Base qty.	Delivery mode
EMIF06-1005MX12Y	N7	QFN-12L wettable flank	8.41 mg	3000	Tape and reel

## 4 Revision history

**Table 6: Document revision history** 

Date	Revision	Changes
03-Mar-2017	1	Initial release.
06-Apr-2017	2	Updated Table 1: "Absolute ratings (limiting values at Tamb = 25 °C unless otherwise specified)", Figure 4: "S21 attenuation measurement" and Figure 5: "Analog cross talk measurements".

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