



DDTC (R1 = R2 SERIES) UA

NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

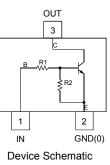
- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP** Capable (Note 4)

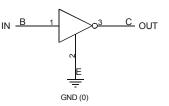
Part Number	R1, R2 (NOM)
DDTC123EUA	2.2ΚΩ
DDTC143EUA	4.7ΚΩ
DDTC114EUA	10ΚΩ
DDTC124EUA	22ΚΩ
DDTC144EUA	47ΚΩ
DDTC115EUA	100ΚΩ

- **Mechanical Data**
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (approximate)

SOT323

Top View





Equivalent Inverter Circuit

Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTC123EUA-7-F	AEC-Q101	N04	7	8	3,000
DDTC143EUA-7-F	AEC-Q101	N08	7	8	3,000
DDTC114EUA-7-F	AEC-Q101	N13	7	8	3,000
DDTC114EUAQ-7-F	Automotive	N13	7	8	3,000
DDTC124EUA-7-F	AEC-Q101	N17	7	8	3,000
DDTC124EUAQ-7-F	Automotive	N17	7	8	3,000
DDTC124EUAQ-13-F	Automotive	N17	13	8	10,000
DDTC144EUA-7-F	AEC-Q101	N20	7	8	3,000
DDTC144EUAQ-7-F	Automotive	N20	7	8	3,000
DDTC144EUAQ-13-F	Automotive	N20	13	8	10,000
DDTC115EUA-7-F	AEC-Q101	N24	7	8	3,000
DDTC115EUAQ-7-F	Automotive	N24	7	8	3,000

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/. 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

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		Nxx	ΥM	
		1177	⋝	
	Τ			•

Nxx = Product Type Marking Code (See Table Above) YM = Date Code Marking

Y = Year (ex: X = 2010)

M = Month (ex: 9 = September)

Date Code Key												_
Year	2010		2011	2012		2013	2014		2015	2016		2017
Code	Х		Y	Z		А	В		С	D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Supply Voltage <pine: (2)="" (3)="" to=""></pine:>		Vcc	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTC123EUA DDTC143EUA DDTC114EUA DDTC124EUA DDTC124EUA DDTC144EUA DDTC115EUA	V _{IN}	-10 to +12 -10 to +30 -10 to +40 -10 to +40 -10 to +40 -10 to +40	V
Output Current	DDTC123EUA DDTC143EUA DDTC114EUA DDTC124EUA DDTC124EUA DDTC144EUA DDTC115EUA	Io	100 100 50 30 100 20	mA
Output Current	All	I _{C(MAX)}	100	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	۵°

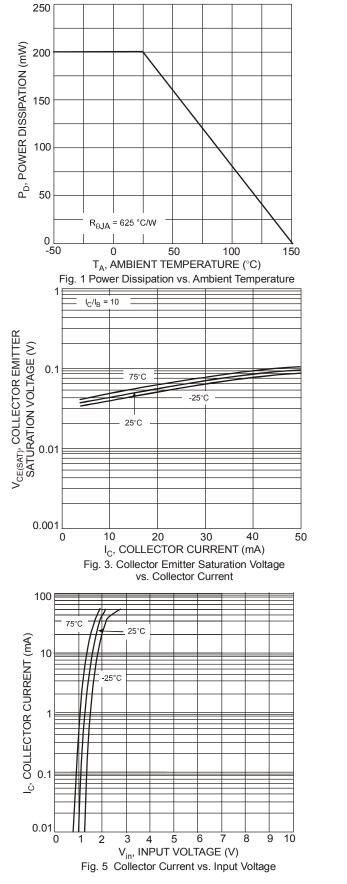
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

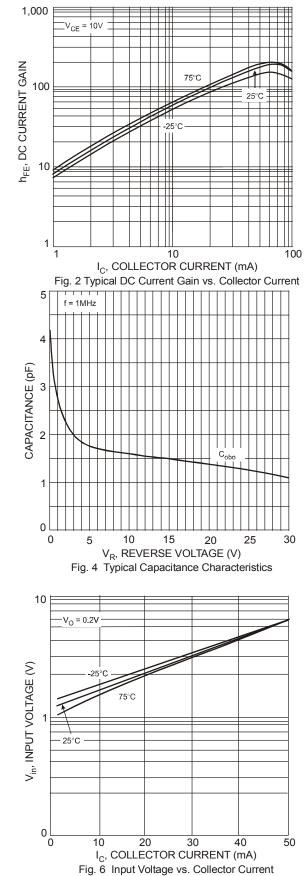
Characteristi	c	Symbol	Min	Тур	Max	Unit	Test Condition
		VI(OFF)	0.5	1.1	_	V	V _{CC} = 5V, I _O = 100 μA
Input Voltage		V _{I(ON)}	_	1.9	3	V	$V_0 = 0.3V$, $I_0 = 20mA$, DDTC123EUA $V_0 = 0.3V$, $I_0 = 20mA$, DDTC143EUA $V_0 = 0.3V$, $I_0 = 10mA$, DDTC114EUA $V_0 = 0.3V$, $I_0 = 5mA$, DDTC124EUA $V_0 = 0.3V$, $I_0 = 1mA$, DDTC115EUA
				1.4	2		V _O = 0.3V, I _O = 2mA, DDTC144EUA
Output Voltage		V _{O(ON)}	_	0.1	0.3	V	$\begin{split} & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC123EUA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC143EUA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC114EUA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC124EUA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC144EUA} \\ & _O/I_I = 5 \text{mA}/0.25 \text{mA}, \text{DDTC115EUA} \end{split}$
Input Current	DDTC123EUA DDTC143EUA DDTC114EUA DDTC124EUA DDTC124EUA DDTC144EUA DDTC115EUA	h	_	_	3.8 1.8 0.88 0.36 0.18 0.15	mA	V ₁ = 5V
Output Current		I _{O(OFF)}	_	_	0.5	μA	$V_{CC} = 50V, V_1 = 0V$
DC Current Gain	DDTC123EUA DDTC143EUA DDTC114EUA DDTC124EUA DDTC124EUA DDTC144EUAQ DDTC115EUA	Gl	20 20 30 56 68 80 82				$ \begin{array}{l} V_{O} = 5V, \ I_{O} = 20mA \\ V_{O} = 5V, \ I_{O} = 10mA \\ V_{O} = 5V, \ I_{O} = 5mA \\ \end{array} $
Input Resistor (R1) Tolerance		ΔR_1	-30		+30	%	
Resistance Ratio		R_2/R_1	0.8	1	1.2		
Gain-Bandwidth Product (Note 7	')	fT	_	250	—	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout. 7. Transistor - For Reference Only.



Typical Curves – DDTC143EUA (@T_A = +25°C, unless otherwise specified.)



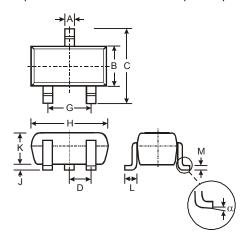


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Package Outline Dimensions

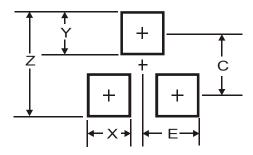
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT323							
Dim	Min	Max	Тур				
Α	0.25	0.40	0.30				
В	1.15	1.35	1.30				
С	2.00	2.20	2.10				
D	-	-	0.65				
G	1.20	1.40	1.30				
Н	1.80	2.20	2.15				
J	0.0	0.10	0.05				
κ	0.90	1.00	1.00				
L	0.25	0.40	0.30				
Μ	0.10	0.18	0.11				
α	0°	8°	-				
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Y	0.9
С	1.9
E	1.0



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