

Description

The FMX-4202S is a fast recovery diode of 200 V / 20 A. The maximum $t_{\rm rr}$ of 30 ns is realized by optimizing a life-time control.

Features

•	V _{RM} 20	0 V
•	$I_{F(AV)}$ 2	0 A
•	V _F 0.9	8 V
•	t_{rr1} 30) ns

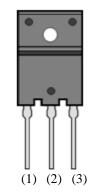
Bare Leads: Pb-free (RoHS Compliant)Flammability: Equivalent to UL94V-0

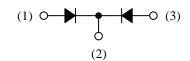
Applications

- Secondary-side Rectifier Diode (Flyback Converter, LLC Converter, etc.)
- Freewheel Diode (Offline Buck Converter, Offline Buck-boost Converter, etc.)

Package

TO3PF-3L





- (1) Anode
- (2) Cathode
- (3) Anode

Not to scale

FMX-4202S

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Rating	Unit
Nonrepetitive Peak Reverse Voltage ⁽¹⁾	V_{RSM}		200	V
Repetitive Peak Reverse Voltage ⁽¹⁾	V_{RM}		200	V
Average Forward Current	$I_{F(AV)}$	See Figure 1 and Figure 2	20	A
Surge Forward Current ⁽¹⁾	I _{FSM}	Half cycle sine wave, positive side, 10 ms, 1 shot	150	A
I ² t Limiting Value ⁽¹⁾	I^2t	$1 \text{ ms} \le t \le 10 \text{ ms}$	112.5	A^2s
Junction Temperature	$T_{\rm J}$		-40 to 150	°C
Storage Temperature	T_{STG}		-40 to 150	°C

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Earned Valters Dury(1)	V_{F}	$T_J = 25 ^{\circ}\text{C}, I_F = 10 \text{A}$	_	_	0.98	V
Forward Voltage Drop ⁽¹⁾		$T_J = 100 ^{\circ}\text{C}, I_F = 10 \text{A}$	_	0.78	_	V
Reverse Leakage Current ⁽¹⁾	I_R	$V_R = V_{RM}$	_		200	μΑ
Reverse Leakage Current under High Temperature ⁽¹⁾	$H \cdot I_R$	$V_R = V_{RM}, T_J = 150 ^{\circ}C$	_		50	mA
Payana Pagayan Time(1)	t _{rr1}	$I_F = I_{RP} = 500 \text{ mA},$ 90% recovery point, $T_J = 25 \text{ °C}$	_	_	30	ns
Reverse Recovery Time ⁽¹⁾	t _{rr2}	$I_F = 500$ mA, $I_{RP} = 1$ A, 75% recovery point, $T_J = 25$ °C	_	_	25	ns
Thermal Resistance (2)	R _{th(J-C)}		_	_	2.0	°C/W

Mechanical Characteristics

Parameter	Conditions	Min.	Typ.	Max.	Unit
Heatsink Mounting Screw Torque		0.686		0.882	N·m

⁽¹⁾ Specifies a value per chip; the FMX-4202S consists of two chips.

⁽²⁾ Refers to thermal resistance between junction and the case. The case temperature is measured at the backside near the screw hole.

Rating and Characteristic Curves

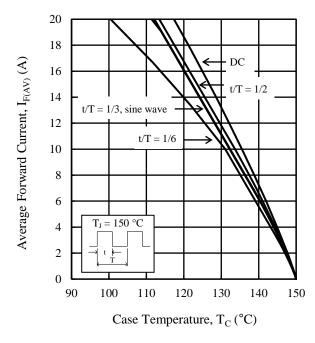


Figure 1. Typical Characteristics: $I_{F(AV)}$ vs. T_{C} $(V_{R}=0\ V)$

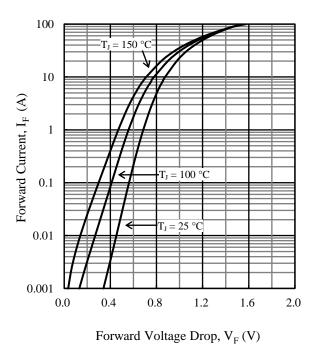


Figure 3. Typical Characteristics: I_F vs. V_F

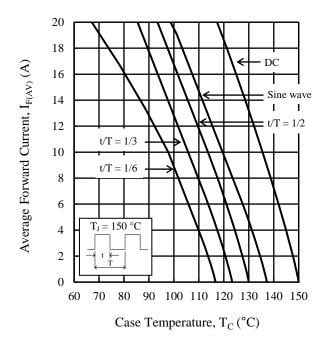


Figure 2. Typical Characteristics: $I_{F(AV)}$ vs. T_C ($V_R = 200 \ V$)

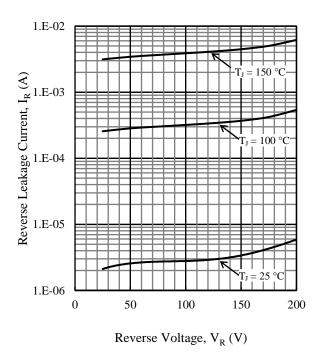
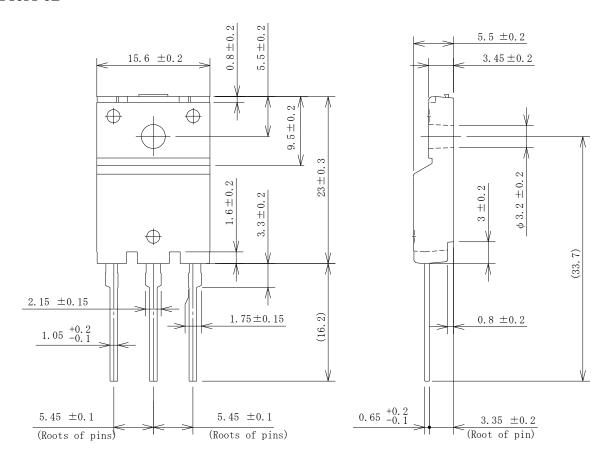
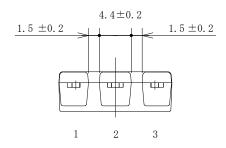


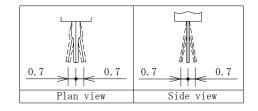
Figure 4. Typical Characteristics: I_R vs. V_R

Physical Dimensions

• TO3PF-3L







NOTES:

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time within the following limits:

Flow: 260 ± 5 °C / 10 ± 1 s, 2 times

Soldering Iron: 380 \pm 10 $^{\circ}C$ / 3.5 \pm 0.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

Marking Diagram

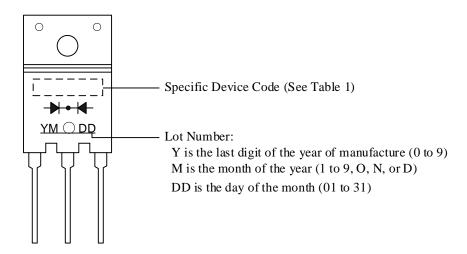


Table 1. Specific Device Code

Specific Device Code	Part Number
X4202S	FMX-4202S

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