



CPC1727 Single-Pole, Normally Open ISOPLUS™-264 DC Power Relay

Characteristics

Parameter	Rating	Units
Blocking Voltage	250	V _P
Load Current, T _A =25°C:		
With 5°C/W Heat Sink	8.6	ADC
No Heat Sink	3.4	ADC
On-Resistance (max)	0.09	Ω
$R_{ heta JC}$	0.3	°C/W

Features

- 8.6A_{DC} Load Current with 5°C/W Heat Sink
- Low 0.09Ω On-Resistance
- 250V_P Blocking Voltage
- 2500V_{rms} Input/Output Isolation
- Low Thermal Resistance (0.3 °C/W)
- Electrically Non-Conductive Thermal Pad for Heat Sink Applications
- Low Drive Power Requirements
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- · Machine Insertable, Wave Solderable

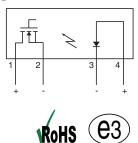
Applications

- Industrial Controls / Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Transportation Equipment
- Aerospace/Defense

Approvals

- UL 508 Certified Component: File E69938
- CSA Certified Component: Certificate 1172007

Pin Configuration



Description

IXYS Integrated Circuits Division and IXYS have combined to bring OptoMOS® technology, reliability and compact size to a new family of high-power Solid State Relays.

As part of this family, the CPC1727 single-pole normally open (1-Form-A) DC Solid State Power Relay employs optically coupled MOSFET technology to provide $2500V_{rms}$ of input to output isolation.

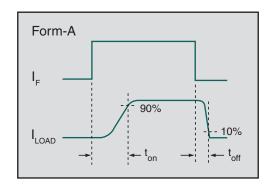
The optically coupled outputs, that use patented OptoMOS architecture, are controlled by a highly efficient GaAlAs infrared LED. The combination of low on-resistance and high load current handling capability makes this relay suitable for a variety of high performance DC switching applications.

The unique ISOPLUS-264 package pioneered by IXYS enables Solid State Relays to achieve the highest load current and power ratings. This package features a unique IXYS process where the silicon chips are soft soldered onto the Direct Copper Bond (DCB) substrate instead of the traditional copper leadframe. The DCB ceramic, the same substrate used in high power modules, not only provides 2500V_{rms} isolation, but also very low thermal resistance (0.3 °C/W).

Ordering Information

Part	Description	
CPC1727J	ISOPLUS-264 Package (25 per tube)	

Switching Characteristics





1 Specifications

1.1 Absolute Maximum Ratings @ 25°C

Symbol	Ratings	Units
Blocking Voltage	250	V _P
Reverse Input Voltage	5	V
Input Control Current	100	mA
Peak (10ms)	1	А
Input Power Dissipation	150	mW
Isolation Voltage, Input to Output	2500	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

1.2 Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Output Characteristics						
Load Current ¹						
Peak	t≤10ms				30	A_P
Continuous	No Heat Sink	ΙL			3.4	
Continuous	T _C =25°C		-	-	20	A_{DC}
Continuous	T _C =99°C	I _{L(99)}			4.2	
On-Resistance ²	I _F =10mA, I _L =1A	R _{ON}	-	0.0675	0.09	Ω
Off-State Leakage Current	V _L =250V _P	I _{LEAK}	-	-	1	μΑ
Switching Speeds						
Turn-On	I _F =20mA, V _I =10V	t _{on}	-	5.5	20	
Turn-Off	1F-20111A, V[-10V	t _{off}	-	0.125	5	ms
Output Capacitance	V _L =25V, f=1MHz	C _{out}	-	700	-	pF
Input Characteristics		*			*	
Input Control Current to Activate 3	I _L =1A	I _F	-	-	10	mA
Input Control Current to Deactivate	-	I _F	0.6	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μА
Input/Output Characteristics		П	1		1	
Input-to-Output Capacitance	-	C _{I/O}		1	-	pF

¹ Higher load currents possible with proper heat sinking.

² Measurement taken within 1 second of on-time.

³ For applications requiring high temperature operation (> 60°C) a LED drive current of 20mA is recommended.



Thermal Characteristics

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Thermal Resistance (Junction to Case)	-	$R_{ hetaJC}$	-	-	0.3	°C/W
Thermal Resistance (Junction to Ambient)	Free Air	$R_{ heta JA}$	-	33	-	°C/W
Junction Temperature (Operating)	-	TJ	-40	-	100	°C

2.1 Thermal Management

Device high current characterization was performed using Kunze heat sink KU 1-159, phase change thermal interface material KU-ALC 5, and transistor clip KU 4-499/1. This combination provided an approximate junction-to-ambient thermal resistance of 12.5°C/W.

2.2 Heat Sink Calculation

Higher load currents are possible by using lower thermal resistance heat sink combinations.

Heat Sink Rating

$$R_{\theta CA} = \frac{(T_{J} - T_{A}) I_{L(99)}^{2}}{I_{L}^{2} \cdot P_{D(99)}} - R_{\theta JC}$$

 $T_J = Junction Temperature (°C), T_J \le 100°C$ *

 T_{Δ} = Ambient Temperature (°C)

 $I_{L(99)}$ = Load Current with Case Temperature @ 99°C (A_{DC}) I_{L} = Desired Operating Load Current (A_{DC}), $I_{L} \le I_{L(MAX)}$ = Thermal Resistance, Junction to Case (°C/W) = 0.3°C/W

 $R_{\theta CA} = Thermal Resistance of Heat Sink & Thermal Interface Material , Case to Ambient (°C/W)$

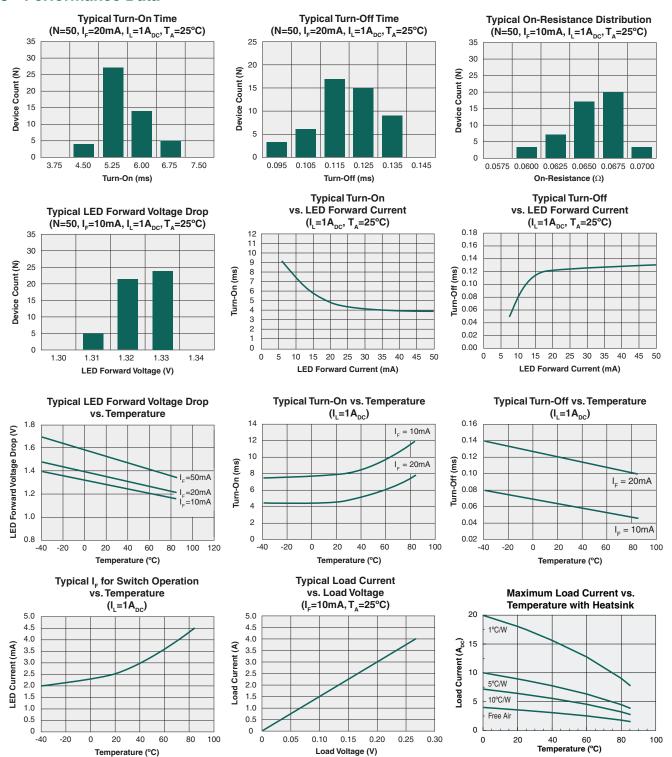
 $P_{D(99)}^{---}$ = Maximum power dissipation with case temperature held at 99°C = 3.33W

3 R04 www.ixysic.com

^{*} Elevated junction temperature reduces semiconductor lifetime.



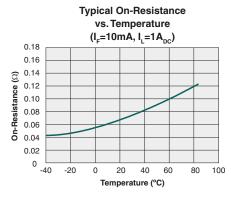
3 Performance Data

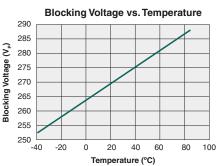


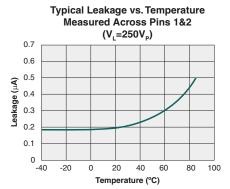
Unless otherwise specified, all performance data was acquired without the use of a heat sink.

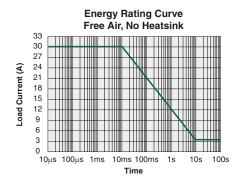
The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.











Unless otherwise specified, all performance data was acquired without the use of a heat sink.

The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



4 Manufacturing Information

4.1 Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1727J	MSL 1

4.2 ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

4.3 Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
CPC1727J	245°C for 30 seconds

4.4 Board Wash

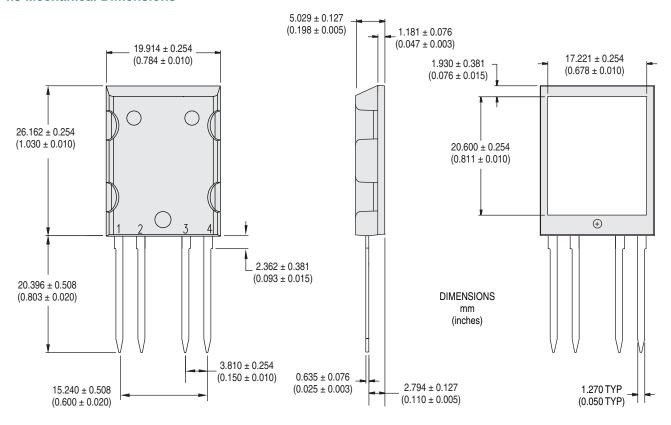
IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake may be necessary if a wash is used after solder reflow processes. Chlorine-based or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.







4.5 Mechanical Dimensions



NOTE: Back-side heat sink meets $2500V_{rms}$ isolation to the pins.

For additional information please visit our website at: www.ixysic.com

IXYS Integrated Circuits Division makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in IXYS Integrated Circuits Division's Standard Terms and Conditions of Sale, IXYS Integrated Circuits Division assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of IXYS Integrated Circuits Division's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. IXYS Integrated Circuits Division reserves the right to discontinue or make changes to its products at any time without notice.

Specification: DS-CPC1727-R04 ©Copyright 2012, IXYS Integrated Circuits Division All rights reserved. Printed in USA. 12/17/2012

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

IXYS: CPC1727J