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February 2015



KSA916 PNP Epitaxial Silicon Transistor

Features

- Audio Power Amplifier
- Driver Stage Amplifier
- Complement to KSC2316



1. Emitter 2. Collector 3. Base

Ordering Information

Part Number	Top Mark	Package	Packing Method	
KSA916YTA	A916	TO-92 3L	Ammo	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-120	V
V _{CEO}	Collector-Emitter Voltage	-120	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ι _C	Collector Current	-800	mA
T _J Junction Temperature		150	°C
T _{STG}	Storage Temperature	-55 to +150	°C

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
Power Dissipation, by $R_{\theta JA}$		900	mW
П	Power Dissipation, by R _{θJC}	3	W
P _D Derate Above 25°C, by R _{θJA}		7.2	mW/°C
	Derate Above 25°C, by R _{0JC}	24	mW/°C
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	130	°C/W
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case	41	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

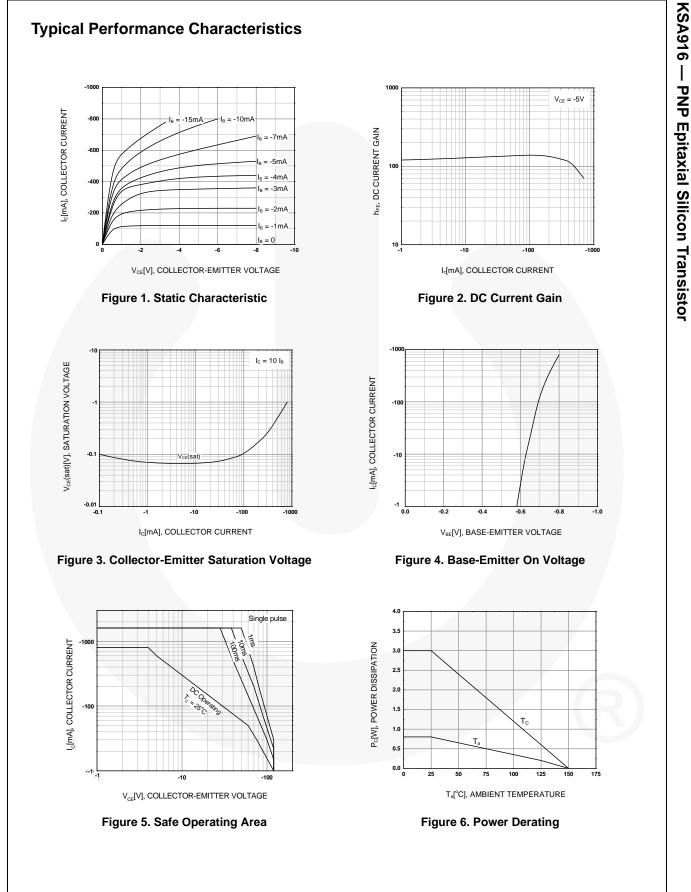
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

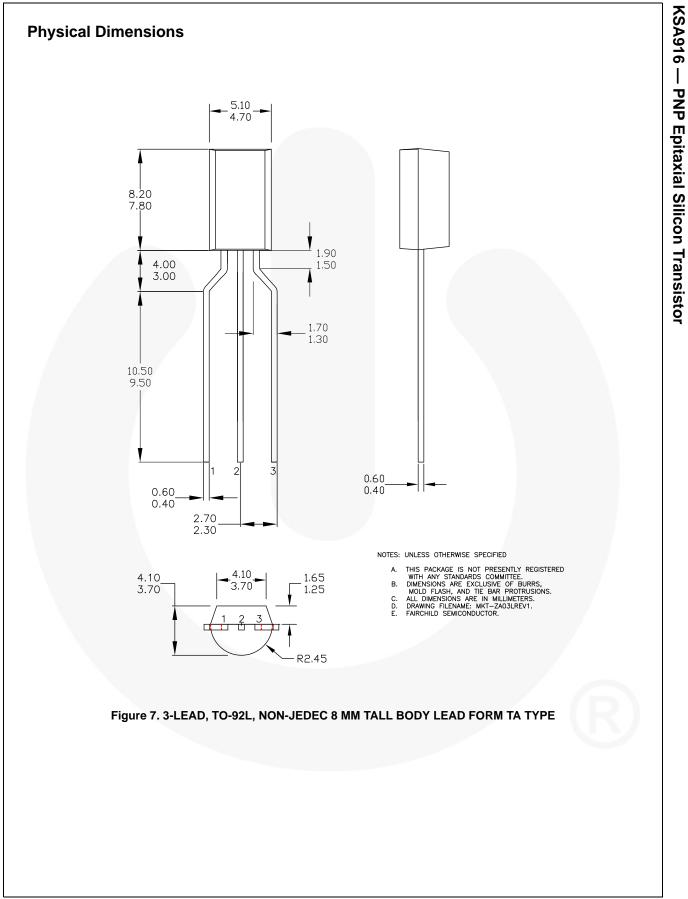
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = -1 {\rm mA}, I_{\rm E} = 0$	-120			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA, I _B = 0	-120			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = -1 \text{ mA}, I_{C} = 0$	-5			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -120 \text{ V}, \text{ I}_{E} = 0$			-0.1	μA
h _{FE1}	DC Current Gain	$V_{CE} = -5 V, I_{C} = -10 mA$	60			
h _{FE2}	DC Current Gain	$V_{CE} = -5 V, I_{C} = -100 mA$	80		240	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{C} = -500 \text{ mA}, I_{B} = -50 \text{ mA}$			-1	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -5V, I_{C} = -100 \text{ mA}$		120		MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1 MHz			40	pF

h_{FE} Classification

Classification	0	Y
h _{FE2}	80 ~ 160	120 ~ 240



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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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