

Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

FEATURES AND BENEFITS

- AEC-Q100 automotive qualified
- Differential Hall sensing rejects common-mode fields
- Patented integrated digital temperature compensation circuitry allows for near closed loop accuracy over temperature in an open loop sensor
- UL60950-1 (ed. 2) certification

 □ Dielectric Strength Voltage = 4.8 kV_{RMS}
 □ Basic Isolation Working Voltage = 1097 V_{RMS}
 □ Reinforced Isolation Working Voltage = 565 V_{RMS}
- Industry-leading noise performance with greatly improved bandwidth through proprietary amplifier and filter design techniques
- Filter pin allows user to filter output for improved resolution at lower bandwidth
- $0.85 \text{ m}\Omega$ primary conductor resistance for low power loss and high inrush current withstand capability
- Low-profile SOIC16 package suitable for spaceconstrained applications
- 4.5 to 5.5 V single supply operation
- · Output voltage proportional to AC or DC current
- Factory-trimmed sensitivity and quiescent output voltage for improved accuracy

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PACKAGE: 16-pin SOICW (suffix MA)



Not to scale

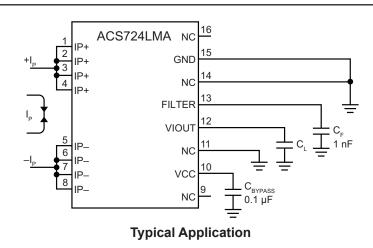
DESCRIPTION

The Allegro[™] ACS724LMA current sensor IC is an economical and precise solution for AC or DC current sensing in automotive, industrial, commercial, and communication systems. The small package is ideal for space-constrained applications while also saving costs due to reduced board area. Typical applications include electric vehicles on-board chargers, motor control, load detection and management, switched-mode power supplies, and overcurrent fault protection.

The device consists of a precise, low-offset, linear Hall sensor circuit with a copper conduction path located near the surface of the die. Applied current flowing through this copper conduction path generates a magnetic field which is sensed by the integrated Hall IC and converted into a proportional voltage. The current is sensed differentially in order to reject common-mode fields, improving accuracy in magnetically noisy environments. The inherent device accuracy is optimized through the close proximity of the magnetic field to the Hall transducer. A precise, proportional voltage is provided by the low-offset, chopperstabilized BiCMOS Hall IC, which includes Allegro's patented digital temperature compensation, resulting in extremely accurate performance over temperature. The output of the device has a positive slope when an increasing current flows through the primary copper conduction path (from pins 1 through 4, to pins 5 through 8), which is the path used for current sensing. The internal resistance of this conductive path is $0.85 \text{ m}\Omega$ typical, providing low power loss.

The terminals of the conductive path are electrically isolated from the sensor leads (pins 9 through 16). This allows the ACS724LMA current sensor IC to be used in high-side current sense applications without the use of high-side differential amplifiers or other costly isolation techniques.

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The ACS724LMA outputs an analog signal, V_{IOUT} , that changes proportionally with the bidirectional AC or DC primary sensed current, I_p , within the specified measurement range.

The FILTER pin can be used to decrease the bandwidth in order to optimize the noise performance.

ACS724LMA-DS, Rev. 14 MCO-0000322

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FEATURES AND BENEFITS (continued)

- Chopper stabilization results in extremely stable quiescent output voltage
- Nearly zero magnetic hysteresis
- Ratiometric output from supply voltage

DESCRIPTION (continued)

The ACS724LMA is provided in a low-profile surface-mount SOIC16 package. The leadframe is plated with 100% matte tin, which is compatible with standard lead (Pb) free printed circuit board assembly processes. Internally, the device is Pb-free. The device is fully calibrated prior to shipment from the factory.

SELECTION GUIDE

Part Number	I _{PR} (A)	Sens(Typ) at V _{CC} = 5 V (mV/A)	T _A (°C)	Packing ^[1]
ACS724LMATR-20AB-T	±20	100		
ACS724LMATR-30AB-T	±30	66		
ACS724LMATR-30AU-T	30	132		
ACS724LMATR-50AB-T	±50	40	-40 to 150	Tape and Reel, 1000 pieces per reel
ACS724LMATR-50AU-T	50	80		
ACS724LMATR-65AB-T	±65	30.75]	
ACS724LMATR-100AB-T	±100	20		

^[1] Contact Allegro for additional packing options.





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ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	V _{CC}		6	V
Reverse Supply Voltage	V _{RCC}		-0.1	V
Output Voltage	V _{IOUT}		V _{CC} + 0.5	V
Reverse Output Voltage	V _{RIOUT}		-0.1	V
Operating Ambient Temperature	T _A	Range L	-40 to 150	°C
Junction Temperature	T _J (max)		165	°C
Storage Temperature	T _{stg}		–65 to 165	°C

ESD RATINGS

Characteristic	Symbol	Test Conditions	Value	Unit
Human Body Model	V _{HBM}	Per AEC-Q100	±2	kV
Charged Device Model	V _{CDM}	Per AEC-Q100	±1	kV

ISOLATION CHARACTERISTICS

Characteristic	Symbol	Notes	Rating	Unit
Dielectric Surge Strength Test Voltage	V _{SURGE}	Tested ± 5 pulses at 2/minute in compliance to IEC 61000-4-5 1.2 µs (rise) / 50 µs (width).	10000	V
Dielectric Strength Test Voltage	V _{ISO}	Agency type-tested for 60 seconds per UL 60950-1 (edition 2). Production tested at 3000 V_{RMS} for 1 second, in accordance with UL 60950-1 (edition 2).	4800	V _{RMS}
Working Voltage for Basic Isolation	N	Maximum approved working voltage for basic (single) isolation	1550	V _{PK} or VDC
	V _{WVBI}	according to UL 60950-1 (edition 2).	1097	V _{RMS}
Working Voltage for Painforced Indiation		Maximum approved working voltage for reinforced isolation	800	V _{PK} or VDC
Working Voltage for Reinforced Isolation	V _{WVRI}	according to UL 60950-1 (edition 2).	565	V _{RMS}
Clearance	D _{cl}	Minimum distance through air from IP leads to signal leads.	7.5	mm
Creepage	D _{cr}	Minimum distance along package body from IP leads to signal leads	8.2	mm
Distance Through Insulation	DTI	Minimum internal distance through insulation	90	μm
Comparative Tracking Index	СТІ	Material Group II	400 to 599	V

THERMAL CHARACTERISTICS^[1]

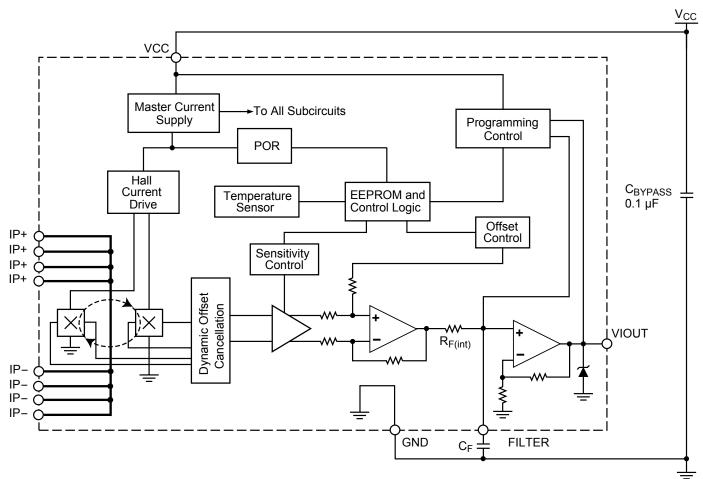
Characteristic Syr		Test Conditions	Value	Unit
Junction-to-Ambient Thermal Resistance	$R_{ extsf{ heta}JA}$	Mounted on the Allegro ASEK724/5 MA evaluation board. Performance values include the power consumed by the PCB. ^[2]	23	°C/W
Junction-to-Lead Thermal Resistance	$R_{ extsf{ heta}JL}$	Mounted on the Allegro ASEK724/5 MA evaluation board. ^[2]	5	°C/W

^[1] Refer to the die temperature curves versus DC current plot (page 22). Additional thermal information is available on the Allegro website.

[2] The Allegro evaluation board has 1500 mm² of 4 oz. copper on each side, connected to pins 1 through 4 and pins 5 through 8, with thermal vias connecting the layers. Performance values include the power consumed by the PCB. Further information about board design and thermal performance also can be found in the Applications Information section of this datasheet.



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Functional Block Diagram

	Terminal List Table											
IP+ 1	16 NC	Number	Name	Description								
IP+ 2	15 GND	1, 2, 3, 4	IP+	Terminals for current being sensed; fused internally								
IP+ 3	14 NC	5, 6, 7, 8	IP-	Terminals for current being sensed; fused internally								
IP+ 4 IP- 5	13 FILTER 12 VIOUT	9, 16	NC	No internal connection; recommended to be left unconnected in order to maintain high creepage								
IP- 6	11 NC	10	VCC	Device power supply terminal								
IP- 7 IP- 8	10 VCC 9 NC	11, 14	NC	No internal connection; recommened to connect to GND for the best ESD performance								
		12	VIOUT	Analog output signal								
		13	FILTER	Terminal for external capacitor that sets bandwidth								
Pinot	Pinout Diagram		GND	Signal ground terminal								



Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

COMMON ELECTRICAL CHARACTERISTICS ^[1]: Valid through the full range of $T_A = -40^{\circ}C$ to 150°C and $V_{CC} = 5 V$, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{cc}		4.5	5	5.5	V
Supply Current	I _{CC}	V_{CC} within V_{CC} (min) and V_{CC} (max)	-	10	14	mA
Output Capacitive Load	CL	VIOUT to GND	-	_	10	nF
Output Resistive Load	RL	VIOUT to GND	4.7	_	_	kΩ
Primary Conductor Resistance	R _{IP}	T _A = 25°C	-	0.85	_	mΩ
Primary Conductor Inductance	L _{IP}	T _A = 25°C	-	4	_	nH
Internal Filter Resistance [2]	R _{F(INT)}		-	1.7	_	kΩ
Common Mode Field Rejection Ratio	CMFRR	Uniform external magnetic field	-	40	_	dB
Primary Hall Coupling Factor	G1	$T_A = 25^{\circ}C$	-	4.5	_	G/A
Secondary Hall Coupling Factor	G2	$T_A = 25^{\circ}C$	-	0.5	_	G/A
Hall Plate Sensitivity Matching	Sens _{MATCH}	T _A = 25°C	-	±1	_	%
Rise Time	t _r	$T_{A} = 25^{\circ}C, C_{L} = 1 \text{ nF}$	-	3	_	μs
Propagation Delay	t _{pd}	$T_{A} = 25^{\circ}C, C_{L} = 1 \text{ nF}$	-	2	_	μs
Response Time	t _{RESPONSE}	$T_{A} = 25^{\circ}C, C_{L} = 1 \text{ nF}$	-	4	_	μs
Output Slew Rate	SR	$T_{A} = 25^{\circ}C, C_{L} = 1 \text{ nF}$	-	0.53	_	V/µs
Internal Bandwidth	BW	Small signal –3 dB, C _L = 1 nF	-	120	-	kHz
Noise Density	I _{ND}	Input-referenced noise density; $T_A = 25^{\circ}C$, $C_L = 1 \text{ nF}$	_	450	_	µA _{RMS} / √Hz
Noise	I _N	Input-referenced noise; $C_F = 4.7 \text{ nF}$, $C_L = 1 \text{ nF}$, BW = 18 kHz, $T_A = 25^{\circ}\text{C}$	_	60	_	mA _{RMS}
Nonlinearity	E _{LIN}	Through full range of I _P	-1	_	1	%
Sensitivity Ratiometry Coefficient	SENS_RAT_ COEF	V_{CC} = 4.5 to 5.5 V, T_{A} = 25°C	-	1.3	-	-
Zero-Current Output Ratiometry Coefficient	QVO_RAT_ COEF	V_{CC} = 4.5 to 5.5 V, T_{A} = 25°C	_	1	-	-
Caturatian Maltana ^[3]	V _{OH}	$R_{L} = 4.7 \text{ k}\Omega, T_{A} = 25^{\circ}\text{C}$	V _{CC} – 0.5	_	_	V
Saturation Voltage [3]	V _{OL}	$R_{L} = 4.7 \text{ k}\Omega, T_{A} = 25^{\circ}\text{C}$	_	_	0.5	V
Power-On Time	t _{PO}	T _A = 25°C	_	80	_	μs
Shorted Output to Ground Current	I _{SC(GND)}	T _A = 25°C	_	3.3	_	mA
Shorted Output to V _{CC} Current	I _{SC(VCC)}	T _A = 25°C		45	_	mA

^[1] Device may be operated at higher primary current levels, I_P, ambient temperatures, T_A, and internal leadframe temperatures, provided the Maximum Junction Temperature, T_J(max), is not exceeded.

 $^{[2]}\mathsf{R}_{\mathsf{F}(\mathsf{INT})}$ forms an RC circuit via the FILTER pin.

(3) The sensor IC will continue to respond to current beyond the range of I_P until the high or low saturation voltage; however, the nonlinearity in this region will be worse than through the rest of the measurement range.



Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

xLMATR-20AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current Sensing Range	I _{PR}		-20	-	20	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	100	_	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	_	V _{CC} × 0.5	-	V
ACCURACY PERFORMANC	E					
Total Output Error ^[2] E _{TOT}	F	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±0.8	2.5	%
	⊏TOT	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±2.7	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±0.7	2	%
Sensitivity Error		$T_A = -40^{\circ}C$ to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.6	5.5	%
Voltage Offset Error		I _P = 0 A, T _A = 25°C to 150°C	-15	±7	15	mV
Vollage Oliset Elloi	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±15	30	mV
LIFETIME DRIFT CHARACT	ERISTICS [4	1				
Sensitivity Error Including	F	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-3	±1	3	%
Lifetime Drift	E _{sens_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including	F	T _A = 25°C to 150°C	-3.5	±1	3.5	%
Lifetime Drift	E _{tot_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime	F	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-20	-	20	mV
Drift	E _{Off_Drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.

^[3] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares. See Application Information section.

^[4] Based on characterization data obtained during AEC-Q100 stress testing.



Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

xLMATR-30AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current Sensing Range	I _{PR}		-30	-	30	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	66	_	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	-	V
ACCURACY PERFORMANC	E					
Total Output Error ^[2] E _{TOT}	F	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±0.8	2.5	%
	FTOT	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±2.7	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±0.7	2	%
Sensitivity Error		$T_A = -40^{\circ}C$ to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.6	5.5	%
Voltage Offset Error		I _P = 0 A, T _A = 25°C to 150°C	-15	±7	15	mV
Vollage Oliset Elloi	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±15	30	mV
LIFETIME DRIFT CHARACT	ERISTICS [4	9				
Sensitivity Error Including	F	$T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-3	±1	3	%
Lifetime Drift	E _{sens_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including	F	T _A = 25°C to 150°C	-3.5	±1	3.5	%
Lifetime Drift	E _{tot_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime	F	$T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-20	-	20	mV
Drift	E _{Off_Drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.

^[3] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares. See Application Information section.

^[4] Based on characterization data obtained during AEC-Q100 stress testing.



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xLMATR-30AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40^{\circ}$ C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE			·	·		
Current Sensing Range	I _{PR}		0	-	30	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	132	-	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Unidirectional; I _P = 0 A	-	V _{CC} × 0.1	_	V
ACCURACY PERFORMANC	E			·		
Total Output Error ^[2]	F	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±0.7	2.5	%
Total Output Error ^[2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±2.5	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±0.7	2	%
		$T_A = -40^{\circ}C$ to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.5	5.5	%
Voltage Offect Error		$I_{P} = 0 \text{ A}, T_{A} = 25^{\circ}\text{C} \text{ to } 150^{\circ}\text{C}$	-15	±7	15	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±20	30	mV
LIFETIME DRIFT CHARACT	ERISTICS [4	1				
Sensitivity Error Including	-	T _A = 25°C to 150°C	-3	±1	3	%
Lifetime Drift	E _{sens_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including	F	T _A = 25°C to 150°C	-3.5	±1	3.5	%
Lifetime Drift	E _{tot_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime		$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-20	-	20	mV
Drift	E _{Off_Drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.



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xLMATR-50AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current Sensing Range	I _{PR}		-50	-	50	A
Sensitivity	Sens	I _{PR(min)} < I _P < I _{PR(max)}	_	40	_	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	-	v
ACCURACY PERFORMANC	E			· · · · · · · · · · · · · · · · · · ·		
Total Output Error ^[2]	F	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1	2.5	%
	E _{TOT}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sonoitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±1	2	%
Sensitivity Error		$T_A = -40^{\circ}$ C to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.8	5.5	%
Voltage Offset Error		I _P = 0 A, T _A = 25°C to 150°C	-15	±5	15	mV
Vollage Oliset Elloi	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±20	30	mV
LIFETIME DRIFT CHARACT	ERISTICS ^{[4}	1				
Sensitivity Error Including	E	$T_A = 25^{\circ}C$ to $150^{\circ}C$	-3	±1	3	%
Lifetime Drift	E _{sens_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including	F	T _A = 25°C to 150°C	-3.5	±1	3.5	%
Lifetime Drift	E _{tot_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime	F	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-20	-	20	mV
Drift	E _{Off_Drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.

^[3] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares. See Application Information section.

^[4] Based on characterization data obtained during AEC-Q100 stress testing.



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xLMATR-50AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40^{\circ}$ C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current Sensing Range	I _{PR}		0	-	50	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	_	80	_	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Unidirectional, I _P = 0 A	-	V _{CC} × 0.1	-	V
ACCURACY PERFORMANC	E			· · · · · ·		
Total Output Error ^[2] E _T	F	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1	2.5	%
	E _{TOT}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sonoitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±1	2	%
Sensitivity Error		$T_A = -40^{\circ}$ C to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.8	5.5	%
Valtage Offeet Error	1/	I _P = 0 A, T _A = 25°C to 150°C	-15	±5	15	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±20	30	mV
LIFETIME DRIFT CHARACT	ERISTICS [4]		· · · · · ·		
Sensitivity Error Including	-	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-3	±1	3	%
Lifetime Drift	E _{sens_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including	F	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-3.5	±1	3.5	%
Lifetime Drift	E _{tot_drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime		$T_A = 25^{\circ}C$ to $150^{\circ}C$	-20	_	20	mV
Drift	E _{Off_Drift}	$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.



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xLMATR-65AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40^{\circ}$ C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Units
NOMINAL PERFORMANCE			,	· · · · · · · · · · · · · · · · · · ·		
Current Sensing Range	I _{PR}		-65	_	65	A
Sensitivity	Sens	I _{PR(min)} < I _P < I _{PR(max)}	-	30.75	-	mV/A
Zero Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E			·		
Total Output Error ^[2]	E _{TOT}	$I_P = I_{PR(max)}$, $T_A = 25^{\circ}C$ to $150^{\circ}C$	-2.5	±1	2.5	%
		$I_P = I_{PR(max)}$, $T_A = -40^{\circ}$ C to 25°C	-6	±3	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)		· · · · ·		
Sensitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±1	2	%
		$T_A = -40^{\circ}C$ to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.8	5.5	%
Voltage Offset Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-15	±5	15	mV
		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±20	30	mV
LIFETIME DRIFT CHARACT	ERISTICS [4	9				
Sensitivity Error Including Lifetime Drift	E _{sens_drift}	T _A = 25°C to 150°C	-3	±1	3	%
		$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±3	5.5	%
Total Output Error Including Lifetime Drift	E _{tot_drift}	T _A = 25°C to 150°C	-3.5	±1	3.5	%
		$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%
Offset Error Including Lifetime Drift	E _{Off_Drift}	T _A = 25°C to 150°C	-20	_	20	mV
		$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.



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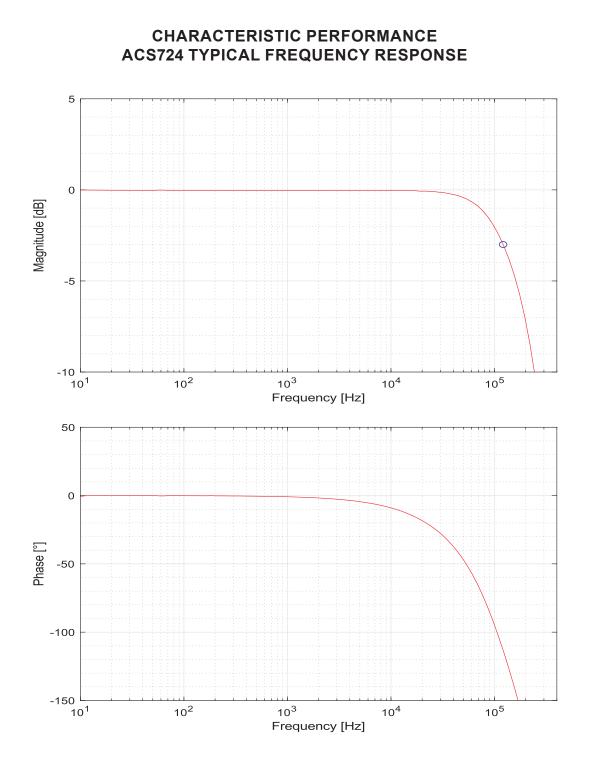
xLMATR-100AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at T_A = -40°C to 150°C, V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Units		
NOMINAL PERFORMANCE								
Current Sensing Range	I _{PR}		-100	-	100	A		
Sensitivity	Sens	I _{PR(min)} < I _P < I _{PR(max)}	-	20	-	mV/A		
Zero Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V		
ACCURACY PERFORMANC	E		·	· · · · · ·				
Total Output Error ^[2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1	2.5	%		
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%		
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] : E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)						
Sonoitivity Error	E _{SENS}	$T_A = 25^{\circ}C$ to 150°C, measured at $I_P = I_{PR(max)}$	-2	±1	2	%		
Sensitivity Error		$T_A = -40^{\circ}C$ to 25°C, measured at $I_P = I_{PR(max)}$	-5.5	±2.8	5.5	%		
Voltage Offset Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-15	±5	15	mV		
		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±20	30	mV		
LIFETIME DRIFT CHARACT	ERISTICS [4	1						
Sensitivity Error Including Lifetime Drift	E _{sens_drift}	$T_A = 25^{\circ}C$ to $150^{\circ}C$	-3	±1	3	%		
		$T_A = -40^{\circ}C$ to 25°C	-5.5	±3	5.5	%		
Total Output Error Including Lifetime Drift	E _{tot_drift}	$T_A = 25^{\circ}C$ to $150^{\circ}C$	-3.5	±1	3.5	%		
		$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±3	6	%		
Offset Error Including Lifetime	E _{Off_Drift}	$T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-20	-	20	mV		
Drift		$T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-30	±20	30	mV		

^[1] Typical values with +/- are 3 sigma values.

^[2] Percentage of I_P , with $I_P = I_{PR}(max)$.





For information regarding bandwidth characterization methods used for the ACS724, see the "Characterizing System Bandwidth" application note (https://allegromicro.com/en/insights-and-innovations/technical-documents/hall-effect-sensor-ic-publications/an-effective-method-for-characterizing-system-bandwidth-an296169) on the Allegro website.



RESPONSE CHARACTERISTICS DEFINITIONS AND PERFORMANCE DATA

Response Time (t_{RESPONSE})

The time interval between a) when the sensed input current reaches 90% of its final value, and b) when the sensor output reaches 90% of its full-scale value.

Propagation Delay (t_{pd})

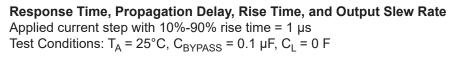
The time interval between a) when the sensed input current reaches 20% of its full-scale value, and b) when the sensor output reaches 20% of its full-scale value.

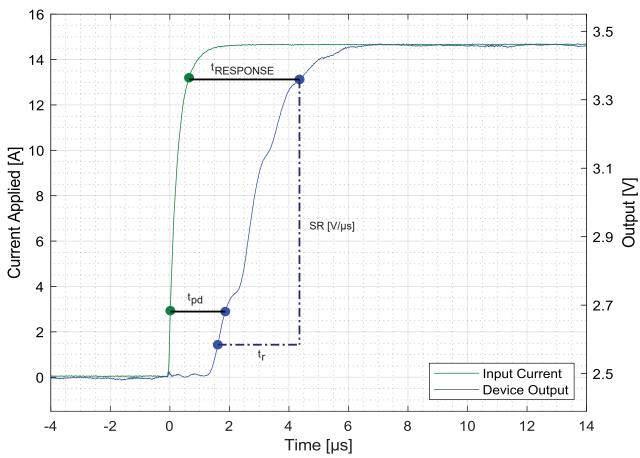
Rise Time (t_r)

The time interval between a) when the sensor reaches 10% of its full-scale value, and b) when it reaches 90% of its full-scale value.

Output Slew Rate (SR)

The rate of change $(V/\mu s)$ in the output voltage from a) when the sensor reaches 10% of its full-scale value, and b) when it reaches 90% of its full-scale value.







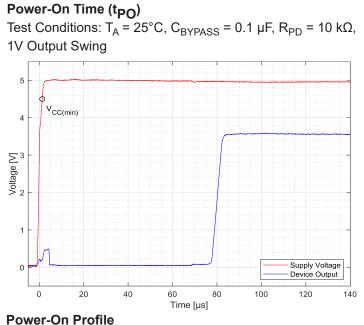
POWER ON FUNCTIONAL DESCRIPTION AND PERFORMANCE DATA

Power-On Time (tpo)

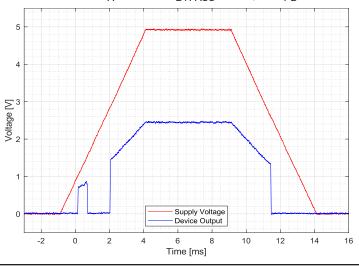
When the supply is ramped to its operating voltage, the device requires a finite amount of time to power its internal components before responding to an input magnetic field. Power-On Time (t_{PO}) is defined as the time interval between a) the power supply has reached its minimum specified operating voltage $(V_{CC}(\min))$, and b) when the sensor output has settled within $\pm 10\%$ of its steady-state value under an applied magnetic field.

Power-On Profile

After applying power, the part remains off in a known state referred to as Power-on Reset, or POR. The device stays in this state until the voltage reaches a point at which the device will remain powered. The power-on profile below illustrates the intended power on/off. A pull-down resistor was used on the output of the tested device.



Supply voltage ramp rate = 1V/ms Test Conditions: $T_A = 25^{\circ}$ C, $C_{BYPASS} = 0.1 \mu$ F, $R_{PD} = 10 k\Omega$





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DEFINITIONS OF ACCURACY CHARACTERISTICS

Sensitivity (Sens)

The change in sensor IC output in response to a 1 A change through the primary conductor. The sensitivity is the product of the magnetic coupling factor (G/A) (1 G = 0.1 mT) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

Nonlinearity (E_{LIN})

The nonlinearity is a measure of how linear the output of the sensor IC is over the full current measurement range. The nonlinearity is calculated as:

$$E_{\text{LIN}} = \left\{ 1 - \left[\frac{V_{\text{IOUT}}(I_{\text{PR}}(\text{max})) - V_{\text{IOUT}(\text{Q})}}{2 \times V_{\text{IOUT}}(I_{\text{PR}}(\text{max})/2) - V_{\text{IOUT}(\text{Q})}} \right] \right\} \times 100 \ (\%)$$

where $V_{IOUT}(I_{PR(max)})$ is the output of the sensor IC with the maximum measurement current flowing through it and $V_{IOUT}(I_{PR(max)}/2)$ is the output of the sensor IC with half of the maximum measurement current flowing through it.

Zero Current Output Voltage (VIOUT(Q))

The output of the sensor when the primary current is zero. For a unipolar supply voltage, it nominally remains at $0.5 \times V_{CC}$ for a bidirectional device and $0.1 \times V_{CC}$ for a unidirectional device. For example, in the case of a bidirectional output device, $V_{CC} = 5.0$ V translates into $V_{IOUT(Q)} = 2.50$ V. Variation in $V_{IOUT(Q)}$ can be attributed to the resolution of the Allegro linear IC quiescent voltage trim and thermal drift.

Voltage Offset Error (V_{OE})

The deviation of the device output from its ideal quiescent value of 0.5 \times V_{CC} (bidirectional) or 0.1 \times V_{CC} (unidirectional) due to nonmagnetic causes. To convert this voltage to amperes, divide by the device sensitivity, Sens.

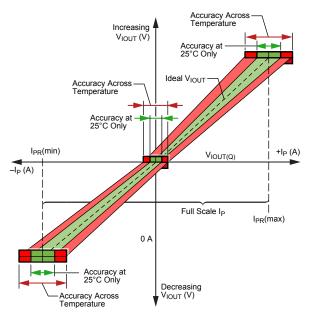
Total Output Error (E_{TOT})

The difference between the current measurement from the sensor IC and the actual current (I_p), relative to the actual current. This is equivalent to the difference between the ideal output voltage and the actual output voltage, divided by the ideal sensitivity, relative to the current flowing through the primary conduction path:

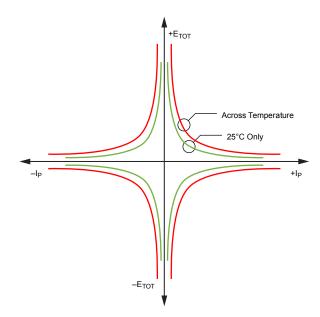
$$E_{\text{TOT}}(I_{\text{P}}) = \frac{V_{\text{IOUT_ideal}}(I_{\text{P}}) - V_{\text{IOUT}}(I_{\text{P}})}{\text{Sens}_{\text{ideal}}(I_{\text{P}}) \times I_{\text{P}}} \times 100$$
(%)

The Total Output Error incorporates all sources of error and is a function of I_P . At relatively high currents, E_{TOT} will be mostly due to

sensitivity error, and at relatively low currents, E_{TOT} will be mostly due to Voltage Offset Error (V_{OE}). In fact, at $I_P = 0$, E_{TOT} approaches infinity due to the offset. This is illustrated in Figure 1 and Figure 2. Figure 1 shows a distribution of output voltages versus I_P at 25°C and across temperature. Figure 2 shows the corresponding E_{TOT} versus I_P .











APPLICATION INFORMATION

Estimating Total Error versus Sensed Current

The Performance Characteristics tables give distribution (±3 sigma) values for Total Error at $I_{PR(max)}$; however, one often wants to know what error to expect at a particular current. This can be estimated by using the distribution data for the components of Total Error, Sensitivity Error, and Voltage Offset Error. The ±3 sigma value for Total Error (E_{TOT}) as a function of the sensed current (I_p) is estimated as:

$$E_{TOT}(I_p) = \sqrt{E_{SENS}^2 + \left(\frac{100 \times V_{OE}}{Sens \times I_p}\right)^2}$$

Here, E_{SENS} and V_{OE} are the ± 3 sigma values for those error terms. If there is an average sensitivity error or average offset voltage, then the average Total Error is estimated as:

$$E_{TOT_{AVG}}(I_{P}) = E_{SENS_{AVG}} + \frac{100 \times V_{OE_{AVG}}}{Sens \times I_{P}}$$

The resulting total error will be a sum of E_{TOT} and E_{TOT_AVG} . Using these equations and the 3 sigma distributions for Sensitivity Error and Voltage Offset Error, the Total Error versus sensed current (I_p) is shown here for the ACS724LMATR-20AB. As expected, as one goes towards zero current, the error in percent goes towards infinity due to division by zero.

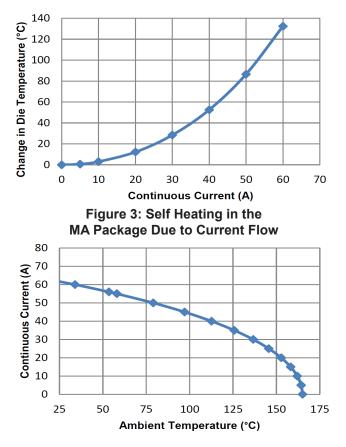


Thermal Rise vs. Primary Current

Self-heating due to the flow of current should be considered during the design of any current sensing system. The sensor, printed circuit board (PCB) and contacts to the PCB will generate heat as current moves through the system.

The thermal response is highly dependent on PCB layout, copper thickness, cooling techniques, and the profile of the injected current. The current profile includes peak current, current "on-time", and duty cycle. While the data presented in this section was collected with Direct-Current (DC) these numbers may be used to approximate thermal response for both AC signals and current pulses.

The plot in Figure 3 shows the measured rise in steady-state die temperature of the ACS724 versus continuous current at an ambient temperature, T_A , of 25 °C. The thermal offset curves may be directly applied to other values of T_A . Conversely, Figure 4 shows the maximum continuous current at a given T_A . Surges beyond the maximum current listed in Figure 4 are allowed given the maximum junction temperature, $T_{J(MAX)}$ (165°C), is not exceeded.





The thermal capacity of the ACS724 should be verified by the end user in the application's specific conditions. The maximum junction temperature, $T_{J(MAX)}$ (165°C), should not be exceeded. Further information on this application testing is available in the <u>DC and</u> Transient Current Capability application note on the Allegro website.

ASEK724 Evaluation Board Layout

Thermal data shown in Figure 3 and Figure 4 was collected using the ASEK724 Evaluation Board (TED-85-0815-003). This board includes 1500 mm² of 4 oz. copper (0.1388 mm) connected to pins 1 through 4, and to pins 5 through 8, with thermal vias connecting the layers. Top and Bottom layers of the PCB are shown below in Figure 5.

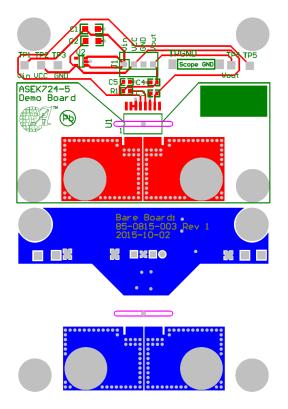


Figure 5: Top and Bottom Layers for ASEK724 Evaluation Board

Gerber files for the ASEK724 evaluation board are available for download from our website. Please see the technical documents section of the <u>ACS724</u> device webpage.



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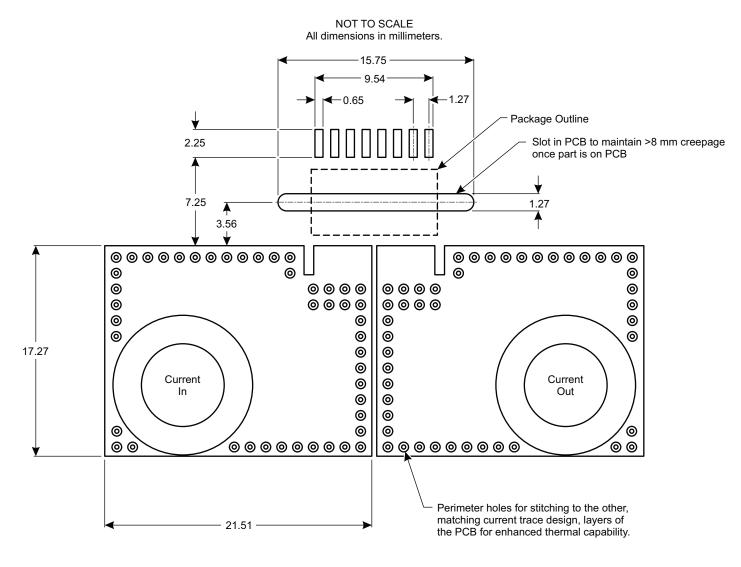
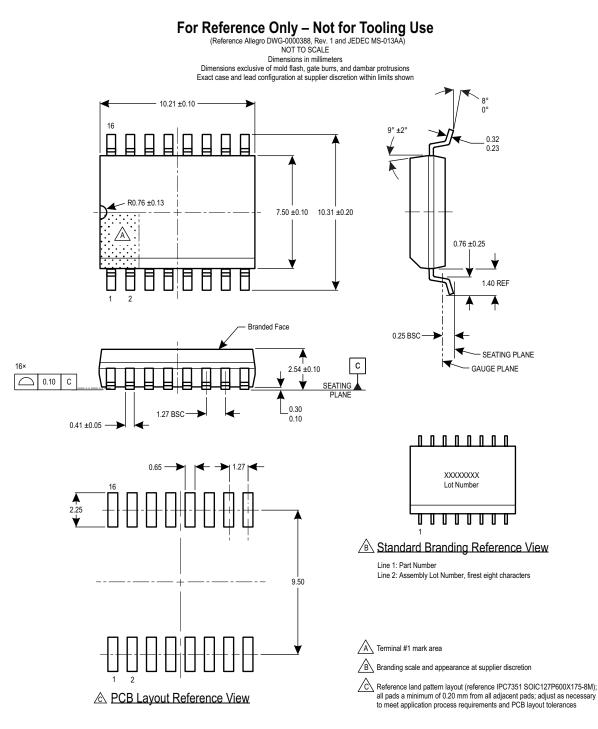


Figure 6: High-Isolation PCB Layout



Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

PACKAGE OUTLINE DRAWING







Automotive-Grade, High-Accuracy, Hall-Effect-Based Current Sensor IC with Common-Mode Field Rejection in High-Isolation SOIC16 Package

Revision History

Number	Date	Description	
_	October 16, 2017	Initial release	
1	October 31, 2017	Updated Lifetime Drift Characteristics (pages 6-10)	
2	November 27, 2017	Added Sensitivity Ratiometry Coefficient and Zero-Current Output Ratiometry Coefficient to Electrical Characteristics table (page 5).	
3	January 8, 2018	Corrected certification status (page 1 and 3)	
4	January 12, 2018	Added Dielectric Surge Strength Test Voltage to Isolation Characteristics table (page 3).	
5	January 22, 2018	Added Common Mode Field Rejection Ratio characteristic (page 5).	
6	March 9, 2018	Added -65AB part option.	
7	May 7, 2018	Updated certification status (page 1 and 3)	
8	June 22, 2018	Added Typical Frequency Response plots (page 18).	
9	December 18, 2018	Updated certificate numbers	
10	June 3, 2019	Updated TUV certificate mark	
11	July 25, 2019	Updated Isolation Characteristics and Thermal Characteristics tables (page 3); added ESD Ratings table (page 3) and Application Information section (page 22).	
12	April 6, 2020	Added -100AB part variant (pages 2, 12); added Maximum Current to Absolute Maximum Ratings table (page 3); updated Isolation Characteristics Table (page 3); updated Rise Time, Response Time, Propagation Delay, and Output Slew Rate test conditions (page 5); added Primary Conductor Inductance and Output Slew Rate values (page 5); updated Typical Frequency Response (page 13); added Response Characteristics Definitions and Performance Data (page 14); added Power On Functional Description and Performance Data (page 18); corrected Offset Voltage to Voltage Offset Error (pages 6-17); removed distribution plots	
13	May 26, 2021	Removed Maximum Continuous Current from Absolute Maximum Ratings table (page 3)	
14	May 26, 2022	Updated package drawing (page 20)	

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