

Vishay Siliconix

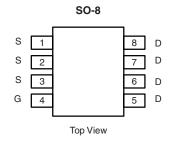
## N-Channel 30-V MOSFET

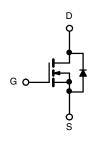
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
30	0.0045 at V <sub>GS</sub> = 10 V	20	24		
30	0.006 at V <sub>GS</sub> = 4.5 V	17	24		

#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFETs
- 100 % Rg Tested







N-Channel MOSFET

Ordering Information: Si4430BDY-T1-E3 (Lead (Pb)-free) Si4430BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \degree C$ , unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30			
Gate-Source Voltage		V <sub>GS</sub>	± 20		V	
	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	20	14		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T <sub>A</sub> = 70 °C		16	11		
Pulsed Drain Current		I <sub>DM</sub>	± 60		А	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.7	1.40		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>		40		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	80		mJ	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.0	1.6	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	١D	2.0	1.0		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55	to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R <sub>thJA</sub>	34	41	
Maximum Junction-to-Ambient (MOSFET) <sup>a</sup>	Steady State	"thJA	67	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	15	19	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	<u> </u>					
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
	1	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			А
	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$ $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 17 \text{ A}$		0.0037	0.0045	Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>			0.0048	0.006	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		80		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{\rm S}$ = 2.7 A, $V_{\rm GS}$ = 0 V		0.72	1.1	V
Dynamic <sup>b</sup>	•			•		
Total Gate Charge	Qg			24	36	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		10.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			7.5		
Gate Resistance	Rg		0.5	1.1	1.7	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			20	30	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		14	22	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		60	90	ns
Fall Time	t <sub>f</sub>			18	30	
	t <sub>rr</sub>	I <sub>E</sub> = 2.7 A, dl/dt = 100 A/μs		35	50	
Source-Drain Reverse Recovery Time	Q <sub>rr</sub>	$r_{\rm F} = 2.7$ A, u/dl = 100 A/µs		32	50	nC

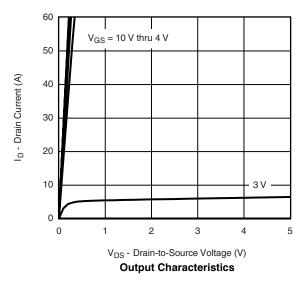
Notes:

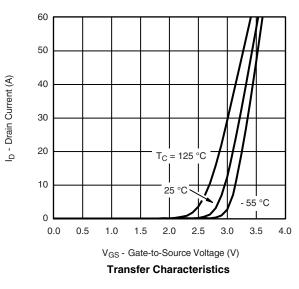
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

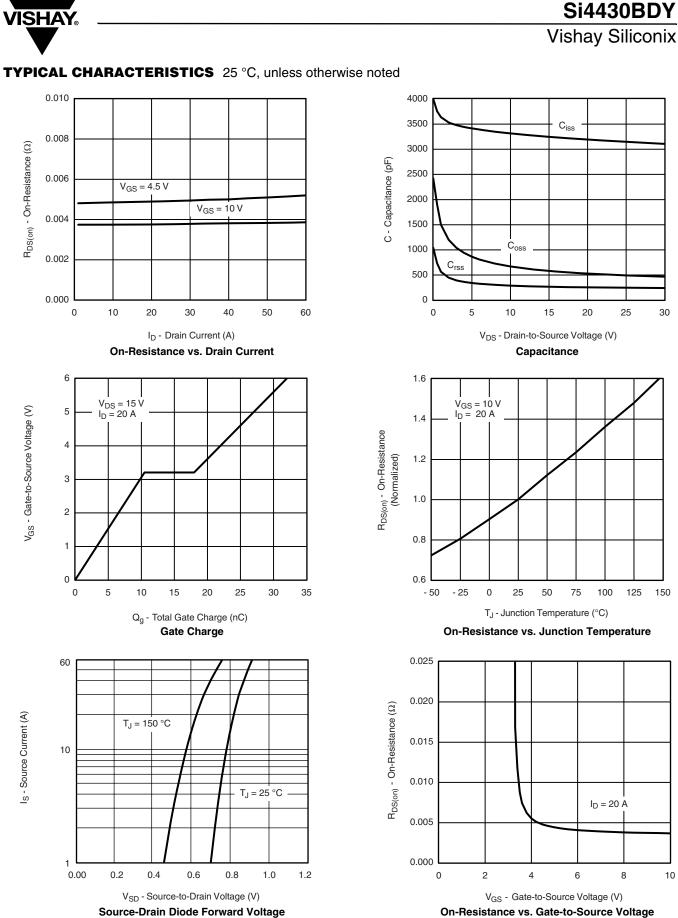
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Source-Drain Diode Forward Voltage

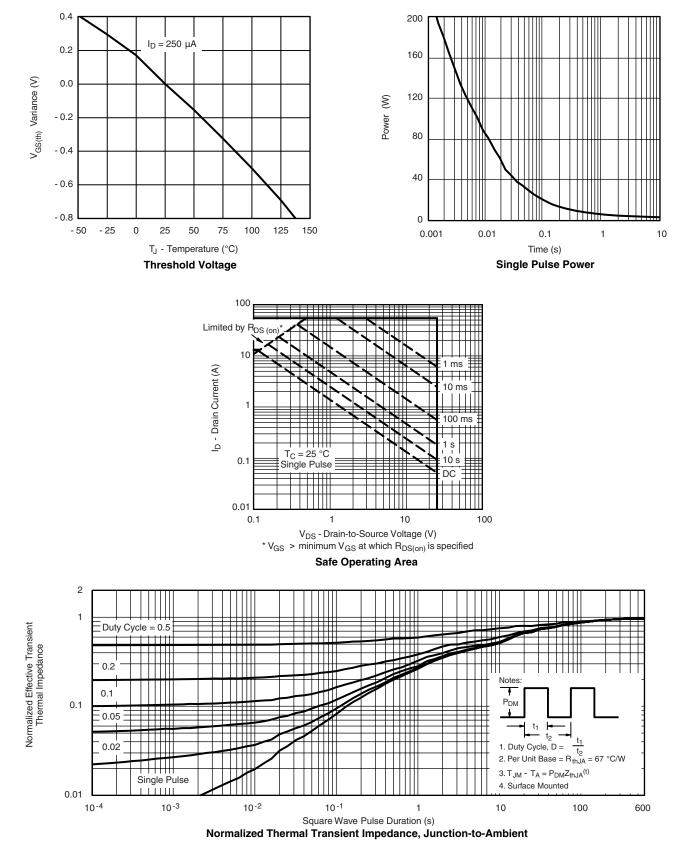
Document Number: 73184 S09-0228-Rev. D, 09-Feb-09 10

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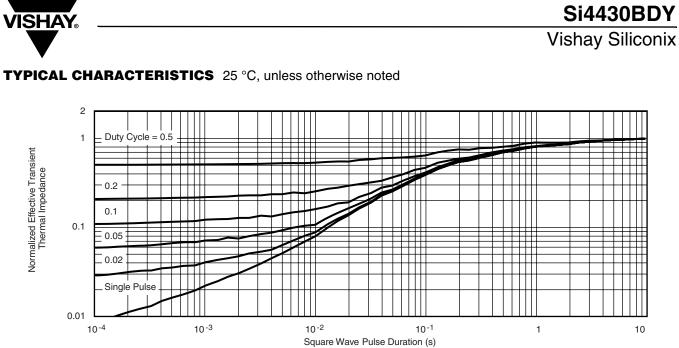
## Si4430BDY

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?73184</u>.



## Package Information

Vishay Siliconix

# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	50 BSC		
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

## **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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