Schottky Barrier Diode

NSR0130P2

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.385 V (max) @ $I_F = 10 \text{ mA}$
- Low Reverse Current
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	30	Vdc
Forward Current DC	١ _F	100	mA
Forward Current Surge Peak (60 Hz, 1 cycle)	I _{FSM}	1.0	A
ESD Rating: Class 3B per Human Body Model Class B per Machine Model			

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) $T_A = 25^{\circ}C$	PD	200	mW
Derate above 25°C		2.0	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	600	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +125	°C

1. FR-5 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

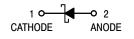
Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage (V _R = 10 V)	۱ _R	-	-	0.35	μA
(V _R = 30 V) Forward Voltage	VF	-	-	3.0	Vdc
$(I_F = 10 \text{ mA})$ $(I_F = 100 \text{ mA})$	- F	-	-	0.385 0.525	

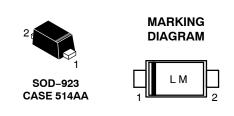


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30 V SCHOTTKY BARRIER DIODE





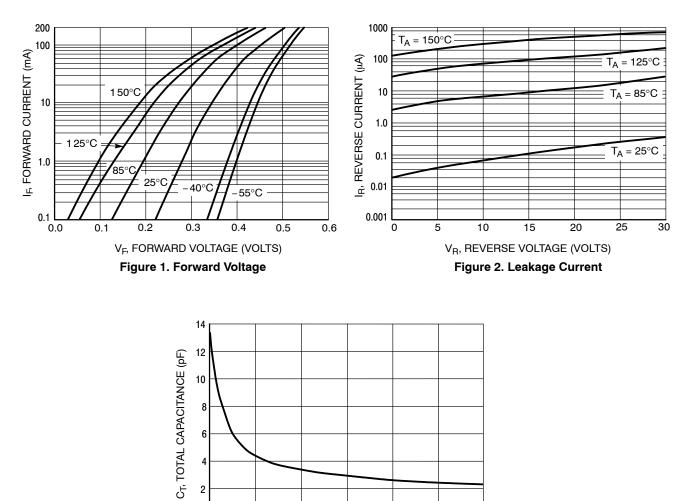
L = Specific Device Code* (Character is rotated 270° clockwise) M = Month Code

ORDERING INFORMATION

Device	Package	Shipping†
NSR0130P2T5G	SOD-923 (Pb-Free)	2 mm Pitch 8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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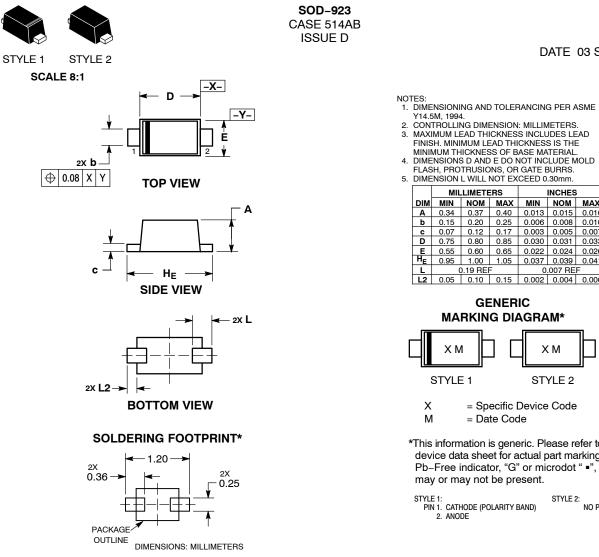


V_R, REVERSE VOLTAGE (VOLTS) Figure 3. Total Capacitance

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MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



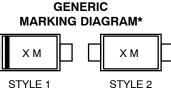
See Application Note AND8455/D for more mounting details

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



DATE 03 SEP 2020

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.34	0.37	0.40	0.013	0.015	0.016
b	0.15	0.20	0.25	0.006	0.008	0.010
С	0.07	0.12	0.17	0.003	0.005	0.007
D	0.75	0.80	0.85	0.030	0.031	0.033
E	0.55	0.60	0.65	0.022	0.024	0.026
HE	0.95	1.00	1.05	0.037	0.039	0.041
L	0.19 REF			0	.007 RE	F
L2	0.05	0.10	0.15	0.002	0.004	0.006



= Specific Device Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ",

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