

Features

- RoHS compliant*
- Protects one line
- ESD protection 30 kV max.

Applications

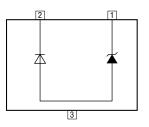
- RS-232, RS-422 & RS-423 data lines
- Portable electronics
- Wireless bus protection
- Control & monitoring systems

CDSOT23-T03LC~T36LC - Low Capacitance TVS Diode Array Series

General Information

Portable communications, computing and video equipment manufacturers are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Transient Voltage Suppressor Array Diodes for surge and ESD protection applications, in compact chip package SOT23 size format. The Transient Voltage Supressor Array series offers a choice of voltage types ranging from 3 V to 36 V. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.



The Bourns® device will meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements.

Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

| Parameter | Symbol | Value | Unit °C | |
|-----------------------|------------------|-------------|------------|--|
| Operating Temperature | T _J | -55 to +150 | | |
| Storage Temperature | T _{STG} | -55 to +150 | °C | |

Additional Information

Click these links for more information:











PRODUCT TECHNICAL INVENTORY SAMPLES

Electrical Characteristics (@ TA = 25 °C Unless Otherwise Noted)

| Parameter | Symbol | T03LC | T05LC | T08LC | T12LC | T15LC | T24LC | T36LC | Unit |
|--|--------------------|-------------------------|------------------|------------------|------------------|------------------|------------------|-----------------|------|
| Breakdown Voltage @ 1 mA | V _{BR} | 4.0 | 6.0 | 8.5 | 13.3 | 16.7 | 26.7 | 40.0 | V |
| Working Peak Voltage | V _{WM} | 3.3 | 5.0 | 8.0 | 12.0 | 15.0 | 24.0 | 36.0 | ٧ |
| Maximum Clamping Voltage V _C @ I _P ¹ | V _F | 8.0 | 9.8 | 13.4 | 19.0 | 24.0 | 43.0 | 51.0 | V |
| Maximum Clamping Voltage @ 8/20 µs V _C @ I _{PP} ¹ | V _F | 10.9 V @ 43 A | 13.5 V @ 42 A | 16.9 V @ 34 A | 25.9 V @ 27 A | 30.0 V @ 17 A | 49.0 V @ 12 A | 76.8 V @ 9 A | V |
| Maximum Leakage Current @ V _{WM} | I _D | 125 | 20 | 10 | 2 | 1 | 1 | 1 | μΑ |
| Typical Capacitance Bidirectional @ 0 V, 1 MHz | C _{j(SD)} | | | | 5 | | | | pF |
| ESD Protection (per IEC 61000-4-2) Contact - Min. Contact - Max. Air - Min. Air - Max. | ESD | ±8 ±30 ±15 ±30 | | | kV | | | | |
| Peak Pulse Power $(t_p = 8/20 \mu s)^2$ | P _{PP} | 500 | | | | W | | | |

Notes:

- 1. See Pulse Wave Form.
- 2. See Peak Pulse Power vs. Pulse Time.
- 3. Positive Potential is applied from Pin 1 to Pin 2 with Pin 2 as ground.
- 4. Do not test or surge from Pin 2 to Pin 1.



WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice.

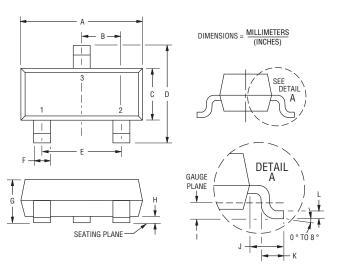
Users should verify actual device performance in their specific applications.

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CDS0T23-T03LC~T36LC - Low Capacitance TVS Diode Array Series **BOURNS**°

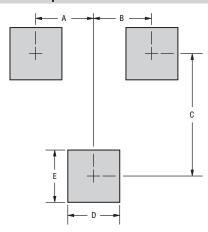
Product Dimensions

This is a molded JEDEC SOT23-6 package with lead free 100 % Sn plating on the lead frame. It weighs approximately 0.6 g and has a flammability rating of UL 94V-0.



| Dimensions | | | |
|------------|--|--|--|
| А | <u>2.80 - 3.00</u> (0.110 - 0.118) | | |
| В | $\frac{0.95}{(0.037)}$ BSC | | |
| С | <u>1.20 - 1.40</u> (0.047 - 0.055) | | |
| D | <u>2.10 - 2.49</u> (0.083 - 0.098) | | |
| E | 1.90 (0.075) BSC | | |
| F | <u>0.30 - 0.50</u> (0.012 - 0.019) | | |
| G | <u>0.89 - 1.17</u> (0.035 - 0.046) | | |
| Н | <u>0.05 - 0.015</u> (0.002 - 0.006) | | |
| I | 0.25 (0.010) BSC | | |
| J | <u>0.46 - 0.64</u> (0.018 - 0.025) | | |
| К | <u>0.40 - 0.58</u> (0.016 - 0.023) | | |
| L | <u>0.08 - 0.20</u> (0.003 - 0.008) | | |

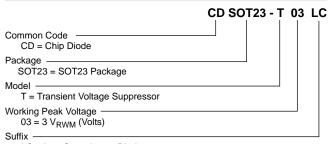
Recommended Footprint



 $\mathsf{DIMENSIONS} = \frac{\mathsf{MILLIMETERS}}{(\mathsf{INCHES})}$

| Dimensions | | |
|------------|------------------------|--|
| А | <u>0.95</u> (0.037) | |
| В | <u>0.95</u> (0.037) | |
| С | 2.00 (0.079) | |
| D | <u>0.85</u> (0.033) | |
| E | <u>0.85</u> (0.033) | |

How to Order



LC = Low Capacitance Diode

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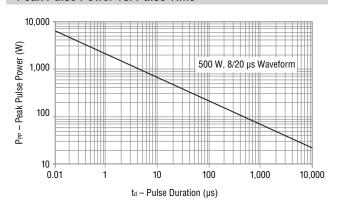
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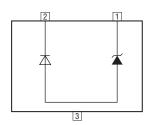
Performance Graphs

Peak Pulse Power vs. Pulse Time

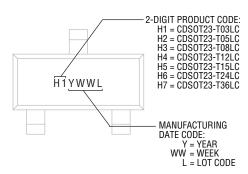


Block Diagram

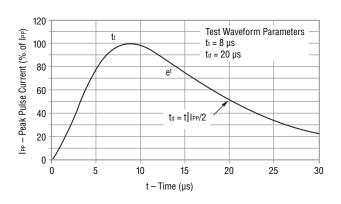
The device block diagram below includes the pin names and basic electrical connections.



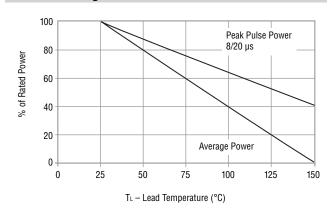
Typical Part Marking



Pulse Waveform



Power Derating Curve



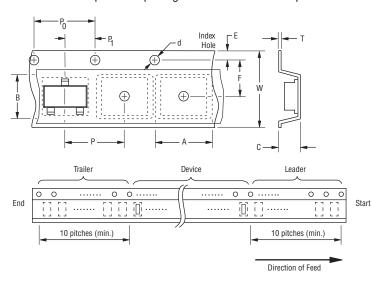
Environmental Specifications

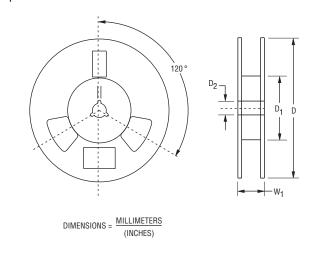
| Moisture Sensitivity Level | 1 |
|----------------------------|----|
| Moisture Sensitivity Level | 1 |
| ESD Classification (HPM) | 2D |

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Packaging Information

The surface mount product is packaged in a 12 mm x 8 mm tape and reel format per EIA-481 standard.





| Item | Symbol | SOT23 |
|--------------------------|----------------|--|
| Carrier Width | А | 2.25 ± 0.10 |
| | | (0.088 ± 0.004) 2.34 ± 0.10 |
| Carrier Length | В | (0.092 ± 0.004) |
| Carrier Depth | С | 1.22 ± 0.10 |
| | | (0.048 ± 0.004) |
| Sprocket Hole | d | $\frac{1.55 \pm 0.05}{(0.061 \pm 0.002)}$ |
| Reel Outside Diameter | D | 178 |
| | | (7.008) 50.0 Min |
| Reel Inner Diameter | D ₁ | (1.969) Min. |
| Facilitate Biometre | - | 13.0 ± 0.20 |
| Feed Hole Diameter | D ₂ | (0.512 ± 0.008) |
| Sprocket Hole Position | E | 1.75 ± 0.10 |
| Oprocket Floic F Osition | | (0.069 ± 0.004) |
| Punch Hole Position | F | 3.50 ± 0.05 |
| | | $ (0.138 \pm 0.002) $ $ 4.00 \pm 0.10 $ |
| Punch Hole Pitch | Р | $\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$ |
| | P ₀ | 4.00 ± 0.10 |
| Sprocket Hole Pitch | | $\frac{1000 \pm 0.004}{(0.157 \pm 0.004)}$ |
| Embossment Center | P ₁ | 2.00 ± 0.05 |
| Embossilient Center | | (0.079 ± 0.002) |
| Overall Tape Thickness | Т | 0.20 ± 0.10 |
| Overall Tape TrileRitess | | (0.008 ± 0.004) |
| Tape Width | W | $\frac{8.00 \pm 0.20}{(0.345 \pm 0.008)}$ |
| • | | (0.315 ± 0.008) |
| Reel Width | W ₁ | 14.4 Max. |
| Quantity per Reel | _ | 3,000 |

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REV. 09/21

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