**Product data sheet** 

## 1. General description

AC Thyristor power switch in a SOT54 plastic package with self-protective capabilities against low and high energy transients

## 2. Features and benefits

- Exclusive negative gate triggering
- Full cycle AC conduction
- High noise immunity
- · Remote gate separates the gate driver from the effects of the load current
- Very sensitive gate for lowest gate trigger current
- Safe clamping of low energy over-voltage transients
- Self-protective turn-on during high energy voltage transients

## 3. Applications

- · Fan motor circuits
- · Pump motor circuits
- · Lower-power highly inductive, resistive and safety loads

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{DRM}$	repetitive peak off- state voltage		-	-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>lead</sub> ≤ 71 °C; <u>Fig. 1</u>	-	-	8.0	Α
Static characte	eristics					
I <sub>GT</sub> g	gate trigger current	$V_D = 12 \text{ V}; I_T = 100 \text{ mA}; LD+ G-;$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 6}}{}$	0.5	-	5	mA
		$V_D = 12 \text{ V}; I_T = 100 \text{ mA}; LD- G-;$ $T_j = 25 \text{ °C}; Fig. 6$	0.5	-	5	mA

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# **5. Pinning information**

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	СМ	common		LD
2	G	gate		
3	LD	load	TO-92 (SOT54)	G <b>→ □</b> CM 001aaj924

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
ACT108-600D	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54		
ACT108-600D/DG	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54		

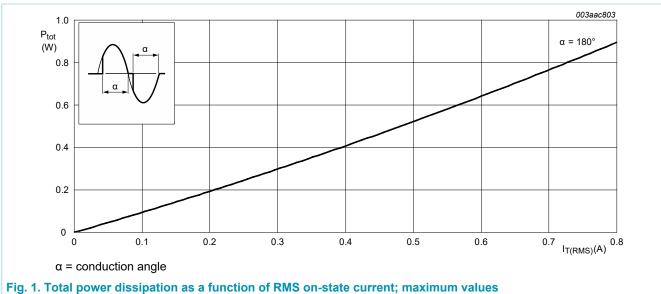
**AC Thyristor power switch** 

# 7. Limiting values

#### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>lead</sub> ≤ 71 °C; Fig. 1	-	0.8	Α
I <sub>TSM</sub>	non-repetitive peak on-	full sine wave; $T_{j(init)} = 25  ^{\circ}C$ ; $t_p = 16.7  \text{ms}$	-	8.8	Α
state current	state current	full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 20 ms; Fig. 2; Fig. 3	-	8	A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	0.32	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 10 mA	-	50	A/µs
I <sub>GM</sub>	peak gate current	t = 20 μs	-	1	Α
$V_{GM}$	peak gate voltage		-	15	V
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.1	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C
$V_{PP}$	peak pulse voltage	T <sub>j</sub> = 25 °C; non-repetitive, off-state; Fig. 4	-	2	kV



#### **AC Thyristor power switch**

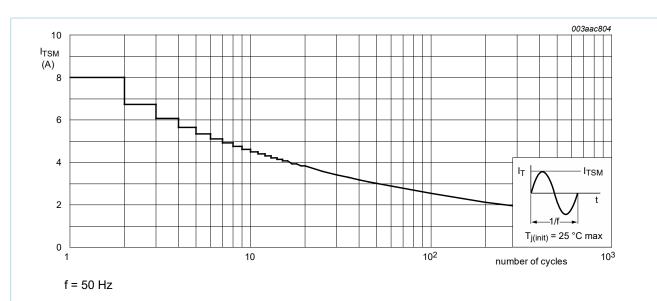


Fig. 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

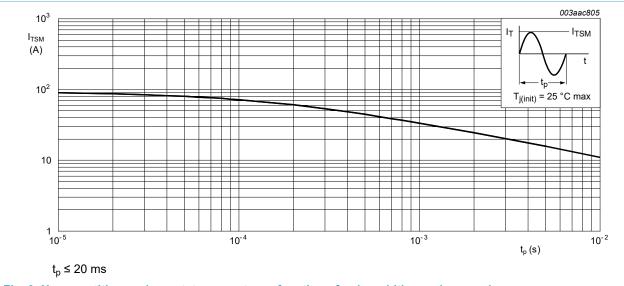


Fig. 3. Non-repetitive peak on-state current as a function of pulse width; maximum values

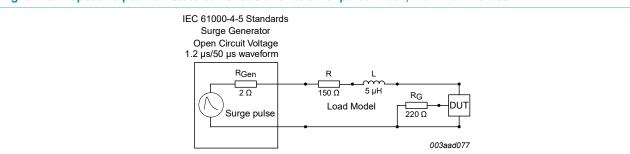


Fig. 4. Test circuit for inductive and resistive loads with conditions equivalent to IEC 61000-4-5

**AC Thyristor power switch** 

### 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-lead)</sub>	thermal resistance from junction to lead	full cycle with heatsink compound; Fig. 5	-	-	60	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	full cycle; printed-circuit board mounted; lead length 4 mm	-	150	-	K/W

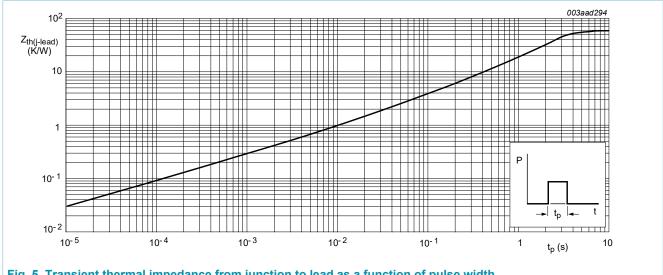


Fig. 5. Transient thermal impedance from junction to lead as a function of pulse width

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### 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 100 mA; LD+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	0.5	-	5	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 100 mA; LD- G-; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	0.5	-	5	mA
IL	latching current	$V_D = 12 \text{ V; } I_G = 100 \text{ mA; LD+ G-;}$ $T_j = 25 \text{ °C; } \frac{\text{Fig. 7}}{}$	-	-	25	mA
		$V_D = 12 \text{ V; } I_G = 100 \text{ mA; LD- G-;}$ $T_j = 25 \text{ °C; } \frac{\text{Fig. 7}}{}$	-	-	25	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	-	20	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 1.1 A; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	1.3	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 400 V; I <sub>T</sub> = 100 mA; T <sub>j</sub> = 125 °C	0.15	-	-	V
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 100 mA; T <sub>j</sub> = 25 °C	-	-	0.9	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 25 °C	-	-	2	μA
		V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	-	0.2	mA
$V_{CL}$	clamping voltage	$I_{CL} = 0.1 \text{ mA}; t_p = 1 \text{ ms}; T_j \le 125 \text{ °C}$	650	-	-	V
Dynamic cl	narateristics			'	'	
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit; Fig. 10	300	-	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$V_D$ = 400 V; $T_j$ = 125 °C; $I_{T(RMS)}$ = 1 A; $dV_{com}/dt$ = 15 V/ $\mu$ s; gate open circuit; Fig. 11; Fig. 12	0.15	-	-	A/ms

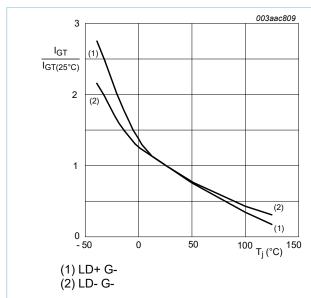


Fig. 6. Normalized gate trigger current as a function of junction temperature

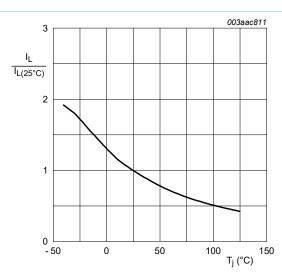


Fig. 7. Normalized latching current as a function of junction temperature

### **AC Thyristor power switch**

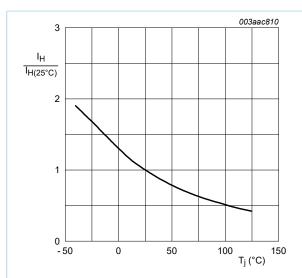
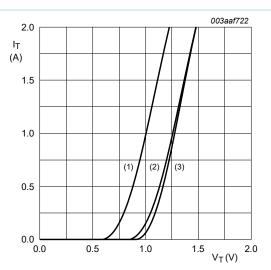


Fig. 8. Normalized holding current as a function of junction temperature



 $V_o = 0.758 \text{ V}; R_s = 0.263 \Omega$ 

(1) T<sub>i</sub> = 125 °C; typical values

(2) T<sub>i</sub> = 125 °C; maximum values

(3) T<sub>i</sub> = 25 °C; maximum values

Fig. 9. On-state current as a function of on-state voltage

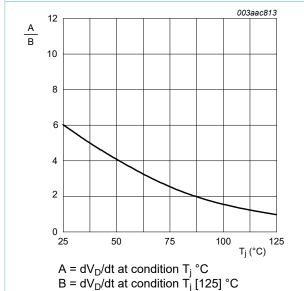
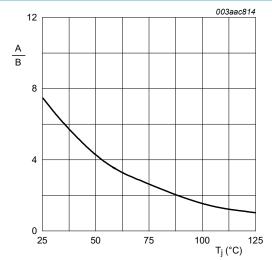


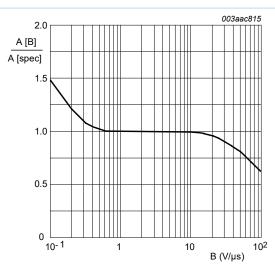
Fig. 10. Normalized rate of rise of off-state voltage as a function of junction temperature



A =  $dI_{com}/dt$  at condition  $T_j$  °C B =  $dI_{com}/dt$  at condition  $T_j$  [125] °C  $V_D$  = 400 V

Fig. 11. Normalized critical rate of rise of commutating current as a function of junction temperature

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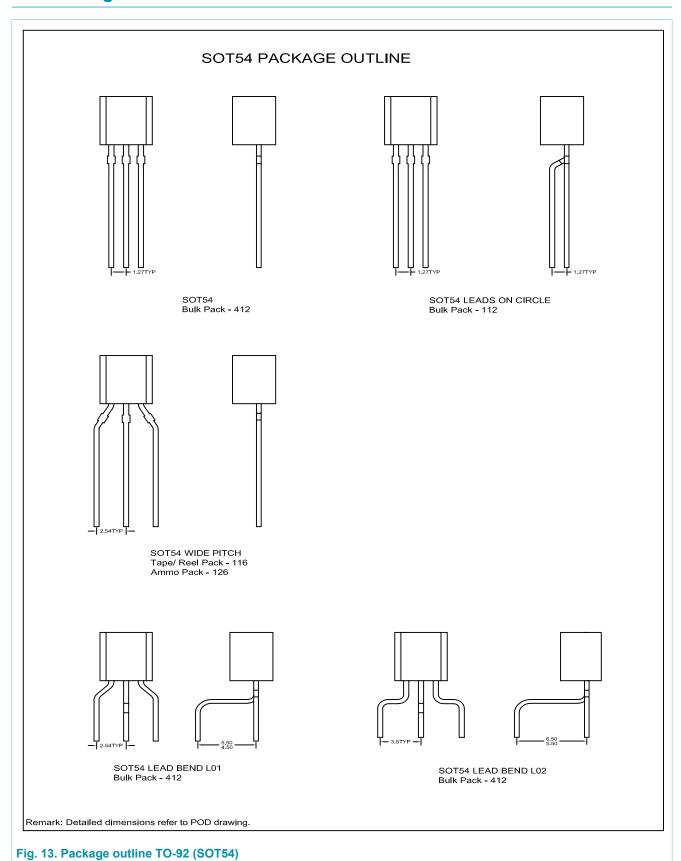


A [B] =  $dI_{com}/dt$  at condition B,  $dV_{com}/dt$ A [spec] is the data sheet value for  $dI_{com}/dt$ turn-off time is less than 20 ms

Fig. 12. Normalized critical rate of change of commutating current as a function of critical rate of change of commutating voltage; minimum values

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## 10. Package outline



ACT108-600D

#### **AC Thyristor power switch**

## 11. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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