500 mA Negative Voltage Regulators

The MC79M00 series of fixed output negative voltage regulators are intended as complements to the popular MC78M00 series devices.

Available in fixed output voltage options of -5.0 V, -8.0 V, -12 V and -15 V, these regulators employ current limiting, thermal shutdown, and safe–area compensation, making them remarkably rugged under most operating conditions. With adequate heatsinking they can deliver output currents in excess of 0.5 A.

Features

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Also Available in Surface Mount DPAK (DT) Package
- Pb-Free Packages are Available

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

Device	Nominal Output Voltage
MC79M05	−5.0 V
MC79M08	−8.0 V
MC79M12	−12 V
MC79M15	−15 V

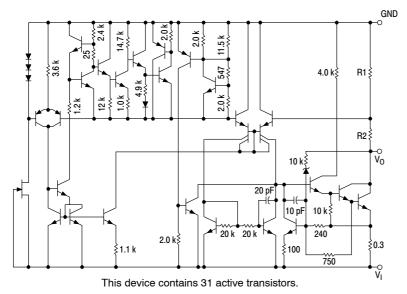


Figure 1. Representative Schematic Diagram

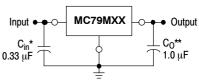


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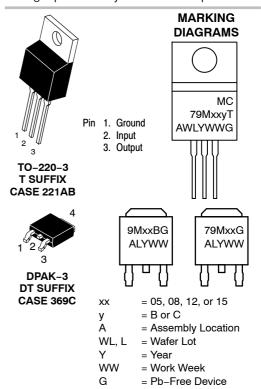
THREE-TERMINAL NEGATIVE FIXED VOLTAGE REGULATORS

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 1.1 V more negative even during the high point of the input ripple voltage. XX These two digits of the type number indicate nominal voltage.

- * C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O improve stability and transient response.



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

MAXIMUM RATINGS ($T_A = 25^{\circ}C$, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage	VI	-35	Vdc
Power Dissipation			
Case 221A (TO-220-3)			
T _A = 25°C	P_{D}	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	65	°C/W
Thermal Resistance, Junction-to-Case	$\theta_{\sf JC}$	5.0	°C/W
Case 369C (DPAK-3)			
T _A = 25°C	P_{D}	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	92	°C/W
Thermal Resistance, Junction-to-Case	$\theta_{\sf JC}$	6.0	°C/W
Storage Junction Temperature	T _{stg}	-65 to +150	°C
Operating Junction Temperature Range	TJ	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Human Body Model 2000 V per MIL_STD_883, Method 3015

Machine Model Method 200 V

MC79M05B, C **ELECTRICAL CHARACTERISTICS** ($V_I = -10 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 2), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-4.8	-5.0	-5.2	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 1) $-7.0 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}$ $-8.0 \text{ Vdc} \ge V_I \ge -18 \text{ Vdc}$	Reg _{line}	- -	7.0 2.0	50 30	mV
Load Regulation, T_J = 25°C (Note 1) 5.0 mA \leq I _O \leq 500 mA	Reg _{load}	-	30	100	mV
Output Voltage $-7.0 \text{ Vdc} \geq V_l \geq -25 \text{ Vdc}, 5.0 \text{ mA} \leq I_O \leq 350 \text{ mA}$	Vo	-4.75	-	-5.25	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	4.3	8.0	mA
Input Bias Current Change $-8.0~Vdc \ge V_I \ge -25~Vdc,~I_O = 350~mA \\ 5.0~mA \le I_O \le 350~mA,~V_I = -10~V$	Δl_{IB}	- -	- -	0.4 0.4	mA
Output Noise Voltage, $T_A = 25^{\circ}C$, 10 Hz \leq f \leq 100 kHz	V _n	_	40	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	66	-	dB
Dropout Voltage I _O = 500 mA, T _J = 25°C	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, $0^{\circ}C \le T_J \le 125^{\circ}C$	$\Delta V_{O}/\Delta T$	-	0.2	-	mV/°C

Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.
 B = T_{low} to T_{high}, -40°C < T_J < 125°C C = T_{low} to T_{high}, 0°C < T_J < 125°C.

^{*}This device series contains ESD protection and exceeds the following tests:

MC79M08B, C **ELECTRICAL CHARACTERISTICS** ($V_I = -10 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 4), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	-7.7	-8.0	-8.3	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 3) -10.5 Vdc \geq V $_I \geq$ -25 Vdc -11 Vdc \geq V $_I \geq$ -21 Vdc	Reg _{line}	- -	5.0 3.0	80 50	mV
Load Regulation, T_J = 25°C (Note 3) 5.0 mA \leq I _O \leq 500 mA	Reg _{load}	_	30	100	mV
Output Voltage $-10.5 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}, 5.0 \text{ mA} \le I_O \le 350 \text{ mA}$	Vo	-7.6	-8.0	-8.4	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	-	8.0	mA
Input Bias Current Change -10.5 Vdc \geq V $_{I}$ \geq -25 Vdc, I $_{O}$ = 350 mA 5.0 mA \leq I $_{O}$ \leq 350 mA, V $_{I}$ = -10 V	ΔI_{IB}	- -	_ _	0.4 0.4	mA
Output Noise Voltage, T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz	V _n	-	60	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	63	-	dB
Dropout Voltage $I_O = 500$ mA, $T_J = 25$ °C	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ mA}, 0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	$\Delta V_{O}/\Delta T$	_	0.4	-	mV/°C

^{3.} Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately.

MC79M12B, C **ELECTRICAL CHARACTERISTICS** ($V_I = -19 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 6), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-11.5	-12	-12.5	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 5) $-14.5 \text{ Vdc} \ge V_l \ge -30 \text{ Vdc}$ $-15 \text{ Vdc} \ge V_l \ge -25 \text{ Vdc}$	Reg _{line}	- -	5.0 3.0	80 50	mV
Load Regulation, T_J = 25°C (Note 5) 5.0 mA \leq I _O \leq 500 mA	Reg _{load}	-	30	240	mV
Output Voltage $-14.5 \text{ Vdc} \geq V_{I} \geq -30 \text{ Vdc}, 5.0 \text{ mA} \leq I_{O} \leq 350 \text{ mA}$	V _O	-11.4	-	-12.6	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change -14.5 Vdc \geq V _I \geq -30 Vdc, I _O = 350 mA 5.0 mA \leq I _O \leq 350 mA, V _I = -19 V	$\Delta I_{ m IB}$	- -	- -	0.4 0.4	mA
Output Noise Voltage, $T_A = 25^{\circ}C$, 10 Hz \leq f \leq 100 kHz	V _n	-	75	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	-	dB
Dropout Voltage I _O = 500 mA, T _J = 25°C	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, $0^{\circ}C \leq T_J \leq 125^{\circ}C$	$\Delta V_{O}/\Delta T$	_	-0.8	-	mV/°C

 ^{5.} Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.
 6. B = T_{low} to T_{high}, -40°C < T_J < 125°C C = T_{low} to T_{high}, 0°C < T_J < 125°C

Pulse testing with low duty cycle is used.

4. B = T_{low} to T_{high}, -40°C < T_J < 125°C
C = T_{low} to T_{high}, 0°C < T_J < 125°C

MC79M15B, C ELECTRICAL CHARACTERISTICS ($V_I = -23 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 8), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-14.4	-15	-15.6	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 7) -17.5 Vdc \geq V $_I \geq$ -30 Vdc -18 Vdc \geq V $_I \geq$ -28 Vdc	Reg _{line}	- -	5.0 3.0	80 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 7) 5.0 mA $\leq I_O \leq 500$ mA	Reg _{load}	_	30	240	mV
Output Voltage $-17.5 \text{ Vdc} \ge V_{\text{I}} \ge -30 \text{ Vdc}, 5.0 \text{ mA} \le I_{\text{O}} \le 350 \text{ mA}$	Vo	-14.25	-	-15.75	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change -17.5 Vdc \geq V $_{I} \geq$ -30 Vdc, I $_{O}$ = 350 mA 5.0 mA \leq I $_{O} \leq$ 350 mA, V $_{I}$ = -23 V	ΔI_{IB}	- -	- -	0.4 0.4	mA
Output Noise Voltage, T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz	V _n	-	90	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	-	dB
Dropout Voltage $I_O = 500 \text{ mA}, T_J = 25^{\circ}\text{C}$	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ mA}, 0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	$\Delta V_{O}/\Delta T$	-	-1.0	-	mV/°C

Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately.
 Pulse testing with low duty cycle is used.

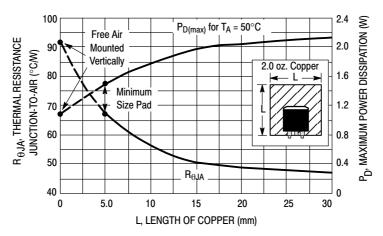


Figure 1. DPAK-3 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

Pulse testing with low duty cycle is used.

8. B = T_{low} to T_{high}, -40°C < T_J < 125°C
C = T_{low} to T_{high}, 0°C < T_J < 125°C

Protection Diodes

When external capacitors are used with MC79M00 series regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator or from output polarity reversals. Generally, no protection diode is required for values of output capacitance less then $10\mu F$. Figure 2 shows the MC79M15 with the recommended protection diodes.

• Opposite Polarity Protection

Diode D1 protects the regulator from output polarity reversals during startup, power off and short-circuit operation.

• Reverse-bias Protection

Diode D2 prevents output capacitor from discharging thru the MC79M15 during an input short circuit or fast switch off of power supply.

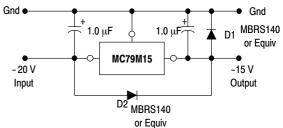


Figure 2. Protection Diodes

ORDERING INFORMATION

Device	Output Voltage Tolerance	Operating Temperature Range	Package	Shipping [†]
MC79M05BDT			DPAK	75 Units / Rail
MC79M05BDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M05BDTRK			DPAK	2500 Units / Reel
MC79M05BDTRKG		$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-Free)	2500 Units / Reel
MC79M05BT			TO-220	50 Units / Rail
MC79M05BTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M05CDT			DPAK	75 Units / Rail
MC79M05CDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M05CDTRK			DPAK	2500 Units / Reel
MC79M05CDTRKG		$T_J = 0$ °C to +125°C	DPAK (Pb-Free)	2500 Units / Reel
MC79M05CT			TO-220	50 Units / Rail
MC79M05CTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M08BDT			DPAK	75 Units / Rail
MC79M08BDTRK			DPAK	2500 Units / Reel
MC79M08BDTRKG		T _J = -40°C to +125°C	DPAK (Pb-Free)	2500 Units / Reel
MC79M08BT			TO-220	50 Units / Rail
MC79M08BTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M08CDT	4.0%	4.0%	DPAK	75 Units / Rail
MC79M08CDTG	,		DPAK (Pb-Free)	75 Units / Rail
MC79M08CDTRK			DPAK	2500 Units / Reel
MC79M08CDTRKG		$T_J = 0$ °C to +125°C	DPAK (Pb-Free)	2500 Units / Reel
MC79M08CT			TO-220	50 Units / Rail
MC79M08CTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M12BDT			DPAK	75 Units / Rail
MC79M12BDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M12BDTRK			DPAK	2500 Units / Reel
MC79M12BDTRKG		$T_J = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-Free)	2500 Units / Reel
MC79M12BT			TO-220	50 Units / Rail
MC79M12BTG			TO-220 (Pb-Free)	50 Units / Rail
MC79M12CDT			DPAK	75 Units / Rail
MC79M12CDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M12CDTRK			DPAK	2500 Units / Reel
MC79M12CDTRKG		$T_J = 0$ °C to +125°C	DPAK (Pb-Free)	2500 Units / Reel
MC79M12CT			TO-220	50 Units / Rail
			TO-220	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ORDERING INFORMATION

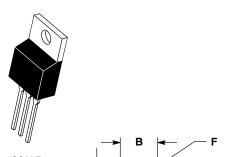
Device	Output Voltage Tolerance	Operating Temperature Range	Package	Shipping†
MC79M15BDT			DPAK	75 Units / Rail
MC79M15BDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M15BDTRK			DPAK	2500 Units / Reel
MC79M15BDTRKG		$T_J = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-Free)	2500 Units / Reel
MC79M15BT			TO-220	50 Units / Rail
MC79M15BTG	4.00/		TO-220 (Pb-Free)	50 Units / Rail
MC79M15CDT	4.0%		DPAK	75 Units / Rail
MC79M15CDTG			DPAK (Pb-Free)	75 Units / Rail
MC79M15CDTRK			DPAK	2500 Units / Reel
MC79M15CDTRKG		$T_J = 0^{\circ}C \text{ to } +125^{\circ}C$	DPAK (Pb-Free)	2500 Units / Reel
MC79M15CT			TO-220	50 Units / Rail
MC79M15CTG			TO-220 (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®





TO-220, SINGLE GAUGE CASE 221AB-01 **ISSUE A**

DATE 16 NOV 2010

- NOTES:

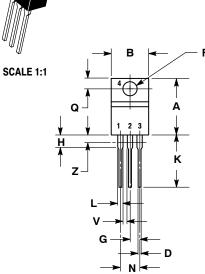
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

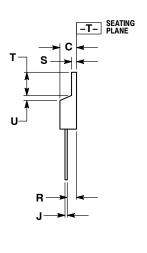
 2. CONTROLLING DIMENSION: INCHES.

 3. DIMENSION 2 DEFINES A ZONE WHERE ALL BODY AND LEAD INREGULARTIES ARE ALLOWED.

 4. PRODUCT SHIPPED PRIOR TO 2008 HAD DIMENSIONS S = 0.045 0.055 INCHES (1.143 1.397 MM)

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.020	0.024	0.508	0.61
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04





STYLE 1:		STYLE 2:		STYLE 3:	
PIN 1.	BASE	PIN 1.	BASE	PIN 1.	CATHODE
2.	COLLECTOR	2.	EMITTER	2.	ANODE
3.	EMITTER	3.	COLLECTOR	3.	GATE
4.	COLLECTOR	4.	EMITTER	4.	ANODE
STYLE 5:		STYLE 6:		STYLE 7:	
PIN 1.	GATE	PIN 1.	ANODE	PIN 1.	CATHODE
2.	DRAIN	2.	CATHODE	2.	ANODE
3.	SOURCE	3.	ANODE	3.	CATHODE
4.	DRAIN	4.	CATHODE	4.	ANODE
STYLE 9:		STYLE 10:		STYLE 11:	
PIN 1.	GATE	PIN 1.	GATE	PIN 1.	DRAIN
2.	COLLECTOR	2.	SOURCE	2.	SOURCE
3.	EMITTER	3.	DRAIN	3.	GATE
4.	COLLECTOR	4.	SOURCE	4.	SOURCE

3.	MAIN TERMINAL 1 MAIN TERMINAL 2 GATE MAIN TERMINAL 2
2. 3.	CATHODE ANODE EXTERNAL TRIP/DELAY ANODE

DOCUMENT NUMBER:	98AON23085D	Electronic versions are uncontrolled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED	, ,
DESCRIPTION:	TO-220, SINGLE GAUGE		PAGE 1 OF 1

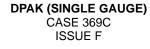
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ROTATED 90° CW

STYLE 1:

STYLE 2:





DATE 21 JUL 2015

- IOTES. 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCHES. 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-

- MENSIONS b3, L3 and Z.

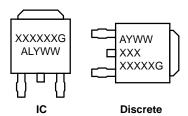
 Jimensions b And E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.

 MENSIONS D AND E ARE DETERMINED AT THE
- OUTERMOST EXTREMES OF THE PLASTIC BODY.

 6. DATUMS A AND B ARE DETERMINED AT DATUM
- 7. OPTIONAL MOLD FEATURE.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.028	0.045	0.72	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
е	0.090 BSC		2.29 BSC	
Н	0.370	0.410	9.40 10.4	
L	0.055	0.070	1.40	1.78
L1	0.114 REF		2.90 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4		0.040		1.01
Z	0.155		3.93	

GENERIC MARKING DIAGRAM*



XXXXXX = Device Code = Assembly Location Α = Wafer Lot L

Υ = Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking.

В L3 Ζ Ո DETAIL A NOTE 7 **BOTTOM VIEW** Cb2 е SIDE VIEW | \oplus | 0.005 (0.13) lacktriangle C **TOP VIEW** Z Ħ L2 GAUGE C SEATING PLANE **BOTTOM VIEW** Α1 ALTERNATE CONSTRUCTIONS **DETAIL A**

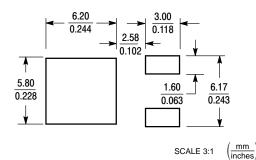
3. EMITTER	3. SOURCE	 ANODE CATHODE 	3. GATE	3. CATHODE
4. COLLECTOR	4. DRAIN		4. ANODE	4. ANODE
3. GATE 3. EMI	LECTOR 2. TTER 3.	N/C PIN CATHODE ANODE	E 9: 1. ANODE 2. CATHODE 3. RESISTOR ADJUST 4. CATHODE	STYLE 10: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. ANODE

STYLE 4:

STYLE 5:

STYLE 3:

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DESCRIPTION:	DPAK SINGLE GAUGE SURFACE MOU	PAGE 1 OF 2		
NEW STANDARD:	REF TO JEDEC TO-252	"CONTROLLED COPY" in red.		
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document Repository. Prin versions are uncontrolled except when stamped		
DOCUMENT NUMBER:	98AON10527D	Electronic versions are uncontrolle	'	



DOCUMENT	NUMBER:
98AON10527	7D

PAGE 2 OF 2

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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY L. GAN	24 SEP 2001
Α	ADDED STYLE 8. REQ. BY S. ALLEN.	06 AUG 2008
В	ADDED STYLE 9. REQ. BY D. WARNER.	16 JAN 2009
С	ADDED STYLE 10. REQ. BY S. ALLEN.	09 JUN 2009
D	RELABELED DRAWING TO JEDEC STANDARDS. ADDED SIDE VIEW DETAIL A. CORRECTED MARKING INFORMATION. REQ. BY D. TRUHITTE.	29 JUN 2010
E	ADDED ALTERNATE CONSTRUCTION BOTTOM VIEW. MODIFIED DIMENSIONS b2 AND L1. CORRECTED MARKING DIAGRAM FOR DISCRETE. REQ. BY I. CAMBALIZA.	06 FEB 2014
F	ADDED SECOND ALTERNATE CONSTRUCTION BOTTOM VIEW. REQ. BY K. MUSTAFA.	21 JUL 2015

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