Rectifier Diode Silicon Diffused Type

CRG05

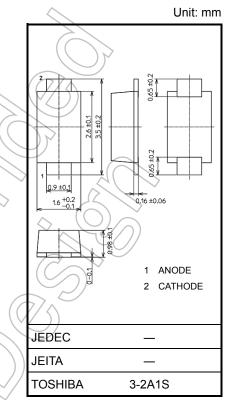
○ General Power Supply Rectification

- Repetitive peak reverse voltage $: V_{RRM} = 800 V$
- Average forward current $I_{\rm F}$ (AV) = 1.0 A
- Peak forward voltage $: V_{FM} = 1.2 V (max)$
- Small, thin package suitable for high-density board assembly Toshiba Nickname: "S-FLATTM"

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive Peak Reverse Voltage	V _{RRM}	800	$\nabla \mathbf{y} \wedge \mathbf{y}$
Average Forward Current	IF (AV)	1.0 (Note1)	$\langle A \rangle$
Non-repetitive peak forward surge current	IFSM	15 (50Hz)	A
Junction Temperature	Тј	-40 to 150	°C
Storage Temperature Range	T _{stg}	-40 to 150	°C

Note 1: Ta = 54°C Device mounted on a ceramic board Board size : 50 mm \times 50 mm Soldering land size : 2 mm \times 2 mm Board thickness : 0.64 mm Rectangular waveform : $\alpha = 180^{\circ}$



Weight: 0.013 g (typ.)

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

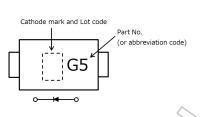
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	VFM(1)	I _{FM} = 0.1 A (Pulse test)	_	0.86	_	V
Peak forward voltage	VFM(2)	J _{FM} = 0.7 A (Pulse test)	_	1.0	_	V
	VFM(3)	I _{FM} = 1.0 A (Pulse test)	_	1.05	1.2	V
Repetitive peak reverse current	IRRM	V _{RRM} = 800 V (Pulse test)		_	10	μA
Thermal resistance	$\begin{array}{llllllllllllllllllllllllllllllllllll$		—	65	°C/W	
(junction to ambient)	R _{th} (j-a)	$\begin{array}{llllllllllllllllllllllllllllllllllll$	_	_	130	0/00
Thermal resistance (junction to lead)	R _{th (j-l)}	—	_	_	20	°C/W

Start of commercial production 2004-08

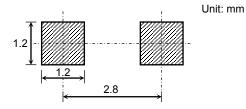
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Marking

Abbreviation Code	Part No.		
G5	CRG05		



Land pattern dimensions for reference only

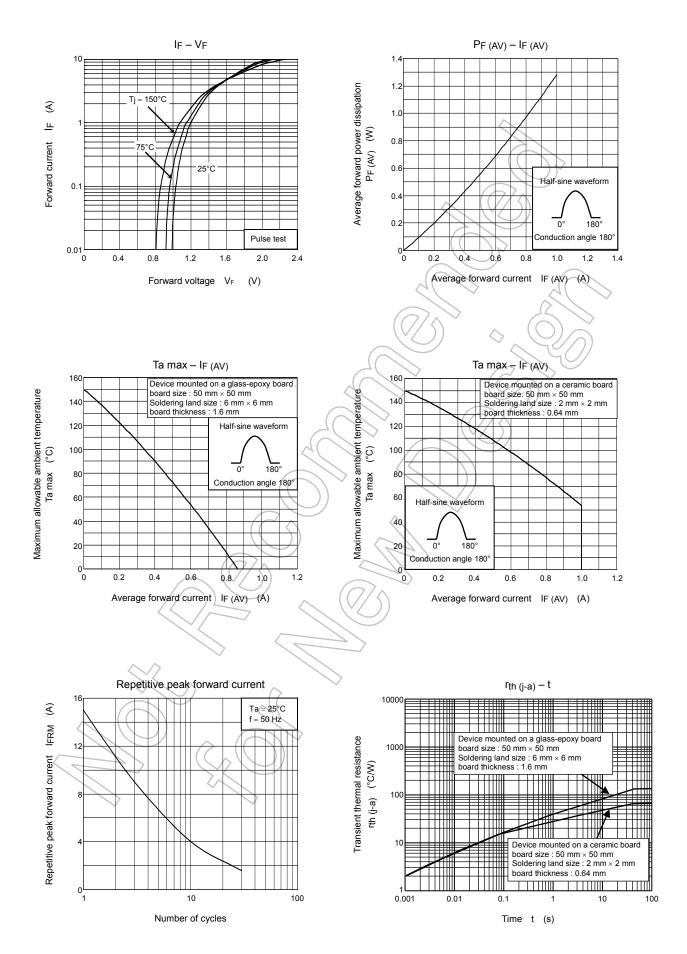


Handling Precaution

- 1) The absolute maximum ratings are rated values and must not be exceeded during operation, even for an instant. The followings are the general derating methods that we recommend when you design a circuit with a device.
 - V_{RRM}: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of V_{RRM} for a DC circuit and be no greater than 50% of that of V_{RRM} for an AC circuit. V_{RRM} has a temperature coefficient of 0.1%^oC. Take this temperature coefficient into account designing a device at low temperature.
 - I_{F(AV)}: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of I_{F(AV)} and Tj be below 120°C. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Ta max I_{F(AV)} curve.
 - IFSM: This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.
 - Tj : We recommend that a device be used at Tj below 120°C under the worst load and heat radiation conditions.
- 2) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.
- 3) For other design considerations, see the Toshiba website.



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