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# ISL9V3040D3S / ISL9V3040S3S / ISL9V3040P3 / ISL9V3040S3

## EcoSPARK® 300mJ, 400V, N-Channel Ignition IGBT

#### **General Description**

The ISL9V3040D3S, ISL9V3040S3S, ISL9V3040P3, and ISL9V3040S3 are the next generation ignition IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D²-Pak (TO-263), and TO-262 and TO-220 plastic packages. This device is intended for use in automotive ignition circuits, specifically as a coil driver. Internal diodes provide voltage clamping without the need for external components.

**EcoSPARK**® devices can be custom made to specific clamp voltages. Contact your nearest On Semiconductor sales office for more information.

Formerly Developmental Type 49362

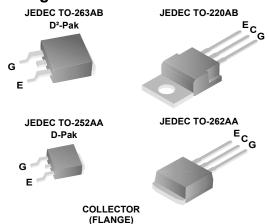
#### **Applications**

- · Automotive Ignition Coil Driver Circuits
- · Coil- On Plug Applications

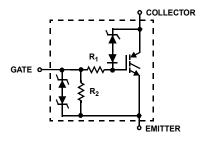
#### **Features**

- · Space saving D-Pak package availability
- SCIS Energy = 300mJ at T<sub>J</sub> = 25°C
- · Logic Level Gate Drive

#### **Package**



### **Symbol**



#### **Device Maximum Ratings** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	430		
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10 mA)	24	V	
E <sub>SCIS25</sub>	At Starting $T_J = 25$ °C, $I_{SCIS} = 14.2A$ , $L = 3.0$ mHy	300	mJ	
E <sub>SCIS150</sub>	At Starting T <sub>J</sub> = 150°C, I <sub>SCIS</sub> = 10.6A, L = 3.0 mHy	170	mJ	
I <sub>C25</sub>	Collector Current Continuous, At T <sub>C</sub> = 25°C, See Fig 9	21	Α	
I <sub>C110</sub>	Collector Current Continuous, At T <sub>C</sub> = 110°C, See Fig 9	17	Α	
$V_{GEM}$	Gate to Emitter Voltage Continuous	±10	V	
P <sub>D</sub>	Power Dissipation Total T <sub>C</sub> = 25°C	150	W	
	Power Dissipation Derating T <sub>C</sub> > 25°C	1.0	W/°C	
TJ	Operating Junction Temperature Range	-40 to 175	°C	
T <sub>STG</sub>	Storage Junction Temperature Range	-40 to 175	°C	
TL	Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)	300	°C	
T <sub>pkg</sub>	Max Lead Temp for Soldering (Package Body for 10s)	260	°C	
ESD	Electrostatic Discharge Voltage at 100pF, 1500Ω	4	kV	

Package	Mark	ing and Ordering	j Inf	ormatio	n						
Device Marking		Device	Package		Reel Size		Tape Width		Quantity		
V3040D		ISL9V3040D3ST	TC	D-252AA		330mm	10	3mm	2500		
V3040S		ISL9V3040S3ST	TO	D-263AB		330mm	24	4mm	8	800	
V3040P ISL9V3040P3 T		TC	D-220AB Tube		Tube	N/A		50			
V3040S ISL9V3040S3 To		TC	D-262AA Tube		Tube	N/A		50			
V3040D ISL9V3040D3S		TC	D-252AA -		Tube	N/A		75			
V3040S ISL9V3040S3S TO			O-263AB Tube			N/A		50			
Electrica	al Cha	racteristics T <sub>A</sub> = 25	°C un	less otherwis	e no	oted					
Symbol		Parameter	Test Conditions			Min	Тур	Max	Units		
Off State (	Charact	eristics									
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage			$I_C = 2\text{mA}, V_{GE} = 0,$ $R_G = 1\text{K}\Omega$ , See Fig. 15			370	400	430	V	
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage			$T_J$ = -40 to 150°C $I_C$ = 10mA, $V_{GE}$ = 0, $R_G$ = 0, See Fig. 15			390	420	450	V	
		$T_J = -40 \text{ to } 150^{\circ}\text{C}$									
BV <sub>ECS</sub>	Emitter to Collector Breakdown Voltage			$I_{C}$ = -75mA, $V_{GE}$ = 0V, $T_{C}$ = 25°C			30	-	-	V	
$BV_{GES}$		te to Emitter Breakdown Voltage		$I_{GES} = \pm 2m$		,	±12	±14	-	V	
I <sub>CER</sub>	Collector to Emitter Leakage Current			$V_{CER} = 250V$ ,		$T_C = 25^{\circ}C$	-	-	25	μA	
				$R_G$ = 1KΩ, See Fig. 11		T <sub>C</sub> = 150°C	-	-	1	mA	
I <sub>ECS</sub>	Emitter to Collector Leakage Current			V <sub>EC</sub> = 24V, See Fig. 11			-	-	1	mA	
						T <sub>C</sub> = 150°C	-	-	40	mA	
R <sub>1</sub>	Series Gate Resistance						-	70	-	Ω	
R <sub>2</sub>	Gate to I	Emitter Resistance					10K	-	26K	Ω	
On State (	Charact	eristics									
V <sub>CE(SAT)</sub>	Collector	ector to Emitter Saturation Voltage		$I_C = 6A$ , $T_C = 25$ °C, $V_{GE} = 4V$ See Fig. 3		-	1.25	1.60	V		
$V_{CE(SAT)}$	Collector	ector to Emitter Saturation Voltage		I <sub>C</sub> = 10A, V <sub>GE</sub> = 4.5V	T <sub>C</sub> = 150°C, See Fig. 4		-	1.58	1.80	V	
V <sub>CE(SAT)</sub>	Collector	Collector to Emitter Saturation Voltage			I <sub>C</sub> = 15A, V <sub>GE</sub> = 4.5V		-	1.90	2.20	V	
Dynamic (	Charact	eristics									
Q <sub>G(ON)</sub>	Gate Ch	Gate Charge		I <sub>C</sub> = 10A, V <sub>CE</sub> = 12V, V <sub>GE</sub> = 5V, See Fig. 14			-	17	-	nC	
V <sub>GE(TH)</sub>	Gate to Emitter Threshold Voltage			$I_C = 1.0 \text{mA},$		T <sub>C</sub> = 25°C	1.3	-	2.2	V	
0_()				V <sub>CE</sub> = V <sub>GE,</sub> See Fig. 10		T <sub>C</sub> = 150°C	0.75	-	1.8	V	
V <sub>GEP</sub>	Gate to I	Emitter Plateau Voltage		I <sub>C</sub> = 10A, V <sub>0</sub>		12V	-	3.0	-	V	
Switching	Charac	cteristics									
t <sub>d(ON)R</sub>	Current	Turn-On Delay Time-Resis	tive	V <sub>CE</sub> = 14V, R <sub>L</sub> = 1		: 1Ω,	-	0.7	4	μs	
t <sub>rR</sub>	Current	Rise Time-Resistive		$V_{GE} = 5V, R$ $T_{J} = 25^{\circ}C, S$	G =	1ΚΩ	-	2.1	7	μs	
t <sub>d(OFF)L</sub>	Current	Turn-Off Delay Time-Induc	tive		L = 500µHy,		-	4.8	15	μs	
t <sub>fL</sub>	Current	Fall Time-Inductive		$V_{GE}$ = 5V, $R_G$ = 1K $\Omega$ T <sub>J</sub> = 25°C, See Fig. 12		-	2.8	15	μs		
SCIS	Self Clar	mped Inductive Switching	$T_J$ = 25°C, L = 3.0 mHy, $R_G$ = 1K $\Omega$ , $V_{GE}$ = 5V, See Fig. 1 & 2			-	-	300	mJ		
Thermal C	haracte	eristics									
$R_{\theta JC}$	Thermal	Resistance Junction-Case	9	All package	s	_	-	-	1.0	°C/V	

#### **Typical Performance Curves**

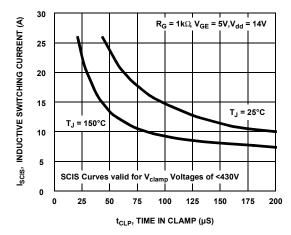


Figure 1. Self Clamped Inductive Switching Current vs Time in Clamp

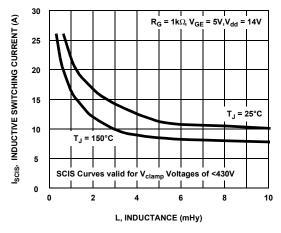


Figure 2. Self Clamped Inductive Switching Current vs Inductance

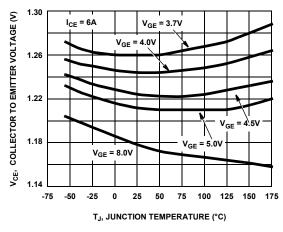


Figure 3. Collector to Emitter On-State Voltage vs Junction Temperature

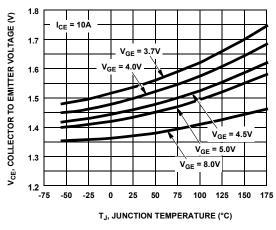


Figure 4. Collector to Emitter On-State Voltage vs Junction Temperature

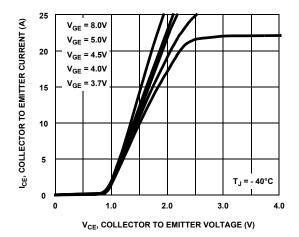


Figure 5. Collector to Emitter On-State Voltage vs Collector Current

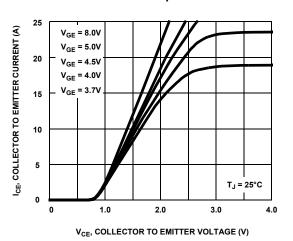


Figure 6. Collector to Emitter On-State Voltage vs Collector Current

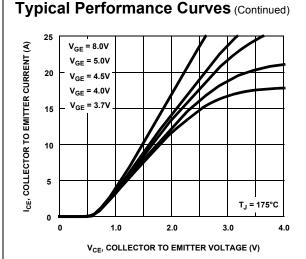
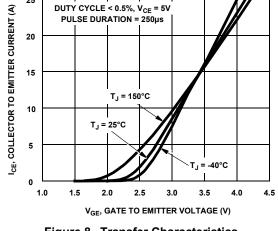


Figure 7. Collector to Emitter On-State Voltage vs Collector Current



25

Figure 8. Transfer Characteristics

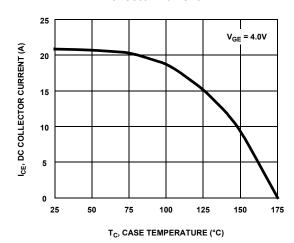


Figure 9. DC Collector Current vs Case Temperature

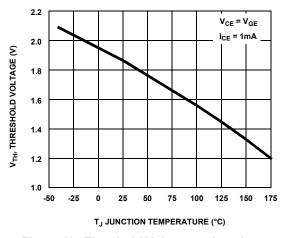


Figure 10. Threshold Voltage vs Junction Temperature

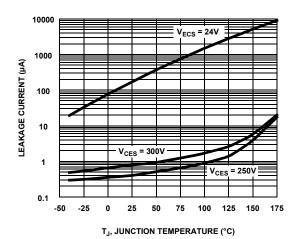


Figure 11. Leakage Current vs Junction Temperature

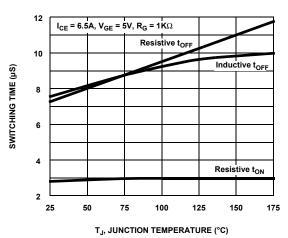
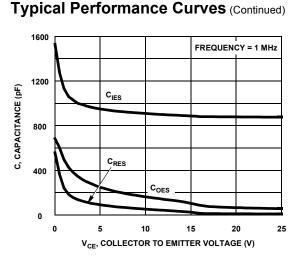


Figure 12. Switching Time vs Junction Temperature



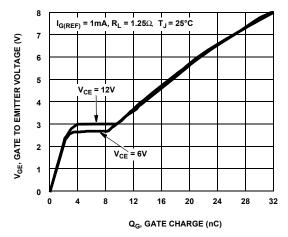


Figure 13. Capacitance vs Collector to Emitter Voltage

Figure 14. Gate Charge

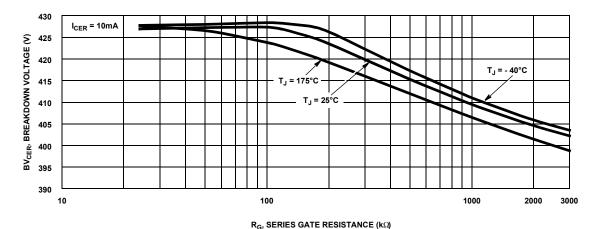


Figure 15. Breakdown Voltage vs Series Gate Resistance

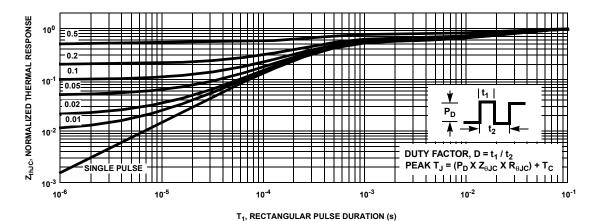


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

## **Test Circuit and Waveforms**

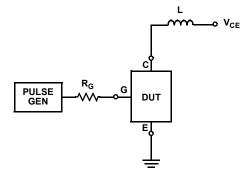


Figure 17. Inductive Switching Test Circuit

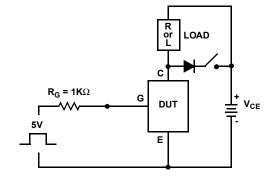


Figure 18.  $t_{ON}$  and  $t_{OFF}$  Switching Test Circuit

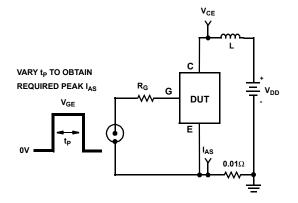


Figure 19. Energy Test Circuit

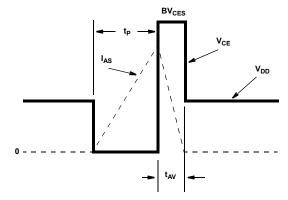


Figure 20. Energy Waveforms

#### SPICE Thermal Model REV 7 March 2002 JUNCTION ISL9V3040D3S / ISL9V3040S3S / ISL9V3040P3 / ISL9V3040S3 CTHERM1 th 6 2.1e -3 CTHERM2 6 5 1.4e -1 CTHERM3 5 4 7.3e -3 CTHERM4 4 3 2.1e -1 RTHERM1 CTHERM1 CTHERM5 3 2 1.1e -1 CTHERM6 2 tl 6.2e +6 RTHERM1 th 6 1.2e -1 6 RTHERM2 6 5 1.9e -1 RTHERM3 5 4 2.2e -1 RTHERM4 4 3 6.0e -2 RTHERM2 CTHERM2 RTHERM5 3 2 5.8e -2 RTHERM6 2 tl 1.6e -3 SABER Thermal Model 5 SABER thermal model ISL9V3040D3S / ISL9V3040S3S / ISL9V3040P3 / ISL9V3040S3 RTHERM3 CTHERM3 template thermal\_model th tl thermal\_c th, tl 4 ctherm.ctherm1 th 6 = 2.1e - 3ctherm.ctherm2 6 5 = 1.4e -1 ctherm.ctherm3 5 4 = 7.3e -3 ctherm.ctherm4 4 3 = 2.2e -1 RTHERM4 CTHERM4 ctherm.ctherm5 3 2 =1.1e -1 ctherm.ctherm6 2 tl = 6.2e +6 rtherm.rtherm1 th 6 = 1.2e -1 3 rtherm.rtherm2 6 5 = 1.9e - 1rtherm.rtherm3 5 4 = 2.2e -1 rtherm.rtherm4 4 3 = 6.0e -2 RTHERM5 CTHERM5 rtherm.rtherm5 3 2 = 5.8e -2 rtherm.rtherm6 2 tl = 1.6e -3 2 RTHERM6 CTHERM6 CASE

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