



1A, 50V - 1000V Glass Passivated Bridge Rectifiers

FEATURES

- Ideal for automated placement
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21







MECHANICAL DATA

Case: Molded plastic body

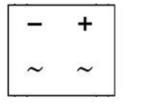
Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020 Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test Polarity: Polarity as marked on the body

Weight: 0.36 g (approximately)



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		DBLS	DBLS	DBLS	DBLS	DBLS	DBLS	DBLS	UNIT
PARAMETER	SYMBOL	101G	102G	103G	104G	105G	106G	107G	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}			•	1				Α
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}			40			3	0	Α
Rating for fusing (t<8.3ms)	l ² t			6.6			3	.7	A ² s
Maximum instantaneous forward voltage (Note 1) I _F = 1 A	V _F				1.1				V
Maximum reverse current @ rated V_R T_J =25°C T_J =125°C	I _R				2 100				μΑ
Typical junction capacitance per leg (Note 2)	CJ	25						pF	
Typical thermal resistance	$R_{ heta JL} \ R_{ heta JA}$	15 40					°C/W		
Operating junction temperature range	TJ	- 55 to +150						°C	
Storage temperature range	T _{STG}	- 55 to +150						°C	

Note 1: Pulse Test with PW=300µs,1% Duty Cycle

Note 2: Measure at 1.0MHz and Applied Reverse Voltage of 4.0 Volts D.C.



ORDERING INFORMATION					
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX ^(*)	PACKAGE	PACKING
DBLS10xG	Н	C1	G	DBLS	50 / TUBE
(Note 1)	RD		DBLS	1,500 / 13" Paper reel	

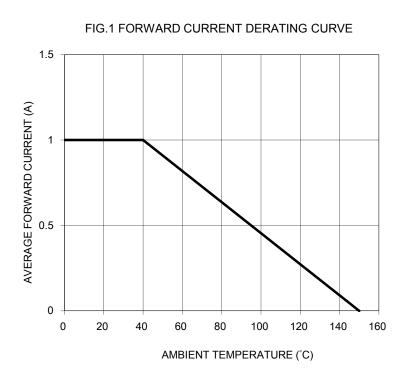
Note 1: "x" defines voltage from 50V (DBLS101G) to 1000V (DBLS107G)

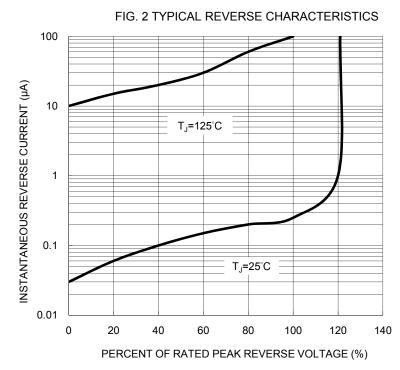
^{*:} Optional available

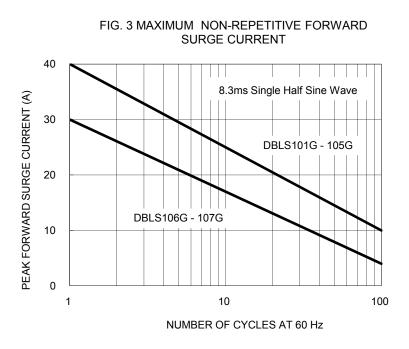
EXAMPLE						
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
DBLS107GHRDG	DBLS107G	Ħ	RD	G	AEC-Q101 qualified Green compound	

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)







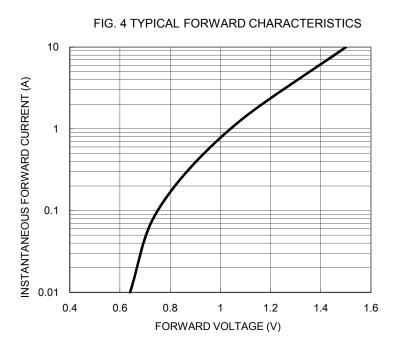
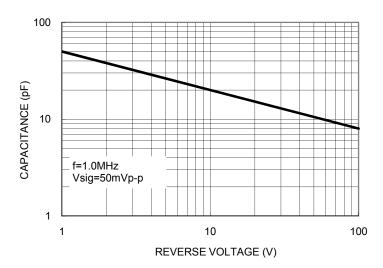
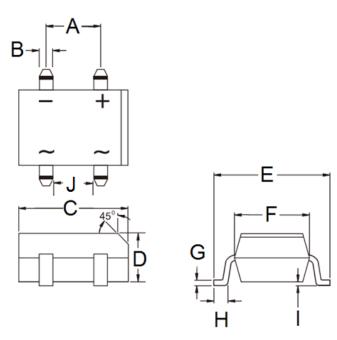




FIG. 5 TYPICAL JUNCTION CAPACITANCE

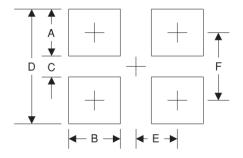


PACKAGE OUTLINE DIMENSIONS DBLS



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min Max		Min	Max	
Α	5.00	5.20	0.197	0.205	
В	1.02	1.20	0.040	0.047	
С	8.13	8.51	0.320	0.335	
D	2.40	2.60	0.094	0.102	
Е	9.80	10.30	0.386	0.406	
F	6.20	6.50	0.244	0.256	
G	0.22	0.33	0.009	0.013	
Н	1.02	1.53	0.040	0.060	
Ī	0.076	0.33	0.003	0.013	
J	3.90	4.10	0.154	0.161	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	2.3	0.091
В	1.3	0.051
С	6.9	0.272
D	11.5	0.453
Е	2.6	0.102
F	9.2	0.362

MARKING DIAGRAM



P/N = Specific Device Code G = Green Compound

YW = Date Code F = Factory Code

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DBLS101G DBLS102G DBLS103G DBLS104G DBLS105G DBLS106G DBLS107G DBLS104GHRDG

DBLS107GHRDG DBLS102GHRDG DBLS103G RDG DBLS102G RDG DBLS103GHRDG DBLS107G RDG

DBLS104G RDG DBLS105GHRDG DBLS105G RDG DBLS101G RDG DBLS106GHRDG DBLS101GHRDG

DBLS106G RDG DBLS102G RD DBLS104G RD DBLS103G RD DBLS106G RD DBLS105G RD DBLS101G RD

DBLS107G RD DBLS106G C1G DBLS107G C1G DBLS105G C1G