ne<mark>x</mark>peria

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Should be replaced with:

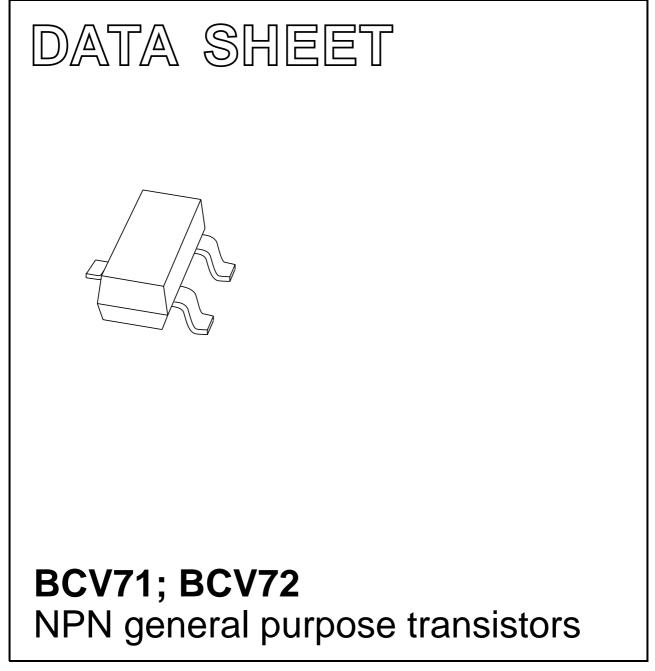
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If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 Mar 11 1999 Apr 08



NPN general purpose transistors

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 60 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

NPN transistor in a SOT23 plastic package.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BCV71	K7*
BCV72	K8*

Note

1. * = p : Made in Hong Kong.

* = t : Made in Malaysia.

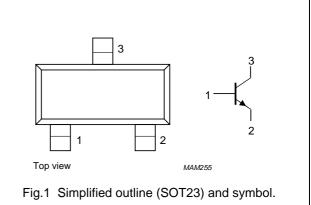
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	80	V
V _{CEO}	collector-emitter voltage	open base; I _C = 2 mA	-	60	V
V _{EBO}	emitter-base voltage	open collector	-	5	V
I _C	collector current (DC)		-	100	mA
I _{CM}	peak collector current		-	200	mA
I _{BM}	peak base current		-	200	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	-	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C



PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



BCV71; BCV72

NPN general purpose transistors

BCV71; BCV72

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W	

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

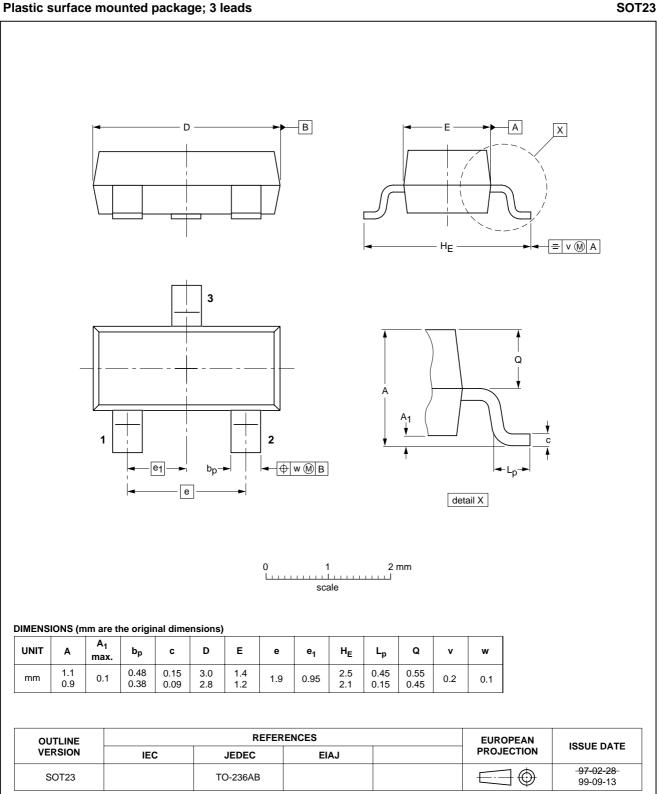
 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 20 V	-	-	100	nA
		I _E = 0; V _{CB} = 20 V; T _j = 100 °C	-	_	10	μA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	-	_	100	nA
h _{FE}	DC current gain	$I_{C} = 10 \ \mu A; V_{CE} = 5 \ V$				
	BCV71		-	90	-	
	BCV72		-	150	_	
	DC current gain	$I_{C} = 2 \text{ mA}; V_{CE} = 5 \text{ V}$				
	BCV71		110	_	220	
	BCV72		200	_	450	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	-	120	250	mV
		I _C = 50 mA; I _B = 2.5 mA	-	210	_	mV
	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	-	750	_	mV
		I _C = 50 mA; I _B = 2.5 mA	-	850	_	mV
V _{BE}	base-emitter voltage	I _C = 2 mA; V _{CE} = 5 V	550	-	700	mV
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	-	2.5	_	pF
f _T	transition frequency	I _C = 10 mA; V _{CE} = 5 V; f = 100 MHz	100	_	_	MHz
F	noise figure	$I_C = 200 \ \mu$ A; V _{CE} = 5 V; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz	_	-	10	dB

BCV71; BCV72

NPN general purpose transistors

PACKAGE OUTLINE



NPN general purpose transistors

BCV71; BCV72

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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