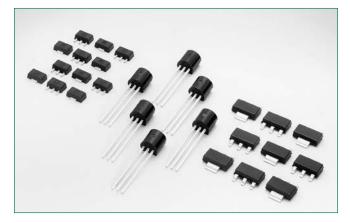
Thyristor Datasheet

LX8 Series EV Series 0.8 Amp Sensitive Triacs

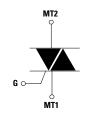
RoHS



Main Features

Symbol	Value	Unit
I _{T(RMS)}	0.8	А
V _{DRM} /V _{RRM}	400 to 600	V
I _{GT}	3 to 5	mA

Schematic Symbol



Description

solid state switch series offering direct interface to microprocessor drivers in economical TO-92 and surface mount packages. The die voltage blocking junctions are glass-passivated to ensure long term reliability and parametric stability.

Features & Benefits

- RoHS compliant and Halogen-Free
- Blocking voltage (VDRM) capability
 up to 600V
- Surge capability > 9.5Amps

Applications

The LX8 EV Series is especially designed for low current applications such as heating controls in hair care products, as well as replacement of mechanical switch contacts where long life is required.

- Static dv/dt > 10 Volts/µsec
- Thru hole and surface mount packages



Absolute Maximum Ratings

Symbol	Parameter	Value	Unit		
		TO-92	$T_c = 50^{\circ}C$		
I _{T(RMS)}	RMS on-state current (full sine wave)	SOT-89	$T_c = 60^{\circ}C$	0.8A	А
		SOT-223	$T_{L} = 90^{\circ}C$		
1	Non repetitive surge peak on-state current	-	F = 50 Hz	8.0	А
I _{TSM}	(Single cycle, T_J initial = 25°C)	-	F = 60 Hz	9.5	A
l²t	I²t Value for fusing	$t_p = 10 \text{ ms}$	F = 50 Hz	0.32	A ² s
1-1	int value for rushing	$t_{p} = 8.3 \text{ ms}$	F = 60 Hz	0.37	A-2
di/dt	Critical rate of rise of on-state current ${\rm I_{g}}$ = 2 x ${\rm I_{gT}}$		$T_J = 110^{\circ}C$	20	A/µs
I _{gtm}	Peak gate current $t_p = 10 \ \mu s$ $T_J = 110^{\circ}C$		1	А	
P _{G(AV)}	Average gate power dissipation $T_J = 110^{\circ}C$			0.1	VV
T _{stg}	Storage junction temperat	-40 to 150	°C		
Tj	Operating junction tempera	ature range		-40 to 110	°C

Electrical Characteristics (TJ = 25°C, unless otherwise specified)

Cumula al	ymbol Description Test Conditions Quadrant	Limit	Value		Unit		
Symbol	Description	lest Conditions	Quadrant	Limit	LX803xy	LX807xy	Unit
I _{gt}	DC Gate Trigger Current	$V_{\rm D} = 12V$	I – II – III, IV	MAX.	3 5	5 7	mA
V _{GT}	DC Gate Trigger Voltage	$R_L = 60 \Omega$	ALL	MAX.	1.3	1.3	V
I _H	Holding Current	Gate Open		MAX.	5	5	mA
dv/dt	Critical Rate-of-Rise of Off- State Voltage	$T_J = 110^{\circ}C, V_D = V_{DRM}$ Exponential WaveformGate Open		MIN.	10	10	V/µs
(dv/dt)c	Critical Rate-of-Rise of Commutating Voltage	(di/dt)c = 0.43A/ms T _J = 110°C		MIN.	1.5	1.5	V/µs
t _{gt}	Turn-On Time	$I_{g} = 150 \text{ C}$ $I_{g} = 25\text{mA}$ $PW = 15\mu\text{s}$ $I_{T} = 1.2\text{A} (\text{pk})$		MAX.	2.0	2.0	μs

Note: x = voltage, y = package

Static Characteristics (TJ = 25°C, unless otherwise specified)

Symbol	Description	Test Conditions	Limit	Value	Unit
V _{TM}	Peak On-State Voltage	I _{TM} = 1.13A (pk)	MAX	1.60	V
1	Off State Current Deals Depatitive	$V_{\rm D} = V_{\rm DRM} T_{\rm J} = 25^{\circ} \rm C$	MAX	5	μA
DRM	Off-State Current, Peak Repetitive	$V_{\rm D} = V_{\rm DRM} T_{\rm J} = 110^{\circ} \rm C$	IVIAA	100	μA

Thermal Resistances

Symbol	Description	Test Conditions		Value	Unit
			TO-92	60	
R _{th(JC)}	Junction to case (AC)	$I_{T} = 0.8A_{(RMS)}^{1}$	SOT-89	50	°C/W
			SOT-223	25	
	Junction to ambient $I_{T} = 0.8A_{(RMS)}^{1}$		TO-92	150	
$R_{th(j-a)}$		$I_{T} = 0.8A_{(BMS)}^{1}$	SOT-89	90	°C/W
			SOT-223	60	

1. 60Hz AC resistive load condition, 100% conduction.



Thyristor **Datasheet**

LX8 Series EV Series 0.8 Amp Sensitive Triacs

Figure 1: Definition of Quadrants

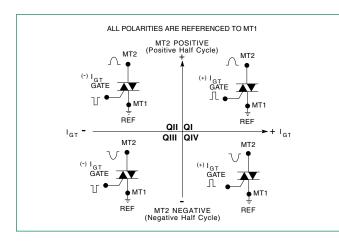


Figure 3: Normalized DC Holding Current vs. Junction Temperature

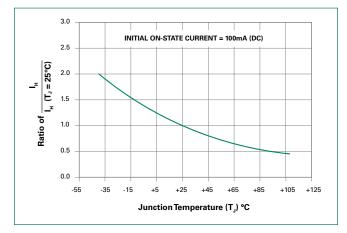


Figure 5: Power Dissipation (Typical) vs. RMS On-State Current

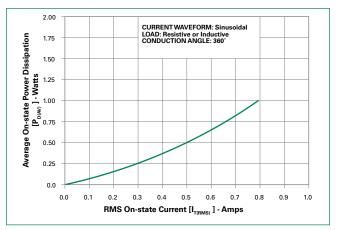


Figure 2: Normalized DC Gate Trigger Current for All Quadrants vs. Junction Temperature

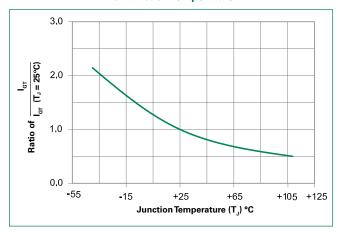


Figure 4: Normalized DC Gate Trigger Voltage for All Quadrants vs. Junction Temperature

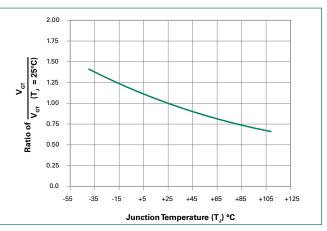


Figure 6: Maximum Allowable Case Temperature vs. On-State Current

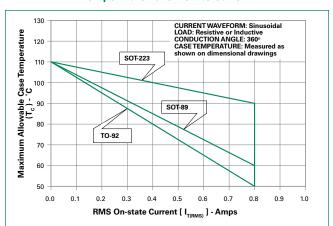
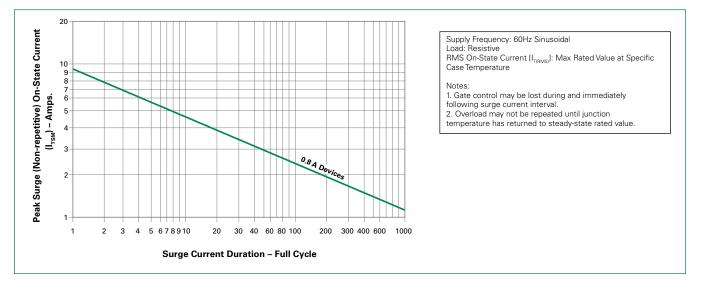
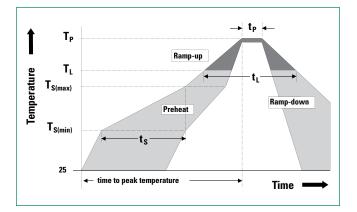


Figure 7: Surge Peak On-State Current vs. Number of Cycles



Soldering Parameters

Reflow Cond	lition	Pb — Free assembly	
	- Temperature Min (T _{s(min)})	150°C	
Pre Heat	- Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 – 180 secs	
Average ram	Average ramp up rate (Liquidus Temp) (T_L) to peak		
$T_{S(max)}$ to T_{L} -	5°C/second max		
Reflow	- Temperature (T _L) (Liquidus)	217°C	
nellow	-Time (min to max) (t _s)	60 – 150 seconds	
Peak Temper	rature (T _P)	260 ^{+0/-5} °C	
Time within	5°C of actual peak Temperature (t_p)	20 – 40 seconds	
Ramp-down	Ramp-down Rate		
Time 25°C to	Time 25°C to peak Temperature (T _P)		
Do not exce	Do not exceed		



Specifications and Conditions MIL-STD-750, M-1040, Cond A Applied Peak AC voltage

100 cycles; -40°C to +150°C; 15-min dwell-time

1008 hours; 320V - DC: 85°C; 85% rel humidity

MIL-STD-750, M-103, 1008 hours; 150°C

ANSI/J-STD-002, category 3, Test A

@ 110°C for 1008 hours MIL-STD-750, M-1051,

EIA / JEDEC, JESD22-A101

MIL-STD-750 Method 2031

MIL-STD-750, M-2036 Cond E

1008 hours; -40°C

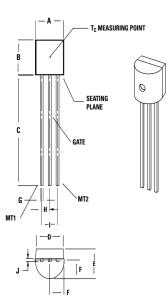
Physical Specifications

Terminal Finish	100% Matte Tin-plated.
Body Material	UL Recognized compound meeting flammability rating V-0.
Lead Material	Copper Alloy

Design Considerations

Careful selection of the correct component for the application's operating parameters and environment will go a long way toward extending the operating life of the Thyristor. Good design practice should limit the maximum continuous current through the main terminals to 75% of the component rating. Other ways to ensure long life for a power discrete semiconductor are proper heat sinking and selection of voltage ratings for worst case conditions. Overheating, overvoltage (including dv/dt), and surge currents are the main killers of semiconductors. Correct mounting, soldering, and forming of the leads also help protect against component damage.

Dimensions — TO-92 (E Package)



Dimensions	Inc	hes	Millin	neters
Dimensions	Min	Max	Min	Max
Α	0.175	0.205	4.450	5.200
В	0.170	0.210	4.320	5.330
С	0.500	-	12.70	-
D	0.135	-	3.430	-
E	0.125	0.165	3.180	4.190
F	0.080	0.105	2.040	2.660
G	0.016	0.021	0.407	0.533
н	0.045	0.055	1.150	1.390
I.	0.095	0.105	2.420	2.660
J	0.015	0.020	0.380	0.500

Reliability/Environmental Tests

Test

Temperature Cycling

High Temp Storage

Low-Temp Storage

Solderability

Lead Bend

Temperature/Humidity

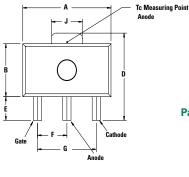
Resistance to Solder Heat

AC Blocking

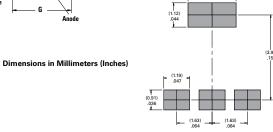


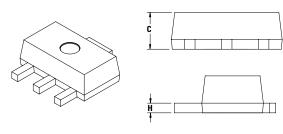
LX8 Series EV Series 0.8 Amp Sensitive Triacs

Dimensions – SOT-89



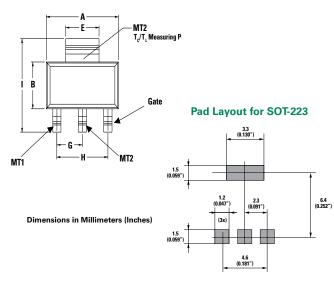
Pad Layout for SOT-89

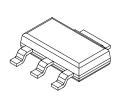




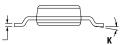
Dimension		Inches		r	Millimeters		
Dimension	Min	Тур	Max	Min	Тур	Max	
Α	0.173	—	0.181	4.40	—	4.60	
В	0.090	—	0.102	2.29	—	2.60	
С	0.055	—	0.063	1.40	—	1.60	
D	0.155	—	0.167	3.94	—	4.25	
E	0.035	—	0.047	0.89	—	1.20	
F	0.056	—	0.062	1.42	—	1.57	
G	0.115	—	0.121	2.92	—	3.07	
Н	0.014	—	0.017	0.35	—	0.44	
I	0.014	—	0.019	0.36	—	0.48	
J	0.064	—	0.072	1.62	—	1.83	

Dimensions – SOT-223









Dimensions		Inches		Ν	s	
Dimensions	Min	Тур	Max	Min	Тур	Мах
Α	0.248	0.256	0.264	6.30	6.50	6.70
В	0.130	0.138	0.146	3.30	3.50	3.70
С	—	—	0.071	—	—	1.80
D	0.001	—	0.004	0.02	—	0.10
E	0.114	0.118	0.124	2.90	3.00	3.15
F	0.024	0.027	0.034	0.60	0.70	0.85
G	—	0.090	—	—	2.30	—
Н	—	0.181	—	—	4.60	—
I.	0.264	0.276	0.287	6.70	7.00	7.30
J	0.009	0.010	0.014	0.24	0.26	0.35
к			10° M	VAX		



Thyristor Datasheet



Product Selector

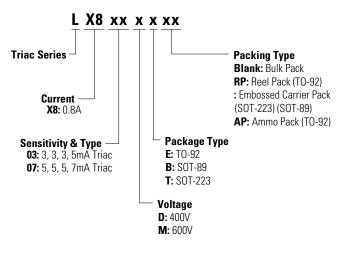
Deut Niemeleen	Malta an	Gate Sensitiv	Deskens	
Part Number	Voltage	1 – 11 – 111	IV	Package
LX803DE	400 V	3 mA	5 mA	TO-92
LX803ME	600 V	3 mA	5 mA	TO-92
LX803DT	400 V	3 mA	5 mA	SOT-223
LX803MT	600 V	3 mA	5 mA	SOT-223
LX807DE	400 V	5 mA	7 mA	TO-92
LX807ME	600 V	5 mA	7 mA	TO-92
LX807DT	400 V	5 mA	7 mA	SOT-223
LX807MT	600 V	5 mA	7 mA	SOT-223
LX807MB	600 V	5 mA	7 mA	SOT-89

Packing Options

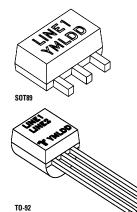
Part Number	Marking	Weight	Packing Mode	Base Quantity
LX8xxyE	LX8xxyE	0.217 g	Bulk	2500
LX8xxyEAP	LX8xxyE	0.217 g	Ammo Pack	2000
LX8xxyERP	LX8xxyE	0.217 g	Tape & Reel	2000
LX8xxyTRP	LX8xxyT	0.120 g	Tape & Reel	1000
LX8xxyBRP	8xx	0.053 g	Tape & Reel	1000

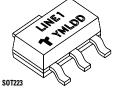
Note: xx = gate sensitivity, y = voltage

Part Numbering System



Part Marking System

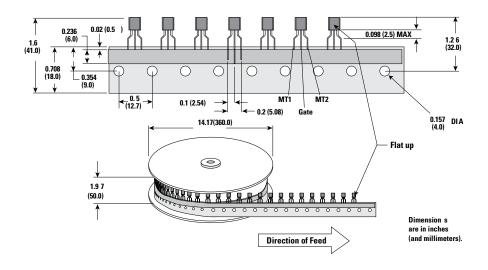




Line1 = Littelfuse Part Number Line2 = continuation...Littelfuse Part Number Y = Last Digit of Calendar Year M = Letter Month Code (A-L for Jan-Dec) L = Location Code DD = Calendar Date

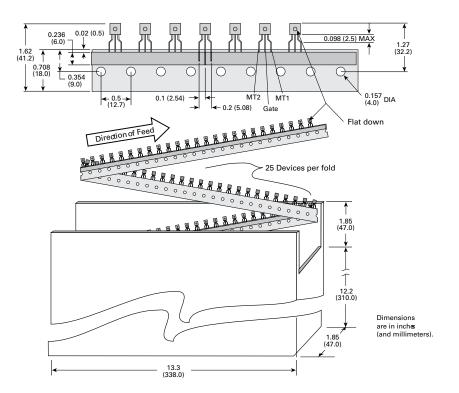
TO-92 (3-lead) Reel Pack (RP) Radial Leaded Specifications

Meets all EIA-468-C Standards



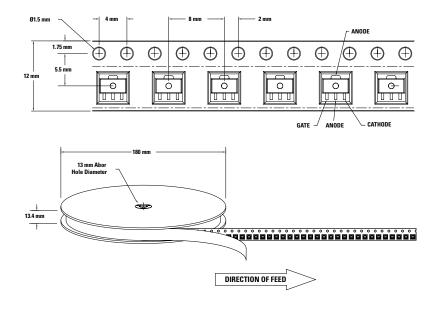
TO-92 (3-lead) Ammo Pack (AP) Radial Leaded Specifications

Meets all EIA-468-C Standards

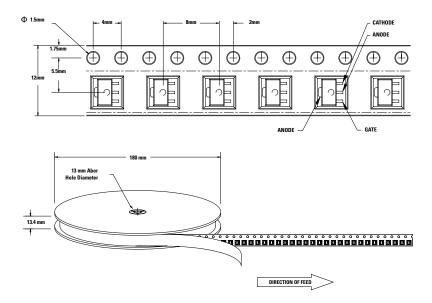




SOT-89 Reel Pack (RP) Specifications

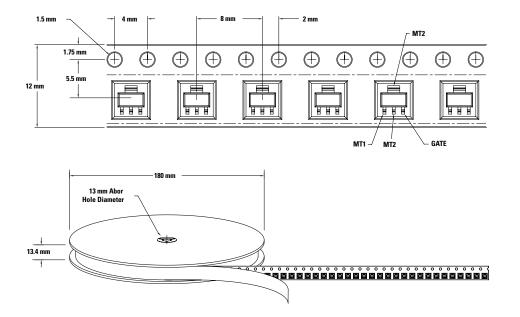


SOT-89 Reel Pack (RP1) Specifications





SOT-223 Reel Pack (RP) Specifications



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