

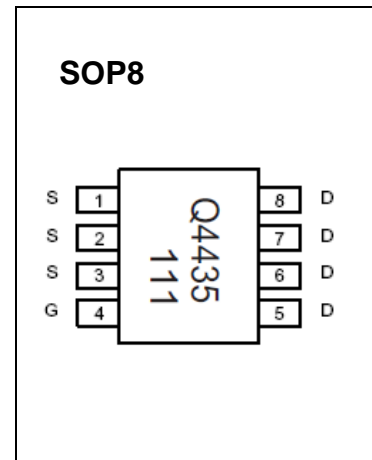
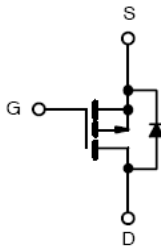


## SOP8 Plastic-Encapsulate MOSFETS

### CJQ4435 P-Channel MOSFET

#### APPLICATIONS

- Load Switches
- Battery Switch



#### Maximum ratings ( $T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current (t =10s) (note 1)	$I_D$	-9.1	A
Pulsed Drain Current	$I_{DM}$	-50	
Drain-Source Diode Forward Current (t =10s) (note 1)	$I_S$	-2	
Power Dissipation (t =10s)	$P_D$	1.4	W
Thermal Resistance from Junction to Ambient (t $\leq$ 10s) (note 1)	$R_{\theta JA}$	89	$^{\circ}\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^{\circ}\text{C}$
Storage Temperature	$T_{stg}$	-55 ~+150	

**Electrical characteristics (T<sub>a</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA
Gate body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-3	V
Drain-Source on-state Resistance (note 2)	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-9.1A			24	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6.9A			35	
Forward Transconductance (note 2)	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-9.1A	20			S
<b>Dynamic Characteristics (note 3)</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f =1MHz		1350		pF
Output Capacitance	C <sub>oss</sub>			215		
Reverse Transfer Capacitance	C <sub>rss</sub>			185		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-9.1A			50	nC
					25	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-9.1A		4		
Gate-Drain Charge	Q <sub>gd</sub>			7.5		
Gate Resistance	R <sub>g</sub>	f =1MHz		5.8		Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V, R <sub>L</sub> =15Ω I <sub>D</sub> =-1A, V <sub>GEN</sub> =-10V, R <sub>G</sub> =1Ω			15	ns
Rise Time	t <sub>r</sub>				15	
Turn-Off Delay Time	t <sub>d(off)</sub>				70	
Fall Time	t <sub>f</sub>				25	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-2A, V <sub>GS</sub> =0V			-1.2	V

**Notes:**

1. Surface mounted on 1"×1" FR4 board.
2. Pulse Test : Pulse Width≤300μs, Duty Cycle ≤2%.
3. Guaranteed by design, not subject to production testing.

# Typical Characteristics

# CJQ4435

