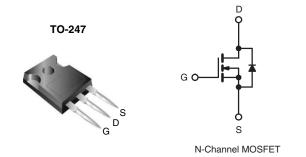


Vishay Siliconix

COMPLIANT

### **Power MOSFET**

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	500		
$R_{DS(on)}\left(\Omega\right)$	V <sub>GS</sub> = 10 V 0.27		
Q <sub>g</sub> (Max.) (nC)	210		
Q <sub>gs</sub> (nC)	29		
Q <sub>gd</sub> (nC)	110		
Configuration	Sin	gle	



### **FEATURES**

- Dynamic dV/dt Rating
- · Repetitive Avalanche Rated
- Isolated Central Mounting Hole
- Fast Switching
- · Ease of Paralleling
- · Simple Drive Requirements
- Lead (Pb)-free Available



Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247 package is preferred for commercial-industrial applications where higher power levels preclude the use of TO-220 devices. The TO-247 is similar but superior to the earlier TO-218 package because its isolated mounting hole. It also provides greater creepage distances between pins to meet the requirements of most safety specifications.

ORDERING INFORMATION	
Package	TO-247
Lead (Pb)-free	IRFP460PbF
Leau (Fb)-liee	SiHFP460-E3
SnPb	IRFP460
SIFD	SiHFP460

ABSOLUTE MAXIMUM RATINGS T	<sub>C</sub> = 25 °C, unless otherw	ise noted		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	500	v
Gate-Source Voltage		$V_{GS}$	± 20	v
Continuous Drain Current	$V_{GS}$ at 10 V $T_C = 25 ^{\circ}C$	I-	20	
Continuous Diain Current	T <sub>C</sub> =100 °C	ID	13	Α
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	80	
Linear Derating Factor			2.2	W/°C
Single Pulse Avalanche Energy <sup>b</sup>		E <sub>AS</sub>	960	mJ
Repetitive Avalanche Currenta		I <sub>AR</sub>	20	Α
Repetitive Avalanche Energy <sup>a</sup>		E <sub>AR</sub>	28	mJ
Maximum Power Dissipation $T_C = 25  ^{\circ}C$		$P_{D}$	280	W
Peak Diode Recovery dV/dtc		dV/dt	3.5	V/ns
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature)	for 10 s	_	300 <sup>d</sup>	
Mounting Torque 6-32 or M3 screw			10	lbf ⋅ in
Mounting Torque	0-32 of M3 Screw		1.1	N · m

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b.  $V_{DD} = 50 \text{ V}$ , starting  $T_J = 25 \,^{\circ}\text{C}$ ,  $L = 4.3 \,\text{mH}$ ,  $R_G = 25 \,\Omega$ ,  $I_{AS} = 20 \,\text{A}$  (see fig. 12).
- c.  $I_{SD} \le 20$  A,  $dI/dt \le 160$  A/ $\mu$ s,  $V_{DD} \le V_{DS}$ ,  $T_J \le 150$  °C.
- d. 1.6 mm from case.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

# IRFP460, SiHFP460

# Vishay Siliconix



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	40	
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.24	-	°C/W
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-	0.45	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static		<u>.</u>					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0	V, I <sub>D</sub> = 250 μA	500	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference t	to 25 °C, I <sub>D</sub> = 1 mA	-	0.63	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V$	<sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>G</sub>	<sub>S</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		$V_{GS} = 0 \text{ V}$ $V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$	-	-	25 250	μΑ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 12 A <sup>b</sup>	-	-	0.27	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 5	60 V, I <sub>D</sub> = 12 A <sup>b</sup>	13	-	-	S
Dynamic						•	
Input Capacitance	C <sub>iss</sub>	V	<sub>GS</sub> = 0 V,	-	4200	-	
Output Capacitance	C <sub>oss</sub>	V	$_{0S} = 25 \text{ V},$	-	870	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0	MHz, see fig. 5	-	350	-	
Total Gate Charge	Qg			-	-	210	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$I_D = 20 \text{ A}, V_{DS} = 400 \text{ V}$ see fig. 6 and 13 <sup>b</sup>	-	-	29	nC
Gate-Drain Charge	Q <sub>gd</sub>	see lig. 0 and 13	-	-	110		
Turn-On Delay Time	t <sub>d(on)</sub>			-	18	-	
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 2 <sup>t</sup>	50 V, I <sub>D</sub> = 20 A ,	-	59	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		$I_D = 13 \Omega$ , see fig. $10^b$	-	110	-	ns
Fall Time	t <sub>f</sub>	1		-	58	-	
Internal Drain Inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") fro	m	-	5.0	-	nH
Internal Source Inductance	L <sub>S</sub>	package and ce	nter of	-	13	-	""
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	Is	MOSFET symbol showing the		ı	-	20	A
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>	integral reverse p - n junction diode		-	-	80	
Body Diode Voltage	$V_{SD}$	T <sub>J</sub> = 25 °C, I <sub>S</sub>	<sub>S</sub> = 20 A, V <sub>GS</sub> = 0 V <sup>b</sup>	-	-	1.8	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	T 25 °C l =	204 dl/dt = 100 4/uch	-	570	860	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$T_J = 25  ^{\circ}\text{C}, I_F = 20\text{A}, dI/dt = 100 A/\mu s^b$		-	5.7	8.6	μС
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )			L <sub>D</sub> )		

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %.



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

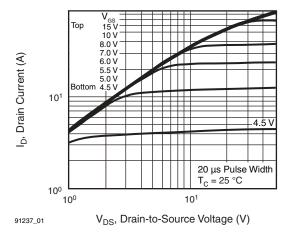


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

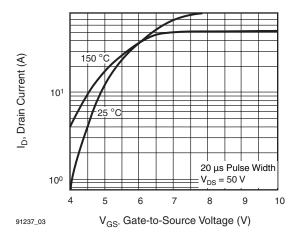


Fig. 3 - Typical Transfer Characteristics

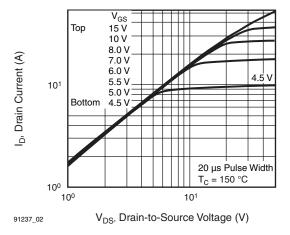


Fig. 2 - Typical Output Characteristics,  $T_C = 150$  °C

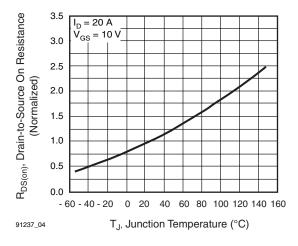


Fig. 4 - Normalized On-Resistance vs. Temperature

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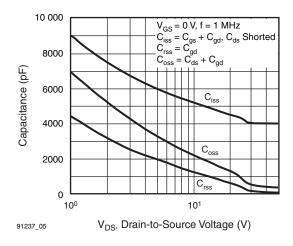


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

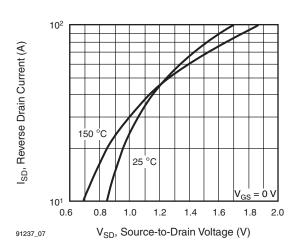


Fig. 7 - Typical Source-Drain Diode Forward Voltage

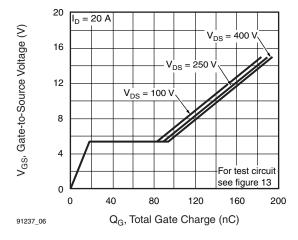


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

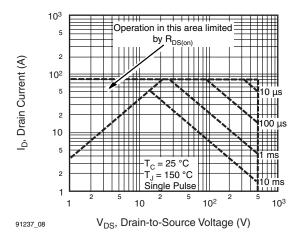


Fig. 8 - Maximum Safe Operating Area





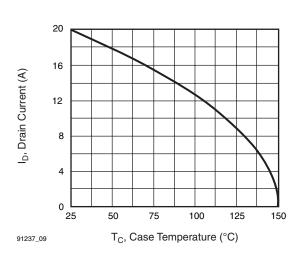


Fig. 9 - Maximum Drain Current vs. Case Temperature

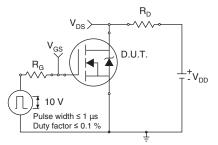


Fig. 10a - Switching Time Test Circuit

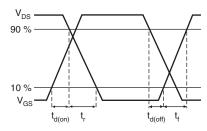


Fig. 10b - Switching Time Waveforms

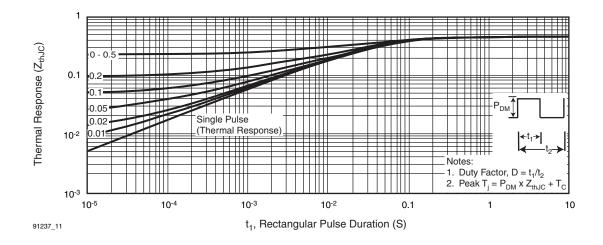


Fig. 11a - Maximum Effective Transient Thermal Impedance, Junction-to-Case

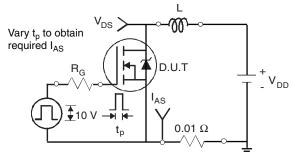


Fig. 12a - Unclamped Inductive Test Circuit

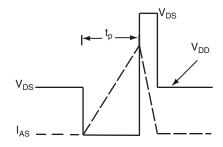


Fig. 12b - Unclamped Inductive Waveforms

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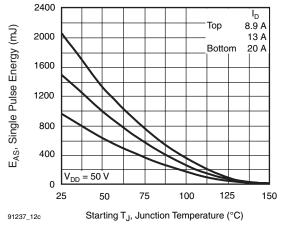


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

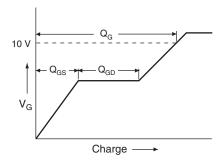


Fig. 13a - Basic Gate Charge Waveform

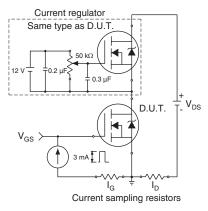
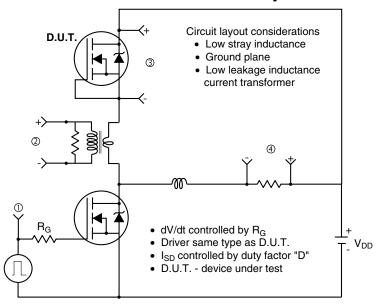


Fig. 13b - Gate Charge Test Circuit



### Peak Diode Recovery dV/dt Test Circuit



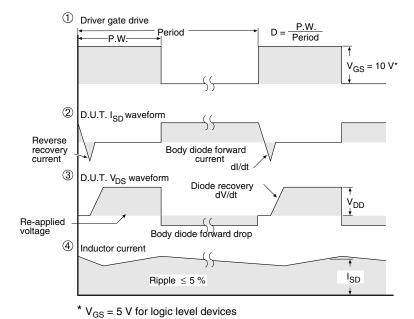


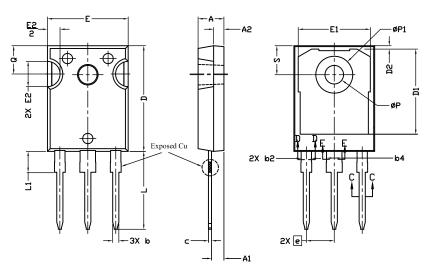
Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?91237.

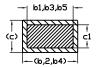


## **TO-247AC (High Voltage)**

### **VERSION 1: FACILITY CODE = 9**







Section C--C, D--D, E--E

	MILLIN		
DIM.	MIN.	MAX.	NOTES
Α	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

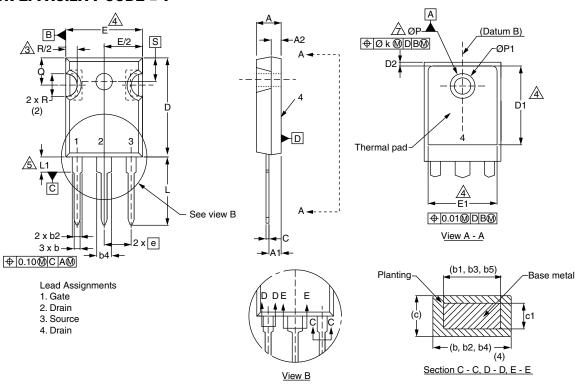
MILLIMETERS		
MIN.	MAX.	NOTES
16.25	16.85	5
0.56	0.76	
15.50	15.87	4
13.46	14.16	5
4.52	5.49	3
5.44	BSC	
14.90	15.40	
3.96	4.16	6
3.56	3.65	7
7.19 ref.		
5.31	5.69	
5.54	5.74	
	MIN. 16.25 0.56 15.50 13.46 4.52 5.44 14.90 3.96 3.56 7.19	MIN. MAX.  16.25 16.85  0.56 0.76  15.50 15.87  13.46 14.16  4.52 5.49  5.44 BSC  14.90 15.40  3.96 4.16  3.56 3.65  7.19 ref.  5.31 5.69

- (1) Package reference: JEDEC® TO247, variation AC
- (2) All dimensions are in mm
- (3) Slot required, notch may be rounded
- (4) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- $^{(7)}$  Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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### **VERSION 2: FACILITY CODE = Y**



	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
Α	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

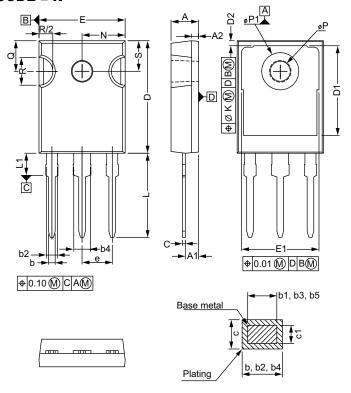
	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
Е	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	0.254	
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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### **VERSION 3: FACILITY CODE = N**



	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	4.65	5.31	
A1	2.21	2.59	
A2	1.17	1.37	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.65	2.39	
b3	1.65	2.34	
b4	2.59	3.43	
b5	2.59	3.38	
С	0.38	0.89	
c1	0.38	0.84	
D	19.71	20.70	
D1	13.08	-	

	MILLIMETERS		
DIM.	MIN.	MAX.	
D2	0.51	1.35	
E	15.29	15.87	
E1	13.46	-	
е	5.46	BSC	
k	0.2	54	
L	14.20	16.10	
L1	3.71	4.29	
N	7.62 BSC		
Р	3.56	3.66	
P1	=	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51	BSC	

ECN: E20-0545-Rev. F, 19-Oct-2020

DWG: 5971

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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Vishay

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