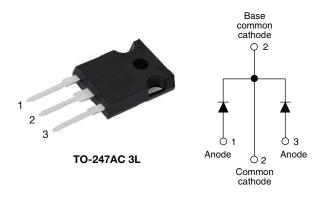
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 15 A



www.vishay.com

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 15 A			
V _R	50 V, 60 V			
V _F at I _F	0.56 V			
I _{RM} typ.	45 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	13 mJ			
Package	TO-247AC 3L			
Circuit configuration	Common cathode			

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



COMPLIANT HALOGEN

FREE

- Guard ring for enhanced ruggedness and long term
- Designed and gualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

reliability

The VS-30CPQ... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNIT							
I _{F(AV)}	Rectangular waveform	30	А				
V _{RRM}		50/60	V				
I _{FSM}	t _p = 5 μs sine	1020	А				
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.56	V				
TJ		-55 to +150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30CPQ050-N3	VS-30CPQ060-N3	UNITS		
Maximum DC reverse voltage	V _R	50	60	V		
Maximum working peak reverse voltage	V _{RWM}	50	80	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS VALUES U			UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	$I_{F(AV)}$ 50 % duty cycle at T _C = 112 °C, rectangular waveform		30		
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	1020	А	
surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V_{RRM} applied	265		
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH 13		13	mJ	
Repetitive avalanche current per leg	I _{AR}			1.50	А	

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SHAY

VS-30CPQ050-N3, VS-30CPQ060-N3

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ELECTRICAL SPECIFICATIONS	
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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS VA			UNITS	
		15 A	T _{.1} = 25 °C	0.60	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	$1_{\rm J} = 25$ C	0.80		
See fig. 1	VFM ()	15 A	− T.ı = 125 °C	0.56		
		30 A	-1j = 125 C	0.70		
		T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.80	m۸	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = naleu V _R	160	mA	
Typical reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	45	mA	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		720	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V _R			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C	
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	2.20		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.10	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24		
Approximate weight				6	g	
Approximate weight	Approximate weight			0.21	oz.	
Mauratian tarawa				6 (5)	kgf ⋅ cm	
Mounting torque ma	maximum		Non-lubricated threads	12 (10)	(lbf · in)	
Marking device				30CPQ050		
			Case style TO-247AC 3L	30CP	Q060	



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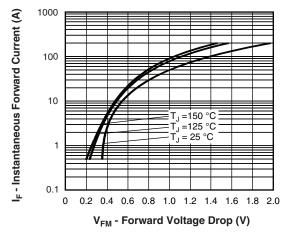


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

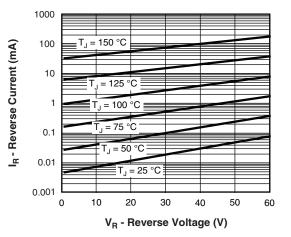


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

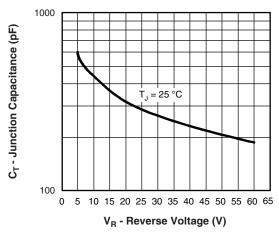
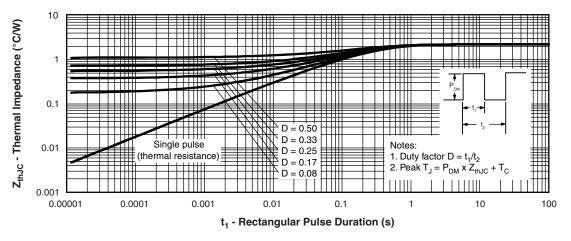


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





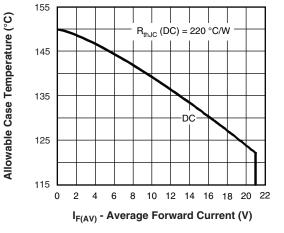
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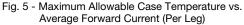
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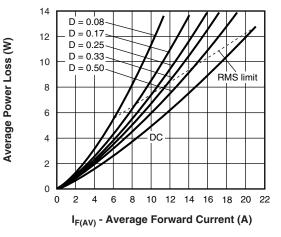


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

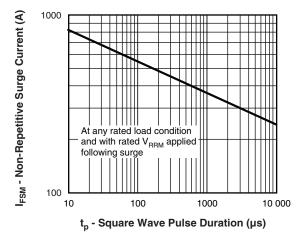


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

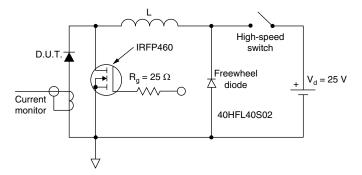


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE

Device code	VS-	30	С	Р	Q	060	-N3	
	1	2	3	4	5	6	7	
	2 -	 Circuit configuration: C = common cathode 						
	5 - 6 - 7 -	Sch Volt Env	ottky "Q age cod ironmer	" series e ıtal digit		(050 = 50 060 = 60 ant, and	

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-30CPQ050-N3	25	500	Antistatic plastic tube			
VS-30CPQ060-N3	25	500	Antistatic plastic tube			

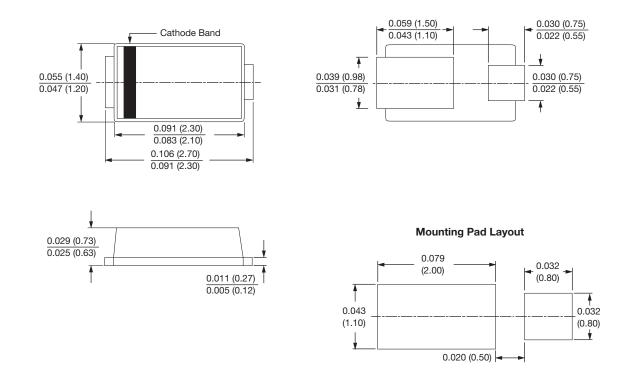
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?96138					
Part marking information	www.vishay.com/doc?95007				



Vishay Semiconductors

MicroSMP (DO-219AD), FRED Pt®

DIMENSIONS in inches (millimeters)





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