



- **FEATURES**
- · Highest CV/cc in Broad Range of Low Profiles
- Conductive Polymer Electrode
- Benign Failure Mode Under Recommended use Conditions
- Lower ESR
- Undertab Terminations Layout:
- High Volumetric Efficiency
- High PCB Assembly Density
- High Capacitance in Smaller Dimensions
- 3x Reflow 260°C Compatible
- 100% Surge Current Tested
- 8 Case Sizes Available



- Consumer Applications (e.g. Mobiles, MP3 etc.)
- Bulk Decoupling of SoC (System on Chip)



## **CASE DIMENSIONS millimeters (inches)**

Polarity Band (Anode+)

L

S A<sub>N</sub>

A<sub>P</sub>

w

 $W_N$ 

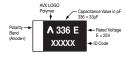
Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H max.	W <sub>p</sub> ±0.10 (0.004)	W <sub>N</sub> ±0.10 (0.004)	A <sub>P</sub> ±0.10 (0.004)	A <sub>N</sub> ±0.10 (0.004)	S Min.
S	1206	3216-12	3.20 (0.126)	1.60 (0.063)	1.20 (0.047)	1.30 (0.051)	1.30 (0.051)	1.15 (0.045)	1.15 (0.045)	0.90 (0.035)
L	1210	3528-10	3.50 (0.138)	2.80 (0.110)	1.00 (0.039)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Т	1210	3528-12	3.50 (0.138)	2.80 (0.110)	1.20 (0.047)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
н	1210	3528-15	3.50 (0.138)	2.80 (0.110)	1.50 (0.059)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
X	2917	7343-15	7.30 (0.287)	4.30 (0.169)	1.50 (0.059)	3.25 (0.128)	3.25 (0.128)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)
z	2917	7343-15	7.30 ±0.30 (0.287 ±0.012)	4.30 ±0.30 (0.169 ±0.012)	1.50 (0.059)	2.40 (0.094)	2.40 (0.094)	1.30 ±0.30 (0.051 ±0.012)	1.30 ±0.30 (0.051 ±0.012)	4.40 (0.173)
4	2924	7361-20	7.30 (0.287)	6.10 (0.240)	2.00 (0.079)	4.75 (0.187)	4.75 (0.187)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)
8	2924	7361-20	7.30 ±0.30 (0.287 ±0.012)	6.10 (0.240)	2.00 (0.079)	4.45 (0.175)	4.45 (0.175)	1.60 ±0.30 (0.063 ±0.012)	1.60 ±0.30 (0.063 ±0.012)	3.80 (0.150)

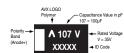
## MARKING

 $W_{P}$ 

## H, L, S, T, X, Z CASE

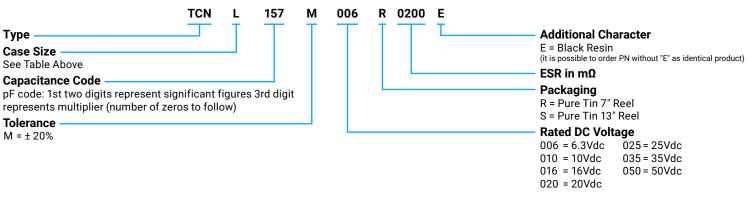
#### 4, 8 CASE





## **HOW TO ORDER**

204





The Important Information/Disclaimer is incorporated in the catalog where these specifications came from or available online at www.avx.com/disclaimer/ by reference and should be reviewed in full before placing any order.



## **TECHNICAL SPECIFICATIONS**

Technical Data:	All technical data relate to an ambient temperature of +25°C									
Capacitance Range:		4.7 μF to 1500 μF								
Capacitance Tolerance:		±20%								
Leakage Current DCL:		0.1CV								
Rated Voltage DC (V <sub>R</sub> )	≤ +85°C:	6.3	10	16	20	25	35	50		
Category Voltage (V <sub>c</sub> )	≤ +105°C:	5	8	13	16	20	28	40		
Surge Voltage (V <sub>s</sub> )	≤ +85°C:	8	13	21	26	33	46	65		
Surge Voltage (V <sub>s</sub> )	≤ +105°C:	6	10	16	20	25	35	50		
Temperature Range:		-55°C to	+105°C							

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.

### CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capacitance			Rated Voltage DC to 85°C / 0.66DC to 105°C											
μF	Code	e 6.3V (J) 10V (A)		16V (C)	20V (D)	25V (E)	35V (V)	50V (T)						
4.7	475						L(300)/T(200)							
10	106						T(150, 200)							
22	226					T(200)								
33	336			L(200)/T(200)				4(200)						
47	476			L(250)/T(150)		X(100)	X(150)/Z(150)							
100	107	L(200)/S(250)				4(100)	4(100)/8(100)							
150	157	L(200)/T(200)		X(100)		4(70)/8(70)								
220	227	H(170)		4(70)	4(100)	4(100)								
330	337			4(70)	4(100)									
470	477	X(50)		4(70,100)										
680	687		4(70)											
1000	108	4(55)												
1500	158	4(55)												

Released ratings, (ESR ratings in mOhms in parentheses)

Engineering Samples - Please Contact AVX

Note: Voltage ratings are minimum values. AVX reserves the right to supply

higher voltage ratings in the same case size, to the same reliability standards.



060921



## **RATINGS & PART NUMBER REFERENCE**

AVX Part No.	Case Size	Capacitance (µF)	Rated Voltage	Maximum Operating Temperature	DCL Max.	DF Max.	ESR Max. @ 100kHz	100kH	Iz RMS Curre	z RMS Current (mA)		MSL
Part NO.	Size	(µr)	(V)	(°C)	(µA)	(%)	(mΩ)	45°C	85°C	105°C	Category	
6.3 Volt @ 85°C												
TCNL107M006#0200E	L	100	6.3	105	60	10	200	700	500	300	3	5
TCNS107M006#0250E	S	100	6.3	105	60	10	250	600	400	300	3	3
TCNL157M006#0200E	L	150	6.3	105	90	10	200	700	500	300	3	5
TCNT157M006#0200E	Т	150	6.3	105	90	10	200	700	500	300	3	4
TCNH227M006#0170E	Н	220	6.3	105	132	10	170	800	600	400	3	4
TCNX477M006#0050E	Х	470	6.3	85	282	10	50	1900	1300	-	5	5
TCN4108M006#0055E	4	1000	6.3	85	600	20	55	1860	1302	-	5	4
TCN4158M006#0055E	4	1500	6.3	85	900	20	55	1860	1302	-	5	4
					10 Volt @ 3	85°C					•	
TCN4687M010#0070E	4	680	10	105	680	20	70	1650	1155	660	3	4
					16 Volt @ 3	85°C	· · · · · · · · · · · · · · · · · · ·			·	· · · · ·	
TCNL336M016#0200E	L	33	16	85	52.8	6	200	700	500	-	5	5
TCNT336M016#0200E	Т	33	16	105	52.8	6	200	700	500	300	3	4
TCNL476M016#0250E	L	47	16	85	75.2	6	250	600	400	-	5	5
TCNT476M016#0150E	Т	47	16	105	75.2	6	150	800	600	400	3	4
TCNX157M016#0100E	Х	150	16	105	240	6	100	1300	900	600	3	4
TCN4227M016#0070E	4	220	16	105	352	20	70	1650	1155	660	2	4
TCN4337M016#0070E	4	330	16	105	528	20	70	1650	1155	660	3	4
TCN4477M016#0070E	4	470	16	105	752	20	70	1650	1155	660	3	4
TCN4477M016#0100E	4	470	16	105	752	20	100	1380	966	552	3	4
			-		20 Volt @ 3	85°C						
TCN4227M020#0100E	4	220	20	85	440	10	100	1380	966	-	5	4
TCN4337M020#0100E	4	330	20	105	660	20	100	1380	966	552	3	4
					25 Volt @	85°C						
TCNT226M025#0200E	Т	22	25	105	55	6	200	700	500	300	3	4
TCNX476M025#0100E	X	47	25	105	117.5	6	100	1300	900	600	2	5
TCN4107M025#0100E	4	100	25	105	250	6	100	1380	966	552	2	4
TCN4157M025#0070E	4	150	25	105	375	6	70	1650	1155	660	2	4
TCN8157M025#0070E	8	150	25	105	375	8	70	1650	1155	660	2	3
TCN4227M025#0100E	4	220	25	105	550	10	100	1380	966	552	3	4
		I		,	35 Volt @	85°C						
TCNL475M035#0300E	L	4.7	35	105	16.5	6	300	600	400	300	2	5
TCNT475M035#0200E	T	4.7	35	105	16.5	10	200	700	500	300	3	4
TCNT106M035#0150E	Т	10	35	105	35	10	150	800	600	400	3	4
TCNT106M035#0200E	T	10	35	105	35	10	200	700	500	300	3	4
TCNX476M035#0150E	X	47	35	105	165	10	150	1100	800	500	3	4
TCNZ476M035#0150E	Z	47	35	105	165	10	150	1100	800	500	3	4
TCN4107M035#0100E	4	100	35	105	350	10	100	1380	966	552	2	3
TCN8107M035#0100E	8	100	35	105	350	10	100	1380	966	552	2	3
				,	50 Volt @ 3							
TCN4336M050#0200E	4	33	50	85	165	12	200	970	679	-	5	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

206

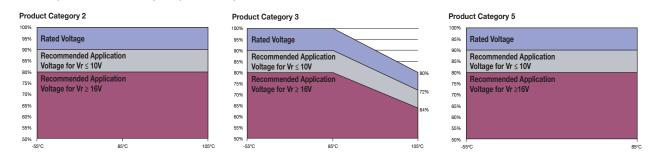
ESR allowed to move up to 1.25 times catalog limit post mounting.

For typical weight and composition see page 276.

NOTE: AVX reserves the right to supply higher voltage ratings in the same case size to the same reliability standards.

## **RECOMMENDED DERATING FACTOR**

#### Voltage and temperature derating as percentage of Vr



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## PRODUCT CATEGORY 2, 3 (TEMPERATURE RANGE -55°C TO +105°C)

TEST		Condition		Characteristics								
	Apply rated volta	age (Ur) at 85°C fo	or 2000 hours	Visual examination	no visib	le damage						
	through a circuit	impedance of $\leq 0$ and / or apply rate	.1Ω/V (all d voltage (Ur)	DCL	1.25 x ir	1.25 x initial limit						
Endurance	(CATEGORY 2) o	or 0.8x rated volta	ge (CATEGORY	ΔC/C	within ±	within ±20% of initial value						
	3) at 105°C for 2 impedance of ≤0	2000 hours throug 0.1Ω/V. Always sta	h a circuit abilize at room	DF	1.5 x ini	tial limit						
	temperature for	1-2 hours before	measuring.	ESR	2 x initia	al limit						
				Visual examination	no visib	le damage						
				DCL (V <sub>R</sub> ≤ 75V)	1.25 x ir	nitial limit						
Storage Life		no voltage applied at room temperat		$DCL(V_R > 75V)$	2 x initia	al limit						
Storage Life	before measurin			ΔC/C	within ±	20% of initi	al value					
		.9.		DF	1.5 x in	itial limit						
				ESR	2 x initia	2 x initial limit						
				Visual examination	no visit	ole damage	9					
		nd 95% relative hu	,	DCL	3 x initi	3 x initial limit						
Humidity		pplied voltage. Sta I humidity for 1-2		ΔC/C	within +	within +30/-20% of initial value						
	measuring.		nours before	DF	1.5 x in	1.5 x initial limit						
	<b>J</b>			ESR	2 x initi	2 x initial limit						
	Step 1	Temperature°C +20	Duration(min) 15	-	+20°C	-55°C	+20°C	+85°C	+105°C	+20°C		
Temperature	2	-55	15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Stability	3	+20 +85	15 15		n/a	+0/-20%	±5%	+20/-0%	+30/-0%	±5%		
-	5	+105	15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*		
	6	+20	15	Visual examination	no visih	no visible damage						
		oltage (Ur) at 105°		DCL		initial limit						
Surge	2, or apply 1.3x 0.	8x rated voltage (U 1000 cycles of dura	Ir) at 105°C for ation 6 min (30	DOL		within +10/-20% of initial value for Vr ≤ 10V						
Voltage	sec charge, 5 min	130 sec discharge)		ΔC/C		within +20/-30% of initial value for Vr $\ge$ 16V						
	/ discharge resist	ance of 1000Ω		DF		1.25 x initial limit						
				Visual examination		no visible damage						
				DCL	initial li	5						
Mechanical	MIL-STD-202 M	ethod 213, Condit	tion C			±5% of initi	al value					
Shock	1112 010 202, 11			DF	initial li							
				ESR								
	1			Visual examination		ole damage	<u>,</u>					
				DCL	initial li	<u> </u>						
Vibration	MIL-STD-202 M	ethod 204, Condit	tion D			±5% of initi	al value					
				DF	initial li							
				ESR	initial li	-						

\*Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.



040320



## PRODUCT CATEGORY 5 (TEMPERATURE RANGE -55°C TO +85°C)

TEST		Condition		Characteristics							
				Visual examination	no visible damage						
	Apply rated voltag	e (Ur) at 85°C for 20	00 hours through	DCL	1.25 x initial limit						
Endurance	a circuit impedance	ce`of ≤0.1Ω/V. Stabili	ize at room	ΔC/C	within ±209	within ±20% of initial value					
	temperature for 1-	2 hours before mea	suring.	DF	1.5 x initial	limit					
				ESR	2 x initial lir	nit					
				Visual examination	no visible d	lamage					
	Store at 85°C, no v	voltage applied, for 2	2000 hours.	DCL	1.25 x initia	al limit					
Storage Life		emperature for 1-2 l		ΔC/C	within ±209	% of initial val	ue				
-	measuring.			DF	1.5 x initia	l limit					
				ESR	2 x initial lir	nit					
				Visual examination	no visible	damage					
	Store at 65°C and	95% relative humidi	tv for 500 hours.	DCL	5 x initial limit						
Humidity		Itage. Stabilize at ro		ΔC/C	within +40/-20% of initial value						
	and humidity for 1	-2 hours before mea	asuring.	DF	1.5 x initial limit						
				ESR	2 x initial limit						
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+85°C	+20°		
Temperature	1 2	+20	15	DCL	IL*	n/a	IL*	10 x IL*	IL*		
Stability	3	+20	15	ΔC/C	n/a	+0/-20%	±5%	+20/-0%	±5%		
	4	+85 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	IL*		
	5	720	15	Visual examination	no visible damage						
		(L) (2500 (		DCL	initial limit						
Surge Voltage	duration 6 min (30 s	ltage (Ur) at 85°C for 1 sec charge, 5 min 30 s discharge resistance	sec discharge)	ΔC/C	within +10/-20% of initial value for Vr $\leq$ 10V within +20/-30% of initial value for Vr $\geq$ 16V						
				DF	1.25 x initial limit						
				Visual examination	no visible damage						
				DCL	initial limit						
Mechanical	MIL-STD-202. Met	hod 213, Condition (	C	ΔC/C	within ±5%	of initial va	lue				
Shock				DF	initial limit						
				ESR	initial limit						
				Visual examination	no visible	damage					
				DCL	initial limit	3					
Vibration	MIL-STD-202. Met	hod 204, Condition [	)		within ±5% of initial value						
				DF							
				ESR	initial limit						

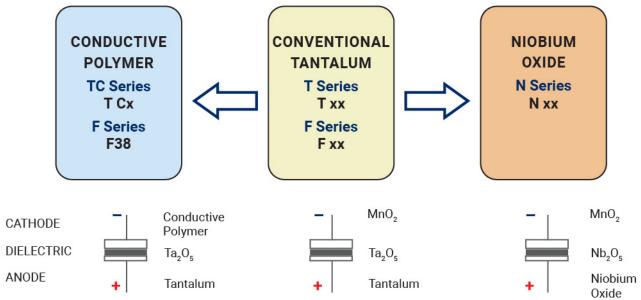
\*Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.





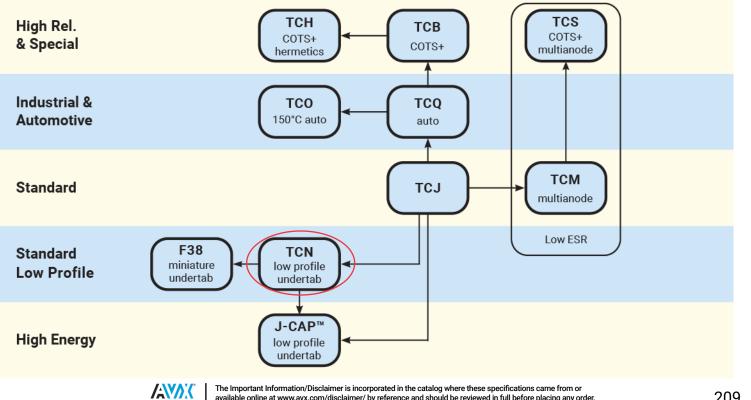
## SOLID ELECTROLYTIC CAPACITOR ROADMAP



## FIVE CAPACITOR CONSTRUCTION STYLES



## **SERIES LINE UP** : Conductive Polymer



# **Mouser Electronics**

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## AVX:

TCNL107M006R0200 TCNL157M006R0200 TCN4158M006R0055 TCNS107M006R0250 TCNK107M006R0250
TCNL336M016R0200 TCNX157M016R0070 TCNX108M006R0200 TCNX476M035R0100 TCNK107M006R0200
TCNT476M016R0200 TCN4157M025R0070 TCNL476M016R0250 TCNN475M025R0500 TCNT336M025R0250
TCNS106M025R0350 TCNK106M025R0350 TCN4337M020R0100 TCN4227M025R0100 TCN4227M020R0100
TCN3107M035R0200 TCNN105M050R1500 TCNX476M035R0150 TCNT476M016R0200A TCNM476M006R0500
TCNO156M010R0500 TCNO106M010R0500 TCNO685M016R0500 TCNO226M004R0500 TCNO226M006R0500
<u>TCNO106M016R0500</u> <u>TCNO156M006R0500</u> <u>TCNT226M025R0200</u> <u>TCNT157M006R0200E</u> <u>TCN4227M025R0100E</u>
<u>TCN4337M016R0070E</u> <u>TCN4107M035R0100</u> <u>TCNT226M025R0200E</u> <u>TCNT336M025R0250E</u> <u>TCNL107M006S0200</u>
TCNX476M035R0150E TCN4477M016R0100E TCN4337M020R0100E TCNX157M016R0100
TCNT106M035R0200E TCN4158M006R0055E TCNT476M016R0150E